



# **GOVERNMENT POLYTECHNIC, KOLHAPUR**

(An Autonomous Institute of Government of Maharashtra)

*Curriculum Document: Semester 1 to 5*

## **CURRICULUM: MPECS 2023**

(NEP 2020 Compliant & Outcome Based Curriculum)

For

## **DIPLOMA IN ELECTRONICS & TELECOMMUNICATION**

**Secretary**

**Chairman**

**Date: 25<sup>th</sup> August 2025**

Programme wise Board of Studies (PBOS)  
Electronics & Telecommunication Programme  
Government Polytechnic, Kolhapur



## CURRICULUM STRUCTURE: TEACHING AND EXAMINATION SCHEME OF SEMSTER I

Programme Code : ET												With Effect From Academic Year : AY 2023-2024												
Duration of Programme : 6 Semesters												Duration : 16 Weeks												
Semester : First												Scheme : H												
Sr. No.	Name of Course	Course Abbreviation	Course Type	Course Code	Level	IKS Hrs per Semester	Learning Scheme					Credits	Paper Duration (Hrs)	Assessment Scheme								Based on Self Learning	Total Marks	
							CL	TL	LL	Self Learning (TW & Assignment)	Notional Learning Hrs / Week			Theory				Based on LL & TL						
														FA-TH	SA-TH	Total		FA-PR		SA-PR		SLA		
																Max	Max	Max	Min	Max	Min	Max		Min
1	ENGINEERING PHYSICS	HPHA	DSC	CCH101	1	4	4	-	2	2	8	4	1.5	30*#	70*#	100	40	25	10	25@	10	25	10	175
2	BASIC MATHEMATICS	HBMT	AEC	CCH105	1	4	4	2	-	2	8	4	3	30	70	100	40	-	-	-	-	25	10	125
3	ENGINEERING GRAPHICS	HGRC	AEC	CCH109	1	2	2	-	2	0	4	2	-					50	20	50@	20			100
4	BASIC ELECTRONICS	HBTX	DSC	ETH101	1	2	3	-	4	1	8	4	3	30	70	100	40	50	20	25@	10	25	10	200
5	ELECTRONICS WORKSHOP PRACTICE	HWET	SEC	ETH102	1	2	2	-	4	0	6	3	-	-	-	-	-	50	20	50@	20	-	-	100
6	FUNDAMENTALS OF ICT	HICT	SEC	CCH202	2	0	1	-	2	1	4	2	-	-	-	-	-	25	10	25@	10	25	10	75
7	YOGA AND MEDITATION	HYAM	VEC	CCH203	2	1	-	-	1	1	2	1	-	-	-	-	-	25	10	-	-	25	10	50
Total:						15	16	2	15	7	40	20	-	90	210	300		250		175		125		825



## CURRICULUM STRUCTURE: TEACHING AND EXAMINATION SCHEME OF SEMSTER II

Programme Code : Electronics & Telecommunication												With Effect From Academic Year : AY 2023-2024												
Duration of Programme : 6 Semesters												Duration : 16 Weeks												
Semester : Second												Scheme : H												
Sr. No.	Name of Course	Course Abbreviation	Course Type	Course Code	Level	IKS Hrs per Semester	Learning Scheme					Credits	Paper Duration (Hrs)	Assessment Scheme								Based on Self Learning	Total Marks	
							CL	TL	LL	Self Learning (TW & Assignment)	Notional Learning Hrs / Week			Theory				Based on LL & TL						
														FA-TH	SA-TH	Total		FA-PR		SA-PR		SLA		
														Max	Max	Max	Min	Max	Min	Max	Min	Max		Min
1	APPLIED MATHEMATICS	HAMT	AEC	CCH301	3	2	4	2	-	-	6	3	3	30	70	100	40	-	-	-	-	-	-	100
2	ENGINEERING CHEMISTRY	HCHA	DSC	CCH103	1	4	4	-	2	2	8	4	1.5	30*#	70*#	100	40	25	10	25@	10	25	10	175
3	COMMUNICATION SKILLS	HCMS	AEC	CCH201	2	0	4	-	2	2	8	4	3	30	70	100	40	25	10	-	-	25	10	150
4	APPLIED ELECTRONICS	HATX	DSC	ETH103	1	0	4	-	2	-	6	3	3	30	70	100	40	25	10	25@	10	-	-	150
5	ELECTRICAL ENGINEERING	HEEG	DSC	ETH104	1	0	3	-	2	1	6	3	3	30	70	100	40	25	10	25@	10	25	10	175
6	C PROGRAMMING	HCPR	SEC	ETH105	1	-	2		2	-	4	2	-	-	-	-	-	25	10	25@	10	-	-	50
7	SOCIAL & LIFE SKILLS	HSLS	VEC	CCH204	2	-	-	-	-	2	2	1	-	-	-	-	-	-	-	-	-	50	20	50
Total:						6	21	2	10	7	40	20		150	350	500		125		100		125		850



## CURRICULUM STRUCTURE: TEACHING AND EXAMINATION SCHEME OF SEMSTER III

Programme Code : Electronics & Telecommunication												With Effect From Academic Year : AY 2024-2025												
Duration of Programme : 6 Semesters												Duration : 16 Weeks												
Semester : Third												Scheme : H												
Sr. No.	Name of Course	Course Abbrevia- tion	Course Type	Course Code	Level	IKS Hrs per Semester	Learning Scheme					Credits	Paper Duration (Hrs)	Assessment Scheme								Based on Self Learning	Total Marks	
							CL	TL	LL	Self Learning (TW & Assignment)	Notional Learning Hrs / Week			Theory				Based on LL & TL						
														FA-TH	SA-TH	Total		FA-PR		SA-PR		SLA		
														Max	Max	Max	Min	Max	Min	Max	Min	Max		Min
1	CIRCUITS & NETWORKS	HCKN	DSC	ETH301	3	0	3	-	2	1	6	3	3	30	70	100	40	25	10	25@	10	25	10	175
2	LINEAR INTEGRATED CIRCUITS	HLIC	DSC	ETH302	3	0	3	-	4	1	8	4	3	30	70	100	40	25	10	25#	10	25	10	175
3	DIGITAL TECHNIQUES & APPLICATIONS	HDTA	DSC	ETH303	3	0	3	-	4	1	8	4	1.5	30*#	70*#	100	40	25	10	25#	10	25	10	175
4	ANALOG COMMUNICATION	HACM	DSC	ETH304	3	0	3	-	2	1	6	3	3	30	70	100	40	25	10	25@	10	25	10	175
5	ELECTRONICS MEASUREMENTS & INSTRUMENTATION	HEMI	DSC	ETH305	3	0	3	-	2	1	6	3	-	-	-	-	-	50	20	25@	10	25	10	100
6	BASIC PYTHON PROGRAMMING	HBPP	AEC	ETH306	3	0	2	-	2	-	4	2		-	-	-	-	25	10	25@	10	-	-	50
7	ESSENCE OF INDIAN CONSTITUTION	HEIC	VEC	CCH205	3	0	1	-	-	1	2	1	-	-	-	-	-	-	-	-	-	50	20	50
Total:						0	18	-	16	6	40	20	-	120	280	400	-	175	-	150	-	175	-	900





## CURRICULUM STRUCTURE: TEACHING AND EXAMINATION SCHEME OF SEMSTER IV

Programme Code : Electronics & Telecommunication												With Effect From Academic Year : AY 2024-2025												
Duration of Programme : 6 Semesters												Duration : 16 Weeks												
Semester : Four												Scheme : H												
Sr. No.	Name of Course	Course Abbreviation	Course Type	Course Code	Level	IKS Hrs per Semester	Learning Scheme					Credits	Paper Duration (Hrs)	Assessment Scheme								Based on Self Learning		Total Marks
							CL	TL	LL	Self Learning (TW & Assignment)	Notional Learning Hrs / Week			Theory				Based on LL & TL						
														FA-TH	SA-TH	Total		FA-PR		SA-PR		SLA		
														Max	Max	Max	Min	Max	Min	Max	Min	Max	Min	
1	MICROCONTROLLERS	HMCS	DSC	ETH307	3	0	4	-	4	-	8	4	3	30	70	100	40	25	10	25#	10	-	-	150
2	DIGITAL COMMUNICATION	HDCM	DSC	ETH308	3	0	3	-	2	1	6	3	3	30	70	100	40	25	10	25#	10	25	10	175
3	POWER ELECTRONICS	HPTX	DSC	ETH309	3	0	4	-	2	-	6	3	3	30	70	100	40	25	10	-	-	-	-	125
4	SIMULATION SOFTWARE	HSMS	DSC	ETH401	4	0	-	-	4	-	4	2	-	-	-	-	-	25	10	25@	10	-	-	50
5	SIGNALS & SYSTEMS	HSYS	DSC	ETH402	4	0	3	-	2	1	6	3	3	30	70	100	40	25	10	-	-	25	10	150
6	CONSUMER ELELCTRONICS	HCEX	DSC	ETH501	5	0	3	-	2	1	6	3	-	-	-	-	-	25	10	-	-	25	10	50
7	ENVIRONMENTAL EDUCATION AND SUSTAINABILITY	HEES	VEC	CCH206	2	2	2	-	-	2	4	2	1.5	30	70*#	100	40	-	-	-	-	25	10	125
Total:						2	19		16	5	40	20	-	150	350	500	-	150	-	75	-	100	-	825



## CURRICULUM STRUCTURE: TEACHING AND EXAMINATION SCHEME OF SEMSTER V

Programme Code : Electronics & Telecommunication												With Effect From Academic Year : AY 2025-2026													
Duration of Programme : 6 Semesters												Duration : 16 Weeks													
Semester : FIFTH												Scheme : H													
Sr. No.	Name of Course	Course Abbrevia- tion	Course Type	Course Code	Level	IKS Hrs per Semester	Learning Scheme					Credits	Paper Duration (Hrs)	Assessment Scheme								Based on Self Learning		Total Marks	
							CL	TL	LL	Self Learning (TW & Assignment)	Notional Learning Hrs / Week			Theory				Based on LL & TL							
														FA-TH	SA-TH	Total		FA-PR		SA-PR		SLA			
																Max	Max	Max	Min	Max	Min	Max	Min		Max
1	ENTREPRENEURSHIP & STARTUP	HESU	AEC	CCH501	5	1	2	-	-	1	3	1	-	-	-	-	-	-	-	-	-	50	20	50	
2	INTERNSHIP (16 WEEKS)	HINT	INP	CCH505	5	0	-	-	-	-	-	10	-	-	-	-	-	100	40	100#	40	-	-	200	
3	DATA COMMUNICATION & NETWORKING	HDCN	DSC	ETH404	4	0	7	-	4	-	11	2	3	30	70	100	40	25	10	25#	10	-	-	150	
4	EMBEDDED SYSTEM	HEMS	DSC	ETH405	4	0	7	-	4	-	11	2	3	30	70	100	40	25	10	25#	10	-	-	150	
5	ELECTIVE 1		GE		5	0	5	-	-	1	6	2	1	15*#	35*#	50	20	-	-	-	-	50	20	100	
6	INTERNET OF THINGS	HIOT	DSC	ETH502	5	0	3	-	4	-	7	1	-	-	-	-	-	50	20	50@	20	-	-	100	
7	PROJECT	HPRJ	INP	ETH503	5	-	-	-	4	2	6	2	-	-	-	-	-	-	-	50#	20	50	20	100	
Total:						1	24	-	16	4	45	20		75	175	250	-	200		250	-	150	-	850	

- IOM, ESU & Project will be conducted online for 4 weeks (Prior to the Offline schedule), followed by 6 weeks of offline sessions.



## ELECTIVES & OPTIONS

Programme Code : Electronics & Telecommunication												With Effect From Academic Year : AY 2025-2026												
Duration of Programme : 6 Semesters												Duration : 16 Weeks												
Semester : FIFTH & SIXTH												Scheme : H												
Sr. No.	Name of Course	Course Abbrevia- tion	Course Type	Course Code	Level	IKS Hrs per Semester	Learning Scheme					Credits	Paper Duration (Hrs)	Assessment Scheme								Based on Self Learning	Total Marks	
							CL	TL	LL	Self Learning (TW & Assignment)	Notional Learning Hrs / Week			Theory				Based on LL & TL						
														FA-TH	SA-TH	Total		FA-PR		SA-PR		SLA		
														Max	Max	Max	Min	Max	Min	Max	Min	Max		Min
	ELELCTIVE 1																							
1	INDUSTRIAL ORGANIZATION AND MANAGEMENT	HIOM	AEC	CCH502	5	0	5	-	-	1	6	2	1	15*#	35*#	50@\$	20	-	-	-	-	50	20	100
2	MARKETING MANAGEMENT	HMKM	AEC	CCH503	5	0	5	-	-	1	6	2	1	15*#	35*#	50@\$	20	-	-	-	-	50	20	100
3	PROJECT MANAGEMENT	HPRM	AEC	CCH504	5	0	5	-	-	1	6	2	1	15*#	35*#	50@\$	20	-	-	-	-	50	20	100



**ABBREVIATIONS :**

CL- CLASSROOM LEARNING , TL- TUTORIAL LEARNING, LL-LABORATORY LEARNING, FA - FORMATIVE ASSESSMENT, SA -SUMMATIVE ASSESSMENT, IKS - INDIAN KNOWLEDGE SYSTEM

LEGENDS : @ INTERNAL ASSESSMENT, # EXTERNAL ASSESSMENT, \*# ON LINE EXAMINATION , @\$ INTERNAL ONLINE EXAMINATION

**NOTE :**

1. FA-TH REPRESENTS AVERAGE OF TWO CLASS TESTS OF 30 MARKS EACH CONDUCTED DURING THE SEMESTER.
2. IF CANDIDATE IS NOT SECURING MINIMUM PASSING MARKS IN FA-PR OF ANY COURSE THEN THE CANDIDATE SHALL BE DECLARED AS "DETAINED" IN THAT SEMESTER.
3. IF CANDIDATE IS NOT SECURING MINIMUM PASSING MARKS IN SLA OF ANY COURSE THEN THE CANDIDATE SHALL BE DECLARED AS FAIL AND WILL HAVE TO REPEAT AND RESUBMIT SLA WORK.
4. NOTIONAL LEARNING HOURS FOR THE SEMESTER ARE (CL+LL+TL+SL)HRS.\* 15 WEEKS
5. 1 CREDIT IS EQUIVALENT TO 30 NOTIONAL HRS.
  - SELF LEARNING HOURS SHALL NOT BE REFLECTED IN THE TIME TABLE.

**COURSE CATEGORIES :**

- A. DISCIPLINE SPECIFIC COURSE CORE (DSC)
- B. DISCIPLINE SPECIFIC ELECTIVE (DSE)
- C. VALUE EDUCATION COURSE (VEC),
- D. INTERN./APPRENTI./PROJECT./COMMUNITY (INP)
- E. ABILITY ENHANCEMENT COURSE (AEC)
- F. SKILL ENHANCEMENT COURSE (SEC)
- G. GENERIC ELECTIVE (GE)





# SEMESTER I COURSES



**COURSE ID :**

**COURSE NAME : ENGINEERING PHYSICS (EE/ET/IT)**

**COURSE CODE : CCH101**

**COURSE ABBREVIATION : HPHA**

### A. LEARNING SCHEME:

Scheme component		Hours	Credits
Actual Contact Hours / week	Classroom Learning	04	4
	Tutorial Learning	-	
	Laboratory Learning	02	
	SLH-Self Learning	02	
	NLH-Notional Learning	08	

### B. ASSESSMENT SCHEME :-

PAPER DURAT ION IN HRS	THEORY				BASED ON LL & TL				BASED ON SLA		TOTAL
					Practical						175
	FA-TH	SA-TH	TOTAL		FA -PR		SA-PR		MAX	MIN	
MAX	MAX	MAX	MIN	MAX	MIN	MAX	MIN	MAX	MIN		
	30*#	70*#	100	40	25	10	25@	10	25	10	

**(Total IKS Hrs for Sem.: 04 Hrs)**

**C: ABBREVIATIONS :-** CL-Classroom Learning, TL-Tutorial Learning, LL-Laboratory Learning, SLH-Self Learning Hours, NLH-Notional Learning Hours, FA - Formative Assessment, SA - Summative assessment, IKS - Indian Knowledge System, SLA -Self Learning Assessment

**Legends:**@Internal Assessment, #External Assessment,\*# Online Examination, @\$Internal Online Examination

1. FA-TH represents average of two class tests of 30 marks each conducted during the semester.
2. If candidate is not securing minimum passing marks in FA-PR of any course then the candidate shall be declared as "Detained" in that semester.
3. If candidate is not securing minimum passing marks in SLA of any course then the candidate shall be declared as fail and will have to repeat and resubmit SLA work.
4. Notional Learning hours for the semester are (CL+LL+TL+SL)hrs.\*15Weeks
5. 1(one) credit is equivalent to 30 Notional hrs.
6. \*Self learning hours shall not be reflected in the Time Table.

\*Self learning includes microproject / assignment / other activities. (Provide list of all assignments here in tabular format At least 6 to 8 assignments to be given)

## **D. i) RATIONALE :-**

Physics is the foundation of engineering and technology. The development of all engineering areas requires good understanding of fundamental principles in physics. Studying physics develops scientific methodology and technical aptitude in the students. Applications of principles of physics in engineering fields create interest and motivate the students.

## **ii) INDUSTRY/ EMPLOYER EXPECTED OUTCOME**

Apply principles of Physics to solve engineering problems as follows:

**Cognitive** : i) Understanding and applying principles and laws of Physics to simple practical problems/ situations. ii) Observing iii) Classifying iv) Interpreting

**Psychomotor** : Handling of instruments, apparatus and tools

**Affective** : Skill of i) working in team ii) curiosity, interest and self-confidence

## **E. COURSE LEVEL LEARNING OUTCOMES (COS)**

**CCH101-1** Estimate errors in measurement of physical quantities.

**CCH101-2** Express importance of semiconductors and nanotechnology.

**CCH101-3** Select proper material in engineering industry by analysis of its physical properties.

**CCH101-4** Apply principles of electricity and magnetism to solve engineering problems.

**CCH101-5** Apply principles of optics to solve engineering problems.

**CCH101-6** Apply principles of fiber optics for related engineering applications.

**Course outcomes and programme outcomes/ programme specific outcomes  
(co-po/pso) matrix**

[ Note : Correlation levels : 1: Slight (Low), 2: Moderate (Medium), 3: Substantial (High), “0”

Cos	Programme Outcomes POs and PSOs								
	PO 1 Basic and Discipli ne specific knowle dge	PO 2 Proble m Analysi s	PO 3 Design / Develo pment of solutio ns	PO 4 Enginee ring Tools, Experim entation and Testing	PO 5 Engineeri ng Practices for society, sustainab ility and Environ ment	PO 6 Project Manag ement	PO 7 Life- long Learni ng	PSO1	PSO2
CCH101-1 Estimate errors in measurement of physical quantities.	3	1	-	1	1	1	1		
CCH101-2 Express importance of semiconductors and nanotechnology	3	-	-	-	1	1	1		
CCH101-3 Select proper material in engineering industry by analysis of its physical properties	3	1	-	1	1	1	1		
CCH101-4 Apply principles of electricity and magnetism to solve engineering problems	3	1	-	1	1	1	1		
CCH101-5 Apply principles of optics to solve engineering problems.	3	1	-	-	1	1	1		
CCH101-6 Apply principles of fiber optics for related engineering applications	3	-	-	-	1	1	1		

## F. CONTENT :

### D) Practical exercises

The following practical exercises shall be conducted in the *Laboratory for Physics developed* by the Institute in practical sessions of batches of about 20- 22 students:

Sr. no	Laboratory experiences	CO
1	To measure internal and external dimensions of hollow cylinder by using Vernier Caliper	CCH101-1
2	To measure the diameter of bob and thickness of plate by using Vernier Caliper	CCH101-1
3	To measure the diameter of bob and thickness of plate by using Micrometer screw gauge	CCH101-1
4	To determine forbidden energy band gap in semiconductors	CCH101-2
5	To determine the viscosity of liquid by Stokes method.	CCH101-3
6	To determine the buoyancy force on a solid immersed in a liquid	CCH101-3
7	To measure unknown resistance of wire by Ohm's law	CCH101-4
8	To verify series law of resistances	CCH101-4
9	To verify parallel law of resistances	CCH101-4
10	To draw magnetic lines of force for given magnet by using magnetic compass	CCH101-4
11	To verify Snell's law using glass slab	CCH101-5
12	To study variation of $\delta$ with $i$ for a prism by pin method	CCH101-5
13	To study Total Internal Reflection using glass slab	CCH101-6
14	To be added by the subject teacher as per requirement	

## II) Theory

### Section I

Sr. no.	Topics/Subtopics	Learning (Hours)	Classroom learning evaluation Marks
CO: CCH101-1 Estimate errors in measurement in Physical quantities.			
1	<b>UNITS AND MEASUREMENT</b> 1.1 Unit, Physical Quantities : Fundamental and Derived Quantities and their units 1.2 Systems of units : CGS, MKS, FPS and SI 1.3 Errors , Types of errors : Instrumental, Systematic and Random error, Estimation of errors : Absolute, Relative and percentage errors 1.4 Significant figures 1.5 Ancient Astronomical Instruments : Chakra, Dhanuryantra, Yasti and Phalaka yantra (IKS learning) 1.6 <b>Simple Numerical problems</b>	<b>10</b>	<b>12</b>
CO: CCH101-2 Express the importance of Semiconductors and nanotechnology.			
2	<b>INTRODUCTION TO SEMICONDUCTORS AND NANOTECHNOLOGY</b> <b>2.1 SEMICONDUCTORS</b> 2.1.1 Conductors, insulators and semiconductors 2.1.2 Energy bands 2.1.3 Intrinsic and extrinsic semiconductors 2.1.4 Minority and majority charge carriers 2.1.5 P and N type semiconductors 2.1.6 Properties of semiconductors 2.1.7 Applications of semiconductors <b>No numericals on above topic</b> <b>2.2 Nanotechnology</b> 2.2.1 Definition of nanoscale, nanometer, nanoparticle 2.2.2 Definition and examples of nanostructured materials 2.2.3 Applications of nanotechnology in electronics, automobile, textile, space, medicine, cosmetics and environment <b>No numericals on above topic</b>	<b>08</b>  (06)         (02)	<b>08</b>  (06)         (02)
CO: CCH101-3 Select proper material in engineering industry by analysis of its physical properties.			
3	<b>PROPERTIES OF MATTER</b> <b>3.1 ELASTICITY</b> 3.1.1 Definitions of elasticity, plasticity, rigidity, deforming force, restoring force	<b>12</b>  (06)	<b>14</b>  (10)

Sr. no.	Topics/Subtopics	Learning (Hours)	Classroom learning evaluation Marks
	3.1.2 Stress, Strain and their types 3.1.3 Elastic Limit, Statement of Hooke's law 3.1.4 Modulus of elasticity and its types, Relation between Y, K and $\eta$ (No derivation) 3.1.5 Ultimate stress, breaking stress, Working stress, Factor of safety 3.1.6 Applications of elasticity 3.1.7 <b>Simple Numerical problems</b>  <b>3.2 VISCOSITY</b> 3.2.1 Definition and meaning of viscosity, velocity gradient 3.2.2 Newton's law of viscosity, Coefficient of viscosity 3.2.3 Stokes law 3.2.4 Derivation of expression for coefficient of viscosity of liquid by Stokes method 3.2.5 Effect of temperature and adulteration on viscosity of liquids 3.2.6 Applications of viscosity <b>No numericals on above topic</b>	(06)	(04)

## Section –II

Sr. no.	Topics/Subtopics	Learning (Hours)	Classroom learning evaluation Marks
CO: CCH101-4 Apply principles of electricity and magnetism to solve engineering problems			
4	<b>ELECTRICITY AND MAGNETISM</b>  <b>4.1 ELECTRICITY</b> 4.1.1 Concept of charge, Coulomb's inverse square law, 4.1.2 Electric field, Electric field intensity 4.1.3 Electric potential and potential difference 4.1.4 Electric current, Resistance, Ohm's law 4.1.5 Specific resistance 4.1.6 Resistances in series and parallel 4.1.7 <b>Simple Numerical problems</b>  <b>4.2 MAGNETISM</b> 4.2.1 Magnetic field and magnetic field intensity and its units 4.2.2 Magnetic lines of force, magnetic flux <b>No numericals on above topic</b>	<b>10</b>  (06)         (04)	<b>12</b>  (08)         (04)



CO: CCH101-5 Apply principles of optics to solve engineering problems			
5	<b>OPTICS</b>  <b>5.1 PROPERTIES OF LIGHT</b> 5.1.1 Refraction of light 5.1.2 Laws of Refraction of Light, Snell's law 5.1.3 Refraction through glass prism 5.1.4 Dispersion & Dispersive Power (in terms of angles of deviation only) 5.1.5 <b>Simple Numerical problems</b>  <b>5.2 LASER</b> 5.2.1 Introduction of LASER 5.2.2 Properties of laser 5.2.3 Spontaneous and stimulated emission 5.2.4 Population inversion and optical pumping 5.2.5 Applications of LASER <b>No numericals on above topic</b>  <b>5.3 X-RAYS</b> 5.3.1 Nature and properties of x-rays. 5.3.2 Production of x-rays by Coolidge tube 5.3.3 Applications of x-rays <b>No numericals on above topic</b>	<b>14</b>  (06)        (04)        (04)	<b>18</b>  (08)        (06)        (04)
CO: CCH101-6 Apply principles of fiber optics for related engineering applications			
6	<b>FIBER OPTICS</b> 6.1 Optical communication link 6.2 Principle of optical fiber (TIR) 6.3 Structure of optical fiber 6.4 Propagation of light in optical fiber 6.5 Advantages of optical fibers over conventional metal conductors 6.6 Applications of optical fibers <b>No numericals on above topic</b>	<b>06</b>	<b>06</b>

**\*\* No questions will be asked on IKS learning subtopics in any question papers.**

## G : List of Microprojects/ Assignments/ Other Activities under SLA

**\*\*One microproject/ assignment/ given activity is to be completed during the semester.**

Sr. No.	List of Microprojects (any one of the following under SLA)	Hrs Allotted
1	Prepare chart showing multipliers required for converting units of physical quantities.	02
2	Prepare prototype vernier caliper of desired least count using card sheet.	02
3	Collect information about ancient astronomical instruments like Chakra, Dhanuryantra, Yasti and Phalaka yantra.	02
4	Collect different materials such as metal, plastic, glass etc and prepare models to show their electrical conductivity.	02
5	Collect different sizes of same material (eg. sugar, salt etc) and list the physical/elerical/optical/chemical/mechanical characteristics for each of them.	02
6	Prepare chart showing the three types of modulus of elasticity developed in a material.	02
7	Prepare working model to differentiate liquids on the basis of viscosity.	02
8	Prepare chart/models to demonstrate magnetic lines of force of different types of magnets.	02
9	Prepare chart/models for series and parallel combination of resistances of different values.	02
10	Prepare a model to demonstrate the variation of angle of refraction with respect to angle of incidence.	02
11	Use keychain laser to differentiate laser with ordinary light.	02
12	Prepare a presentation for application of x-rays in different fields.	02
13	Prepare a model to demonstrate total internal reflection. (For EE/ET/IT students)	02
OR		
Sr.No	List of Assignment (any one of the following under SLA)	Hrs Allotted
1	Write fundamental and derived Physical quantities with their SI units	02
2	Enlist the rules used to decide significant figures in measurements.	02
3	Write points to differentiate conductors, semiconductors and insulators on the basis of energy band diagram.	02
4	List applications of semiconductors in Civil, Mechanical, Electrical, Information Technology, Electronics and Telecommunication, Metallurgical Engineering etc.	02
5	Write down the applications of nanotechnology in the field of electronics, cosmetics, textile, environment, medical, space and defense, automobiles.	02
6	Write applications of elasticity.	02
7	Explain free fall of a sphere in a liquid column.	02

## H : Specification table for setting question paper for semester end theory examination

Section / Topic no.	Name of topic	Distribution of marks (level wise)			Total marks	CO
		Remember	Understand	Apply		
I / 1	Units and measurements	2	4	6	12	CCH101-1
I / 2	Introduction to Semiconductors and Nanotechnology	2	2	4	08	CCH101-2
I / 3	Properties of matter (Elasticity and Viscosity)	4	2	8	14	CCH101-3
II / 4	Electricity and Magnetism	2	4	6	12	CCH101-4
II / 5	Optics (Properties of light, Laser & X-rays)	6	6	6	18	CCH101-5
II / 6	Fiber Optics	2	2	2	06	CCH101-6
Total Marks					70	

## I :-Assessment Criteria

### i) Formative Assessment of Practical:-

Every practical assignment shall be assessed for 25 marks as per following criteria:

Domain	Particulars	Marks out of 25
Cognitive	Understanding	05
	Presentation ( Observations, calculations & Result table)	05
Psychomotor	Operating Skills	05
	Drawing skills (Neat & complete circuit Diagram / schematic Diagram)	05
Affective	Discipline and punctuality	05
TOTAL		25

### ii) Summative Assessment of Practical :

Every practical assignment shall be assessed for 25 marks as per following criteria:

Sr.no	Criteria	Marks allotted
1	Attendance at regular practical	05
2	Preparedness for practical	05
3	Neat & complete Diagram / observation table	05
4	Observations / Calculations / Result / Graph	05
5	Safety / use of proper tools	05
TOTAL		25

### iii) Assessment of SLA :-

Every Self-learning assignment shall be assessed for 25 marks as per following criteria:

Sr.no	Criteria	Marks allotted
1	Attendance	05
2	Preparedness and workmanship	05
3	Presentation (neat figures/ diagrams/ tables/ graphs etc.)	05
4	Conclusion / Inference	05
5	Oral Based on microproject/ assignment/ activity	05
<b>TOTAL</b>		<b>25</b>

**J) Instructional Methods:**

1. Lectures cum Discussions
2. Regular Home Assignments
3. Laboratory work
4. Use of projector and soft material for demonstration

**K) Teaching and Learning resources:**

1. Chalk board
2. Video clips
3. Slides
4. Item Bank
5. Charts

**L) Reference Books:**

S.N.	Name of Book	Author	Publication
1	Text book of Physics for class XI & XII (Part-I, II)	Narlikar	N.C.E.R.T Delhi
2	Engineering Physics	P.V.Naik.	Pearson Edu. Pvt. Ltd, New Delhi.
3	Concepts in Physics, Vol. I & II.	Narkhede, Pawar, Sutar	Bharti Bhawan Ltd, New Delhi.
4	Principles of Physics.	Walker, Halliday, Resnik	Wiley Publication. , New Delhi.
5	Engineering Physics	B.L. Theraja	S. Chand Publishers – New Delhi
6	Concept of modern physics	Beiser	Tata Mc-Graw Hill
7	Physics for Technicians	E. Zebro Wski	Tata Mc-Graw Hill
8	Engineering Physics	V. Rajendran	Tata McGraw-Hill Publications
9	The Archaic and The Exotic : Studies in the history of Indian astronomical instruments	Steeramula Rajeswara Sarma	Manohar Book Services
10	The Surya Siddhanta	Aryabhatta	Baptist Mission Press, Calcutta

**M) Learning Website & Software**

- 1) <http://www.physicsclassroom.com>
- 2) <http://scienceworld.wolfram.com/physics/>

- 3) <http://physics.about.com/>
- 4) <http://nptel.ac.in/course.php?disciplineId=115>
- 5) <http://nptel.ac.in/course.php?disciplineId=104>
- 6) [www.fearofphysics.com](http://www.fearofphysics.com)
- 7) [www.science.howstuffworks.com](http://www.science.howstuffworks.com)
- 8) [www.iksindia.org](http://www.iksindia.org)

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**COURSE ID:**

**Course Name : BASIC MATHEMATICS**  
**Course Code : CCH105**  
**Course Abbreviation : HBMT**  
**Course Type : AEC**  
**Course Level : 1**

**A. LEARNING & ASSESSMENT SCHEME:**

IKS Hrs per Semester	Learning Scheme					Credits	Paper Duration (Hrs)	Assessment Scheme								Based on Self Learning		Total Marks
	C L	T L	L L	Self Learning (TW & Assignment) Notional	Learning Hrs / Week			Theory				Based on LL & TL						
								FA TH	SA TH	Total		FA-PR		SA-PR		SLA		
										Ma x	Ma x	Ma x	Mi n	Ma x	Mi n	Ma x	Mi n	
4	4	2	-	2	8	4	3	30	70	100	40	25	10	-	-	25	10	150

CO.S

CCH105-1 : To Apply concepts of algebra to solve engineering related problems

CCH105-2 : To Use techniques and methods of statistics to compare multiple sets of data

CCH105-3 : Solve area specific engineering problems under given conditions of straight lines

CCH105-4:- To memorize trigonometric formulae and solve problems based on them.

CCH105-5:- To solve the problems of maxima, minima, radius of curvature and geometrical applications.

## Section I

Unit No.	Topics / Sub-topics	Lectures (Hours)	SA-TH (Marks)
Unit 1 Algebra	<b>1.1 LOGARITHMS</b> 1.1.1 Concept and laws of logarithm 1.1.2 Simple examples based on laws of Logarithms <b>1.2 MATRICES</b> 1.2.1 Definition of a matrix, Types of matrices, Algebra of matrices, Equality of two matrices, Transpose of a matrix, 1.2.3 Adjoint and Inverse of a matrix 1.2.4 Solution of simultaneous equations having 3 unknowns using Matrix inversion method <b>1.3 PARTIAL FRACTIONS</b> 1.3.1 Definition of rational, proper and improper fractions 1.3.2 Various cases of Partial fractions and Examples 1.4 Algebra of Indian Knowledge System: Solution of simultaneous equations using Vedic Mathematics	12	14
Unit 2 Statistics	<b>MEASURES OF DISPERSION</b> 2.1 Range, coefficient of Range for Discrete & Grouped Data 2.2 Mean deviation and Standard Deviation about mean for Discrete & Grouped Data (except Assumed mean method and Step deviation method) 2.3 Variance and coefficient of Variance 2.4 Comparison of 2 sets of observations	10	12
Unit 3 Coordinate Geometry	<b>THE STRAIGHT LINE</b> 3.1 Slope, intercepts & various methods of finding slope 3.2 Conditions for two straight lines to be parallel and Perpendicular to each others 3.3 Various forms of straight line 3.4 Perpendicular distance of a point from a line 3.5 Distance between two parallel lines 3.6 Angle between two straight lines 3.7 Geometry in Sulabh sutras in Indian Knowledge System	06	08
Total		28	34

## Section II

Unit No.	Topics / Sub-topics	Lectures (Hours)	SA-TH (Marks)
Unit 4 Trigonometry	<b>TRIGONOMETRY</b> 4.1 Fundamental Identities(Only state,No examples) 4.2 Conversion of degree into radian and vice versa of standard angles 4.3 Trigonometric ratios of Compound Angles(Without Proof) , Examples 4.4 Trigonometric ratios of Allied Angles (Without Proof) , Examples 4.5 Trigonometric ratios of Multiple and Submultiple Angles (Without Proof) , Examples 4.6 Factorization and De-Factorization Formulae (Without Proof) , Examples 4.7 Inverse Trigonometric ratios , Principle values and simple problems 4.8 Trigonometry in Indian Knowledge System : The evolution of sine function in India 4.9 Trigonometry in Indian Knowledge System : Indian Trigonometry-From ancient beginning to Nilakantha 4.10 Trigonometry in Indian Knowledge System : Ancient Indian Astronomy 4.11 Trigonometry in Indian Knowledge System: Pythagorean to triples in Sulabhsutras	14	14
Unit 5 Differential Calculus	5.1 <b>Functions:</b> Concept of Functions and simple examples 5.2 Limits:Concept of Limits without examples 5.3 <b>Derivatives:</b> 5.3.1 Derivative of sum, difference, product and quotient of two or more functions 5.3.2 Derivative of composite functions 5.3.3 Derivative of Inverse functions 5.3.4 Derivative of Implicit functions 5.3.5 Derivative of Parametric functions 5.3.6 Derivative of exponential and logarithmic functions 5.3.7 Calculus in Indian Knowledge system “ Discovery of Calculus by Indian Astronomers ( Indian Mathematics)	14	16
Unit 6 Application of Derivatives	<b>APPLICATIONS OF DERIVATIVES</b> 6.1 Second Order Derivatives(without examples) 6.2 Equation of Tangent & Normal 6.3 Maxima & Minima 6.4 Radius of curvature	4	6
	Total	32	36



### LIST OF TUTORIALS

Sr. no	Tutorial Title	No.of Hrs	Relevant CO
1	Solve Simple problems of Logarithms based on given application	2	CO1
2	Solve elementary problems on Algebra of Matrices	2	CO1
3	Solve simultaneous equations using Matrix inversion method	2	CO1
4	Resolve into Partial Fractions using linear non repeated, repeated and irreducible quadratic factors	2	CO1
5	Practice problems on equation of straight lines using different forms, Solve problems on perpendicular distance, distance between two parallel lines and angle between two lines	2	CO3
6	Solve problems on finding range, coefficient of range and mean deviation	2	CO2
7	Solve problems on Standard deviation, coefficient of variation and comparison of two sets	2	CO2
8	Solve problems on Allied & Compound angles	2	CO4
9	Solve problems on Multiple & submultiple angles	2	CO4
10	Solve problems on factorization & De- factorization formulae	2	CO4
11	Solve problems on Inverse Trigonometric Functions	2	CO4
12	Solve examples on functions & rules of derivatives	2	CO5
13	Solve examples on Derivative of composite function ,inverse & parametric functions,	2	CO5
14	Solve examples on Derivative of exponential, implicit and logarithmic functions	2	CO5
15	Solve examples on Application of Derivatives	2	CO5

**COURSE ID** : ET  
**COURSE NAME** : ENGINEERING GRAPHICS  
**COURSE CODE** : CCH109  
**COURSE ABBREVIATION** : HGRC

### C. LEARNING SCHEME:

Scheme component		Hours	Credits
Actual Contact Hours / week	Classroom Learning	02	2
	Tutorial Learning	-	
	Laboratory Learning	02	
	SLH-Self Learning	02	
	NLH-Notional Learning	04	

### D. ASSESSMENT SCHEME :-

PAPER DURATION IN HRS	THEORY				BASED ON LL&TL				BASED ON SLA		TOTAL
					Practical						100
	FA-TH	SA-TH	TOTAL		FA -PR		SA-PR		MAX	MIN	
-	MAX	MAX	MAX	MIN	MAX	MIN	MAX	MIN	MAX	MIN	
-	-	-	-	-	50	20	50@	20	-	-	

**(Total IKS Hrs for Sem.: 02 Hrs)**

**C: ABBREVIATIONS:-** CL- Class Room Learning , TL- Tutorial Learning, LL- Laboratory Learning, SLH-Self Learning Hours, NLH-Notional Learning Hours, FA - Formative Assessment, SA - Summative assessment, IKS - Indian Knowledge System, SLA - Self Learning Assessment

**Legends:** @ Internal Assessment, # External Assessment, \*# On Line Examination, @\$ Internal Online Examination.

1. FA-TH represents average of two class tests of 30 marks each conducted during the semester.
2. If candidate is not securing minimum passing marks in FA-PR of any course then the candidate shall be declared as "Detained" in that semester.
3. If candidate is not securing minimum passing marks in SLA of any course then the candidate shall be declared as fail and will have to repeat and resubmit SLA work.
4. Notional Learning hours for the semester are (CL+LL+TL+SL)hrs.\* 15 Weeks
5. 1(one) credit is equivalent to 30 Notional hrs.
6. \* Self learning hours shall not be reflected in the Time Table.

\* Self learning includes micro project / assignment / other activities. (Provide list of all assignments here in tabular format At least 6 to 8 assignments to be given)

## **D. i) RATIONALE:-**

Engineering graphics is the language of engineers. The concepts of graphical language are used in expressing the ideas, conveying the instructions, which are used in carrying out the jobs on the sites, shop floor etc. This course is useful in developing drafting and sketching skills in the student. It covers the knowledge & use of drawing instruments & also familiarizes the learner about Bureau of Indian standards related to engineering drawing.

The curriculum aims at developing the ability to draw and read various engineering curves, projections and dimensioning styles. The subject mainly focuses on use of drawing instruments, developing imagination and translating ideas into sketches. This course also helps to develop the idea of visualizing the actual object or part on the basis of drawings and blue prints.

## **ii) INDUSTRY / EMPLOYER EXPECTED OUTCOME**

The aim of this course is to help the student to attain the following industry identified outcome through various learning experiences:

1. Prepare engineering drawing using prevailing drawing instruments.

## **E. COURSE LEVEL LEARNING OUTCOMES (COS)**

CCH109-1 Draw lines, letters and numbers using standard geometrical dimensions.

CCH109-2 Demonstrate the ability to draw conic sections using different methods and concepts.

CCH109-3 Apply orthographic projections to effectively convert diverse surface features.

CCH109-4 Apply basic CAD commands for drawing different entities.

CCH109-5 Draw free hand sketches of given engineering elements.

**Competency, course outcomes and programme outcomes/programme specific outcomes  
(cp-co-po/ps) matrix**

[Note: Correlation levels: 1: Slight (Low), 2: Moderate (Medium), 3: Substantial (High), - : no correlation]

Competency and Cos	Programme Outcomes POs and PSOs								
	PO 1 Basic and Discipline specific knowledge	PO 2 Problem Analyses	PO 3 Design / Development of solutions	PO 4 Engineering Tools, Experimentation and Testing	PO 5 Engineering Practices for society, sustainability and Environment	PO 6 Project Management	PO 7 Life-long Learning	PSO1 Work in manufacturing and service sector	PSO2 Start entrepreneurial activity
Competency: Prepare engineering drawing using prevailing drawing instruments.	-	-	-	-	-	-	-	-	-
CCH109-1 CO-1 Draw lines, letters and numbers using standard geometrical dimensions.	3	-	-	2	-	2	-	1	1
CCH109-2 CO-2 Demonstrate the ability to draw conic sections using different methods and concepts.	3	-	-	2	-	2	-	1	1
CCH109-3 CO-3 Apply orthographic projections to effectively convert diverse surface features.	3	-	-	2	-	2	-	1	1
CCH109-4 CO-4 Use Apply basic CAD commands for drawing different entities.	3	-	-	2	-	2	-	1	1
CCH109-4 CO-5 Draw free hand sketches of given engineering elements.	3	-	-	2	-	2	-	2	1

**PSO 1: Operate and Maintain:** Competency to apply the concepts of Electronics & Telecommunication engineering in the operation and maintenance of engineering application systems.

**PSO 2: Supervision and providing solution:** Ability to supervise work and reach appropriate solution to simple practical problems in Electronics & Telecommunication engineering industry.

## F. CONTENT:

### I) Practical exercises

Sr No	Laboratory Practical Exercise	Skills / Competencies to be Developed	RelevantCOs
1	Draw horizontal, vertical, 30-degree, 45 degrees, 60- & 75-degrees lines using Tee and Set squares/drafter. (SketchBook).	To develop drawing skill	CO1
2	Draw different types of lines, dimensioning styles (SketchBook)	To develop drawing skills of line and dimensions	CO1
3	Draw one figure showing dimensioning techniques, two problems on redraw the figures and one problem on loci of points - slider crank mechanism. (Sketch Book)	To develop drawing skill of various loci points	CO1
4	Draw one figure showing dimensioning techniques, two problems on redraw the figures (01 Sheet)	To develop drawing ability in dimensioning technique	CO1
5	Draw any four Engineering Curves (Sketchbook)	To develop drawing skill for various Engineering curves	CO1
6	Draw any four Engineering Curves – (01 Sheet)	To develop drawing ability in Engineering drawing.	CO1
7	Draw two problems on orthographic projections using first angle method of projection having plain surfaces, slanting surfaces and slots etc.- (Sketchbook)	To develop drawing ability to draw Orthographic projection	CO2 CO4
8	Draw two problems on orthographic projections using first angle method of projection having plain surfaces, slanting surfaces and slots etc.	To develop drawing skill	CO2 CO4
9	Draw two problems on orthographic projections using first angle method of projection having cylindrical surfaces, ribs etc. (Sketchbook)	To develop drawing skill	CO2 CO4
10	Draw two problems on orthographic projections using first angle method of projection having cylindrical surfaces, ribs etc.- (01 Sheet)	To develop drawing ability to draw Orthographic projection	CO2 CO4
11	Draw basic 2D entities like rectangle, rhombus, polygon, arcs, circles using CAD. Commands.	To develop ability to draw using CAD software	CO3
12	Draw basic 2D entities using rectangular and circular arrays.	To develop ability to draw using CAD software	CO3
13	Draw basic branch specific components using CAD commands	To develop ability to draw using CAD software	CO3 CO4

14	Draw complex branch specific components using CAD commands.	To develop ability to draw using CAD software	CO3 CO4
15	Problem Based Learning: Given the orthographic views of at least three objects with few missing lines, the student will try to imagine the corresponding objects, complete the views and draw these views (sketch book).	To develop drawing ability to draw Orthographic projection and sectional orthographic projection	CO2 CO4
16	Draw freehand Sketches of 12 different standard components (Sketch book)	To develop drawing skill in free hand sketches	CO5
17	Draw freehand Sketches of 12 different standard components (1 Sheet)	To develop drawing skill in free hand sketches	CO5
18	Correlate ancient Indian sculptures, Indian temples, Monuments, etc. with Engineering Graphics	To develop drawing skill in free hand sketches	CO1CO2CO3 CO4CO5

## II) Theory

Sr. no.	Topics/Subtopics	Learning (Hours)	Classroom learning evaluation Marks
CO: CCH109-1 Draw lines, letters and numbers using standard geometrical dimensions.			
1	<b>Basic Elements of Drawing</b> 1.1 Drawing Instruments and supporting material: method to use them with applications. 1.2 Standard sizes of drawing sheets (ISO-A series) 1.3 I.S. codes for planning and layout. 1.4 Letters and numbers (single stroke vertical) 1.5 Convention of lines and their applications. 1.6 Dimensioning techniques as per SP-46 (Latest edition) – types and applications of chain, parallel and coordinate dimensioning Geometrical constructions. 1.7 Mandala Art: It's known for its intricate and symmetric design, often used in meditation and spiritual practices.	6	
CO: CCH109-2 Demonstrate the ability to draw conic sections using different methods and concepts.			
2	<b>Engineering curves &amp; Loci of Points.</b> 2.1 Concept and understanding of focus, directrix, vertex and eccentricity. Conic sections. 2.2 Methods to draw an ellipse by Arcs of circle method & concentric circles method. 2.3 Methods to draw a parabola by Directrix-Focus method & Rectangle method 2.4 Methods to draw a hyperbola by Directrix-Focus	6	

Sr. no.	Topics/Subtopics	Learning (Hours)	Classroom learning evaluation Marks
	method. 2.5 Methods to draw involutes: circle & pentagon, Point Starter and Solid state starter 2.6 Warli Art: uses basic geometric shapes to depict scenes from tribal life also uses of circles, triangles, and squares.		
CO: CCH109-3 Apply orthographic projections to effectively convert diverse surface features.			
3	<b>Orthographic projections</b> 3.1 Introduction of projections-orthographic, perspective, isometric and oblique: concept and applications. Orthographic projection: First angle and Third angle method, their symbols. Conversion of pictorial view into Orthographic Views – object containing plain surfaces, slanting surfaces, slots, ribs, cylindrical surfaces.	8	
CO: CCH109-4 Apply basic CAD commands for drawing different entities.			
4	<b>Computer Aided Drafting</b> 4.1 Basic entities: line, circle, arc, polygon, ellipse, rectangle, multiline, polyline. 4.2 Commands: trim, delete, copy, offset, array, block, layers. 4.3 Dimensioning: linear, horizontal, vertical, aligned, rotated, baseline, continuous, diameter, radius, angular dimensions. 4.4 Text: Single line, multiline. Standard sizes of sheet, selecting various plotting parameters such as paper size, paper units, drawing orientation, plot scale, plot offset, plot area, print preview.	4	
CO: CCH109-5 Draw free hand sketches of given engineering elements.			
5	<b>Free Hand Sketches of Engineering Elements</b> 5.1 Free hand sketches of machine elements: Thread profiles, nuts, bolts, studs, set screws, washers, Locking arrangements. (For branches other than mechanical Engineering, the teacher should select branch specific elements for free hand sketching) Cave Paintings and Rock Art: 5.2 India has ancient rock art and cave paintings found in places like Bhimbetka, depicting scenes from prehistoric times using graphic representations.	6	

\*\* No questions will be asked on IKS learning subtopics in any question papers.

## G. List of Assignments under SLA

**\*\*At least complete six assignments must be completed.**

Sr.No	List of Assignment (under SLA)	Hrs Allotted
01	List the applications of engineering curves in different fields of engineering and submit a report on it.	02
02	Prepare a list of industrial and household components in which conic curves are used and justify the utility of these curves.	02
03	Produce 2D technical drawings using industry-standard conventions and symbols	02
04	identify and use geometric dimensioning and tolerance (GD&T) symbols correctly	02
05	Proficiently use Computer-Aided Design (CAD) software to create and edit engineering drawings?	02
06	List various industry and international standards (e.g., ASME Y14.5) in your engineering graphics work	04
07	Developed a systematic approach to quality assurance in your engineering graphics projects	02
08	List various ethical considerations in engineering graphics, such as intellectual property and honesty in design	02

## H. Assessment Criteria

### i) Formative Assessment of Practical:-

Every assignment shall be assessed for 50 marks as per following criteria:

Domain	Particulars	Marks out of 50
Cognitive	Understanding	10
	Application	10
Psychomotor	Operating Skills	10
	Drawing / drafting skills	10
Affective	Discipline and Punctuality	10
<b>Total</b>		<b>50</b>

### ii) Summative Assessment of Practical:

Every practical assignment shall be assessed for 50 marks as per following criteria:

Sr.no	Criteria	Marks allotted
1	Attendance at regular practical	10
2	Preparedness for practical	10
3	Neat & complete Diagram.	10
4	Observations & handling of instrument.	10
5	Oral Based on Lab work and completion of task	10
<b>TOTAL</b>		<b>50</b>

## J) Instructional Methods:

1. Lectures cum Demonstrations.
2. Class room practices.



3. Use of projector and soft material for demonstration

### K) Teaching and Learning resources:

Chalk board, LCD presentations, Demonstrative kits, Demonstrative charts.

### L) Reference Books:

S.N.	Name of Book	Author	Publication
1	Bureau of Indian Standards.	Engineering Drawing Practice for Schools and Colleges IS: SP-46	Third Reprint, October 1998 ISBN No. 81-7061-091-2
2	Bhatt, N.D.	Engineering Drawing	Charotar Publishing House, 2010 ISBN No.978-93-80358-17-8
3	Bhatt, N.D.; Panchal, V. M	Machine Drawing	Charotar Publishing House, 2010 ISBN No.978-93-80358-11-6
4	Jolhe, D.A.	Engineering Drawing	Tata McGraw Hill Edu. New Delhi, 2010, ISBN No. 978-0-07-064837-1
5	Dhawan, R. K.	Engineering Drawing	S. Chand and Company New Delhi, ISBN No.81-219-1431-0
6	Pradhan, S.K Jain, K.K	Engineering Graphics	Khanna Book Publishing CO(P) LTD, New Delhi, ISBN No. 978-93-91505-50-9
7	Jeyapooan T	Engineering Drawing and Graphics using AutoCAD	Vikas Publishing House Pvt. Ltd., First Reprint 2013, ISBN NO.978-81259-4000-5

### M) Learning Website & Software

Sr.No	Link / Portal	Description
1	<a href="https://www.youtube.com/watch?v=dmt6_n7Sgcg">https://www.youtube.com/watch?v=dmt6_n7Sgcg</a>	Free Hand Sketches
2	<a href="https://www.youtube.com/watch?v=dmt6_n7Sgcg">https://www.youtube.com/watch?v=dmt6_n7Sgcg</a>	Orthographic Projection
3	<a href="https://www.youtube.com/watch?v=3WXPanCq9LI">https://www.youtube.com/watch?v=3WXPanCq9LI</a>	Basics of Projection
4	<a href="https://www.youtube.com/watch?v=fvjk7PlxAuo">https://www.youtube.com/watch?v=fvjk7PlxAuo</a>	Introduction to Engineering Graphics
5	<a href="https://www.youtube.com/watch?v=cmR9cfWJRU">https://www.youtube.com/watch?v=cmR9cfWJRU</a>	Basics of AutoCAD

**COURSE ID :**

**COURSE NAME : BASIC ELECTRONICS**

**COURSE CODE : ETH101**

**COURSE ABBREVIATION : HBTX**

### A. LEARNING SCHEME:

Scheme component		Hours	Credits
Actual Contact Hours / week	Classroom Learning	03	04
	Tutorial Learning	00	
	Laboratory Learning	04	
	SLH-Self Learning	01	
	NLH-Notional Learning	08	

### B. ASSESSMENT SCHEME:-

PAPER DURATION IN HRS	THEORY				BASED ON LL&TL				BASED ON SLA		Total
					Practical						
03	FA-TH	SA-TH	TOTAL		FA -PR		SA-PR		MAX	MIN	
	MAX	MAX	MAX	MIN	MAX	MIN	MAX	MIN			
	30	70	100	40	50	20	25@	10	25	10	200

**C. ABBREVIATIONS:** - CL- Class Room Learning, TL- Tutorial Learning, LL-Laboratory Learning, SLH-Self Learning Hours, NLH-Notional Learning Hours, FA - Formative Assessment, SA -Summative assessment, IKS - Indian Knowledge System, SLA - Self Learning Assessment

**Legends:** @ Internal Assessment, # External Assessment, \*# On Line Examination, @\$ Internal Online Examination.

1. FA-TH represents average of two class tests of 30 marks each conducted during the semester.
2. If candidate is not securing minimum passing marks in FA-PR of any course then the candidate shall be declared as "Detained" in that semester.
3. If candidate is not securing minimum passing marks in SLA of any course then the candidate shall be declared as fail and will have to repeat and resubmit SLA work.
4. Notional Learning hours for the semester are (CL+LL+TL+SL) hrs.\* 15 Weeks
5. 1(one) credit is equivalent to 30 Notional hrs.
6. \* Self learning hours shall not be reflected in the Time Table.

\* Self learning includes micro project / assignment / other activities. (Provide list of all assignments here in tabular format At least 6 to 8 assignments to be given)

## D. i) RATIONALE: -

A basic electronics course is essential for building foundational knowledge, developing practical skills, preparing for various careers, and fostering technological literacy. It also promotes critical thinking, innovation and interdisciplinary applications, while supporting broader educational and personal development goals. Section I covers Semiconductor Diodes, Rectifiers and Filters and Regulators and Power supply. In section II Bipolar Junction Transistor, Biasing of transistor, BJT amplifiers and Field Effect Transistors (FET) are covered. So this course is the base of all electronic courses offered in forthcoming semesters.

## ii) INDUSTRY / EMPLOYER EXPECTED OUTCOME

The aim of this course is to help the student to attain the following industry identified outcome through various learning experiences:

1. Maintain and operate semiconductor diode in rectifiers and regulator power supply.
2. Maintain and operate the transistor in basic electronics circuits..

## E. COURSE LEVEL LEARNING OUTCOMES (COs)

ETH101-1 Describe construction, working and characteristics of diodes.

ETH101-2 Use rectifiers and filters in electronics circuit

ETH101-3 Maintain DC regulated power supply.

ETH101-4 Use BJT in electronics circuits

ETH101-5 Examine and use various types of biasing circuits of BJT

ETH101-6 Use FET and MOSFET in electronics circuits

## Course outcomes and programme outcomes/ programme specific outcomes (CO- PO/PSO) matrix

[Note: Correlation levels: 1: Slight (Low), 2: Moderate (Medium), 3: Substantial (High), “-“ : No correlation]

Cos	Programme Outcomes POs and PSOs								
	PO 1 Basic and Discipline specific knowledge	PO 2 Problem Analysis	PO 3 Design / Development of solutions	PO 4 Engineering Tools, Experimenta tion and Testing	PO 5 Engineering Practices for society, sustainability and Environment	PO 6 Project Managemen t	PO 7 Life- long Learning	PSO1 Operate and Maintain	PSO2 Supervision and Providing Solution
ETH101-1 Describe construction, working and characteristics of diodes	3	2	-	3	-	-	--	2	--
ETH101-2 Use rectifiers and filters in electronics circuit	3	2	-	2	--	--	--	3	2
ETH101-3 Maintain DC regulated power supply	3	2	2	2	-	--	--	3	3
ETH101-4 Use BJT in electronics circuits	3	1	1	2	--	--	--	3	--

Cos	Programme Outcomes POs and PSOs								
	PO 1 Basic and Discipline specific knowledge	PO 2 Problem Analysis	PO 3 Design / Development of solutions	PO 4 Engineering Tools, Experimenta tion and Testing	PO 5 Engineering Practices for society, sustainability and Environment	PO 6 Project Managem ent	PO 7 Life- long Learning	PSO1 Operate and Maintain	PSO2 Supervision and Providing Solution
ETH101-5 Examine and use various types of biasing circuits of BJT	3	2	2	2	-	--	--	3	1
ETH101-6 Use FET and MOSFET in electronics circuits	3	2	--	1	--	--	--	3	--

## F. CONTENT:

### I) Practical exercises

The following practical exercises shall be conducted in the *Laboratory for AC Machine developed* by the Institute in practical sessions of batches of about 20- 22 students:

Sr. No.	Laboratory experiences	CO
1.	Identification and operation of CRO and Function Generator for voltage and frequency measurement in basic electronics laboratory	ETH 101-1
2.	Identification and operation of Regulated Power Supply for amplitude measurement in basic electronics laboratory	ETH 101-1
3.	Plot V-I characteristics of PN junction diode.	ETH 101-1
4.	Plot V-I characteristics of Zener diode.	ETH 101-1
5.	Plot V-I characteristics of LED.	ETH 101-1
6.	Plot V-I characteristics of Photodiode.	ETH 101-1
7.	Test the performance of half wave rectifier.	ETH 101-2
8.	Test the performance of full wave center tapped rectifier.	ETH 101-2
9.	Test the performance of full wave bridge rectifier.	ETH 101-2
10.	Test the performance of full wave rectifier with capacitor filter.	ETH 101-2
11.	Test the performance of full wave rectifier with LC filter.	ETH 101-2
12.	Test the performance of full wave rectifier with CLC filter.	ETH 101-2
13.	Build and test the performance of regulated power supply using IC 78XX	ETH 101-3
14.	Build and test the performance of regulated power supply using IC 79XX	ETH 101-3
15.	Build and test the performance of regulated power supply using ICLM317	ETH 101-3
16.	Verify performance of zener diode as voltage regulator: Line regulation	ETH 101-3
17.	Verify performance of zener diode as voltage regulator: Load regulation	ETH 101-3
18.	Identify and select transistors using datasheets	ETH 101-4
19.	Build, Test and Plot input and output characteristics of CE configuration	ETH 101-4

20.	Build, Test and Plot input and output characteristics of CB configuration	ETH 101-4
21.	Build and Test the BJT Fixed bias circuit for given input.	ETH 101-5
22.	Build and Test the BJT voltage divider bias circuit for given input.	ETH 101-5
23.	Construct and Test the performance parameters of BJT as Switch.	ETH 101-5
24.	Test the performance of JFET drain characteristics	ETH 101-6
25.	Test the performance of JFET transfer characteristics and calculate transconductance.	ETH 101-6
26.	Build complete circuit diagram of regulated power supply using IC 78XX/79XX on general purpose PCB	ETH 101-3
27.	Build the circuit of BJT as Switch on general purpose PCB	ETH 101-5

## II) Theory

### Section I

Sr. no.	Topics/Subtopics	Learning (Hours)	Classroom learning evaluation Marks
	<b><i>ETH101-1 Describe construction, working and characteristics of diodes.</i></b>		
<b>1</b>	<b>Semiconductor Diodes</b> 1.1 Construction, Symbol of P-N junction diode 1.2 P-N Junction with Unbias, forward Bias and reverse Bias. 1.3 Working principle of P-N junction diode 1.4 V-I characteristics of P-N junction diode. 1.5 Ideal diode and practical diode model. 1.6 Important specifications of P-N junction diode: PIV rating, Static forward and reverse resistance, dynamic forward and reverse resistance, Steady state forward current, Static forward voltage drop, Continuous power dissipation 1.7 Breakdown mechanism: Avalanche and zener breakdown 1.8 Zener diode: Construction Symbol, V-I characteristics and applications. 1.9 Specifications of zener diode: Zener voltage, Break over current ( $I_{ZK}$ ), Zener test current ( $I_{ZT}$ ) Dynamic Impedance ( $Z_Z$ ), Power dissipation. 1.10 Construction, symbol and V-I characteristics of LED, photodiode, varactor diode and tunnel diode.	<b>10</b>	<b>14</b>
	<b><i>ETH101-2 Use rectifiers and filters in electronics circuit</i></b>		
<b>2</b>	<b>Rectifiers and Filters</b> 2.1 Definition, need and classification of rectifier. 2.2 Half wave and full wave rectifier: Circuit diagram, operation and waveforms. 2.3 Parameters of rectifier: Average DC value of current and voltage ripple factor ripple frequency, PIV of diode, TUF, efficiency of rectifier of HWR and Full wave Center tapped and Bridge rectifier. (No derivation) 2.4 Definition and need of filter, types of filters: shunt capacitor	<b>6</b>	<b>10</b>

	filter, series inductor filter, LC filter and CLC filter. 2.5 Operation of each filter w.r.t. full wave bridge rectifier. 2.6 Comparison of filters		
	<b>ETH 101-3 Maintain DC regulated power supply.</b>		
<b>3</b>	<b>Regulators and Power supply</b> 3.1 Need of Regulated power supply. Basic block diagram of DC regulated power supply and function of each block 3.2 Load and Line regulation. 3.3 Zener diode voltage regulator: load and line regulation 3.4 Fixed voltage IC Regulator: Three terminal Pin diagram, working and specifications of 78XX and 79XX series. 3.5 Complete Circuit diagram of DC regulated power supply. 3.6 Variable voltage IC Regulator : LM 317 pin diagram Simple numerical on IC LM317	<b>6</b>	<b>10</b>
	<b>Sub-total</b>	<b>22</b>	<b>34</b>

## Section –II

Sr. no.	Topics/Subtopics	Learning (Hours)	Classroom learning evaluation Marks
	<b>ETH 101-4 Use BJT in electronics circuits</b>		
<b>4</b>	<b>Bipolar Junction Transistor</b> 3.1 BJT-Types, symbols 3.2 Construction of BJT. 3.3 Operating principles of NPN & PNP Transistor 3.4 Transistor configurations: CB,CE,CC 3.5 Modes of operation: Active, Cut-off, Saturation 3.6 Current amplification factor ( $\alpha, \beta, \gamma$ ) and Relation between them 3.7 Transistor input & output characteristic for CE & CB configuration. Numerical based on transistor current and amplification factor.	<b>7</b>	<b>12</b>
	<b>ETH101-5 Examine and use various types of biasing circuits of BJT</b>		
<b>5</b>	<b>Biasing of transistor</b> 4.1 Load line: AC Load line, DC Load Line 4.2 Quiescent Point for DC Load Line 4.3. Bias Stability, stability factor, Factors affecting bias stability, Thermal runaway 4.4 Transistor Biasing Methods: Different transistor biasing circuits. Circuit equations, advantages & disadvantages of 4.4.1 Fixed Bias Circuit 4.4.2 Voltage Divider Bias Circuit	<b>8</b>	<b>12</b>
	<b>ETH101-6 Use FET and MOSFET in electronics circuits</b>		
<b>6</b>	<b>Field Effect Transistor (FET)</b> 6.1 FET as voltage controlled device, Classification of FET <b>Junction Field Effect Transistor(JFET)</b> 6.2 Symbols of N-channel and P-channel JFET	<b>8</b>	<b>12</b>

6.3 Construction of N-channel and P-channel JFET 6.4 Working principle of N-channel JFET 6.5 Drain and transfer Characteristics of N- channel JFET 6.6 JFET parameters-A.C. drain resistance( $r_d$ ), transconductance ( $g_m$ ), amplification factor( $\mu$ ), relation between $\mu$ , $r_d$ & $g_m$ 6.7 Advantages, disadvantages, applications of JFET 6.8 Comparison between JFET and BJT <b>Metal Oxide Field Effect Transistor:-</b> 6.9 Types and symbol of MOSFET- Depletion type MOSFET and Enhancement type MOSFET 6.10 Working principle of N-channel depletion type and enhancement type MOSFET 6.11 Applications of MOSFET		
<b>Sub-total</b>	<b>23</b>	<b>36</b>

### G. List of Assignments under SLA

Sr. No.	List of Assignment (under SLA)	Hours allotted
1.	Write procedure to measure AC and DC amplitude, time period and frequency using CRO and function generator.	02
2.	Tabulate important characteristics of commonly available semiconductor diodes.	02
3.	Tabulate important characteristics of commonly available zener diodes.	02
4.	Tabulate important characteristics of commonly available LEDs.	02
5.	Write a procedure to test semiconductor diode using DMM	02
6.	Draw a chart showing circuit diagrams of half wave and full wave rectifiers with waveforms and equations.	02
7.	Study rectifier IC KBU 808	02
8.	Study of different types of regulator ICs other than mentioned in curriculum.	02
9.	Study Switch Mode Power Supply.	02
10.	Write procedure to identify terminals of BJT using digital multimeter	02
11.	Select specific BJTs and study the datasheet.	02
12.	Study different types of transistor biasing circuits.	02
13.	Which type of transistor biasing method is widely used? Why?	02
14.	Study working of JFET as variable resistor	02
15.	Study working of MOSFET as variable capacitor	02

\*\*Eight out of 12 assignments covering all six COs are compulsory. As per the requirement course teacher can modify the assignments.

## H. Specification table for setting question paper for semester end theory assessment

Section / Topic no.	Name of topic	Distribution of marks (level wise)			Total marks	CO
		Remember	Understand	Apply		
I / 1	Semiconductor Diodes	4	2	8	14	ETH101-1
I / 2	Rectifiers and Filters	2	2	6	10	ETH101-2
I / 3	Regulators and Power supply	2	8	-	10	ETH101-3
II / 4	Bipolar Junction Transistor	2	8	2	12	ETH101-4
II / 5	Biasing of transistor	-	4	8	12	ETH101-5
II / 6	Field Effect Transistor	-	4	8	12	ETH101-6
Total Marks		10	28	32	70	

## I. Assessment Criteria

### i) Formative Assessment of Practical: -

Every assignment shall be assessed for 25 marks as per following criteria:

Domain	Particulars	Marks out of 25
Cognitive	Understanding	05
	Application	05
Psychomotor	Operating Skills	05
	Drawing / drafting skills	05
Affective	Discipline and punctuality	05
<b>TOTAL</b>		<b>25</b>

### ii) Summative Assessment of Practical:

Every practical assignment shall be assessed for 25 marks as per following criteria:

Sr. no	Criteria	Marks allotted
1	Attendance at regular practical	05
2	Preparedness for practical	05
3	Neat & complete Diagram.	05
4	Observations & handling of instrument.	05
5	Oral Based on Lab work and completion of task	05
<b>TOTAL</b>		<b>25</b>

## J. Instructional Methods:

4. Lectures cum Demonstrations
5. Class room practices
6. Use of projector and soft material for demonstration
4. Virtual Laboratory



## K. Teaching and Learning resources:

Chalk board, LCD presentations, Demonstrative kits, Demonstrative charts

## L. Reference Books:

Sr. No	Name of Book	Author	Publication
1	Principles of Electronics	Mehta, V.K. Mehta, RohitMehta	S.Chand New Delhi, edition-2008 ISBN-13: 978-8121927833
2	Electronics Circuit and Circuit theory	Robert L.Boylestead	Pearson Education India, ISBN-13978-9332542600
3	A Text book of Applied Electronics	Sedha, R.S.	S.Chand (G/L) & Company Ltd; ISBN-13 978-8121904209
4	Basic Electronics (solid State)	B.L.Theraja	S Chand; ISBN-13 978-8121925556
5	Electronic Principles	Albert P. Malvino, David J.Bates	McGraw Hill; ISBN-13 978-9354602399
6	Basic Electronics and Linear Circuits	by N.N. Bhargava (Author), D.C. Kulshreshtha (Author), S.C. Gupta (Author)	NITTTR Chandigarh 2 <sup>nd</sup> Edition

## M. Learning Website & Software

- [www.circuitstoday.com/](http://www.circuitstoday.com/)
- [www.circuitlab.com/](http://www.circuitlab.com/)
- [www.vlab.com](http://www.vlab.com)
- [www.tinkercad.com](http://www.tinkercad.com)

**COURSE ID :**

**COURSE NAME : ELECTRONICS WORKSHOP PRACTICE**

**COURSE CODE : ETH102**

**COURSE ABBREVIATION : HWET**

**A. LEARNING SCHEME:**

Scheme component		Hours	Credits
Actual Contact Hours / week	Classroom Learning	02	03
	Tutorial Learning	--	
	Laboratory Learning	04	
	SLH-Self Learning	--	
	NLH-Notional Learning	06	

**B. ASSESSMENT SCHEME :-**

PAPER DURATION IN HRS	THEORY				BASED ON LL&TL				BASED ON SLA		TOTAL
					Practical						100
--	FA-TH	SA-TH	TOTAL		FA -PR		SA-PR		MAX	MIN	
	MAX	MAX	MAX	MIN	MAX	MIN	MAX	MIN			
	--	--	--	--	--	50	20	50@	20	--	

**(Total IKS Hrs for Sem.: 02 Hrs)**

**C. ABBREVIATIONS:-** CL- Class Room Learning , TL- Tutorial Learning, LL-Laboratory Learning, SLH-Self Learning Hours, NLH-Notional Learning Hours, FA - Formative Assessment, SA -Summative assessment, IKS - Indian Knowledge System, SLA - Self Learning Assessment

**Legends:** @ Internal Assessment, # External Assessment, \*# On Line Examination, @\$ Internal Online Examination.

1. FA-TH represents average of two class tests of 30 marks each conducted during the semester.
2. If candidate is not securing minimum passing marks in FA-PR of any course then the candidate shall be declared as "Detained" in that semester.
3. If candidate is not securing minimum passing marks in SLA of any course then the candidate shall be declared as fail and will have to repeat and resubmit SLA work.
4. Notional Learning hours for the semester are (CL+LL+TL+SL)hrs.\* 15 Weeks
5. 1(one) credit is equivalent to 30 Notional hrs.
6. \* Self learning hours shall not be reflected in the Time Table.

\* Self learning includes micro project / assignment / other activities. (Provide list of all assignments here in tabular format At least 6 to 8 assignments to be given)

## D. i) RATIONALE:-

Engineering diploma holders specializing in electronics are expected to handle various mechanical, electrical and electronics tools in the workshop in any industry in which they are employed. This course provides simulated industrial environment and enable students to perform a variety of operations in various shops using relevant electrical and electronic materials as well as use appropriate hand tools, equipment, tools and machinery. Through this course student will develop practical skills in identifying, testing, soldering, de-soldering, assembly, simulate, PCB design etc. of electronic components and circuits that will also be very useful for projects and other courses that he or she will undertake during the diploma programme as well as in the world of work.

## ii) INDUSTRY / EMPLOYER EXPECTED OUTCOME

The aim of this course is to help the student to attain the following industry identified outcome through various learning experiences:

1. Build simple electronic circuits on PCB.

## E. COURSE LEVEL LEARNING OUTCOMES

**ETH 102.1** Identify and measure the values of passive components.

**ETH 102.2** Identify the different types of switches, relays and digital displays

**ETH 102.3** Prepare the PCB using SMD component and ICs

**ETH 102.4** Identify the different types of cables and connectors

**ETH 102.5** Use digital meters and transducers in electronics circuits

**ETH102-6** Identify and use oscilloscope and signal generator in electronics circuits.

## Competency, course outcomes and programme outcomes/programme specific outcomes (cp-co-po/ps) matrix

[ Note : Correlation levels : 1: Slight (Low), 2: Moderate (Medium), 3: Substantial (High), "0"

Competency and Cos	Programme Outcomes POs and PSOs								
	PO 1 Basic and Disciplin e specific knowled ge	PO 2 Proble m Analys is	PO 3 Design / Develo pment of solutio ns	PO 4 Engine ering Tools, Experi mentat ion and Testin g	PO 5 Enginee ring Practice s for society, sustaina bility and Environ ment	PO 6 Proje ct Mana geme nt	PO 7 Life- long Learn ing	PSO1 Operat e and Maint ain:	PSO2 Supervi sion and providin g solution :
Competency: Build simple electronic circuits on PCB	3	1	2	2	0	0	2	2	-
ETH 102.1 CO-1 Identify and measure the values of passive components.	3	2	0	2	0	0	2	2	-
ETH 102.2 CO-2 Identify the different types of switches, relays and digital displays	3	0	0	2	0	0	2	2	-
ETH 102.3 CO-3 Prepare the PCB using SMD component and ICs	3	0	2	2	0	2	2	2	-
ETH 102.4 CO-4 Identify the different	3	0	0	1	0	0	2	2	-

Competency and Cos	Programme Outcomes POs and PSOs								
	PO 1 Basic and Discipline specific knowledge	PO 2 Problem Analysis	PO 3 Design / Development of solutions	PO 4 Engineering Tools, Experimentation and Testing	PO 5 Engineering Practices for society, sustainability and Environment	PO 6 Project Management	PO 7 Life-long Learning	PSO1 Operation and Maintenance:	PSO2 Supervision and providing solution:
types of cables and connectors									
ETH 102.5 CO-5 Use digital meters and transducers in electronics circuits	3	1	0	1	0	0	2	2	-
ETH102-6 CO-6 Identify and use oscilloscope and signal generator in electronics circuits.	3	1	0	1	0	0	2	2	-

## F. CONTENT:

### I) Practical exercises

The following practical exercises shall be conducted in the *Laboratory for Electronics Workshop Practice* by the Institute in practical sessions of batches of about 20- 22 students:

Sr. no	Laboratory experiences	CO
1	Safety symbols	ETH102-1
2	Fire extinguishers and accessories	ETH102-1
3	Identification and operation of DMM, Breadboard and Test different types of fixed resistors.	ETH102-1
4	Test different types of variable resistors.	ETH102-1
5	Test different types of fixed capacitors.	ETH 102.1
6	Test different types of variable capacitors.	ETH 102.1
7	Test different types of inductors.	ETH 102.1
8	Identification of windings of transformer	ETH 102.1
9	Test performance of relay	ETH 102.2
10	Test performance of Seven segment display	ETH 102.2
11	Test performance of LCD Displays	ETH 102.2
12	Test performance of Switches	ETH 102.2
13	Build simple circuits on a breadboard using resistors, diode, switch and LED.	ETH 102.2
14	Design PCB(Demonstration using software tool or Video)	ETH 102.3
15	Design PCB using copper clad or zero PCB	ETH 102.3

Sr. no	Laboratory experiences	CO
16	Identification of SMDs and ICs	ETH 102.3
17	Identify the different types of cables	ETH 102.4
18	Identify the different types of connectors	ETH 102.4
19	Test the performance of RTD	ETH 102.5
20	Test the performance of LVDT	ETH 102.5
21	Use thermocouple to measure temperature of the given liquid	ETH 102.5
22	Test relation between Linear displacement and output voltage using LVDT	ETH 102.5
23	Identification and operation of Regulated Power Supply for amplitude measurement in basic electronics laboratory	ETH102-6
24	Identification and operation of CRO and Function Generator for amplitude measurement in basic electronics laboratory	ETH102-6
25	Identification and operation of CRO and Function Generator for frequency measurement in basic electronics laboratory	ETH102-6
26	Identification and operation of DSO to measure amplitude and frequency of given signal	ETH102-6
27	Visit the industry	All Cos

## II) Theory

### Section I

Sr. no.	Topics/Subtopics	Learning (Hours)
CO: ETH 102.1 Identify and measure the values of passive components.		
1	<b>Resistors:</b> 1.1 Components-discrete, non-discrete, Active, passive components. 1.2 Concept of Resistors, Classification of resistors, Resistors general specification: - maximum voltage rating, power rating, temperature coefficient, tolerance, Ohmic range, operating temperature 1.3 Color Coding with three, four and five bands of resistors <b>Capacitors:</b> 1.4 Concept of Capacitor 1.5 Classification of capacitors 1.6 Coding of capacitors using numerals and color band system. <b>Inductors:</b> 1.7 Concept of Inductor, Classification of Inductor 1.8 Specifications:- self-inductance, mutual inductance, coefficient of coupling, Q factor, Inductive Reactance 1.9 Color coding of Inductor. <b>Transformers:</b> 1.10 Definition of transformer. Types of transformer: Step up and Step down transformer	06

CO: ETH 102.2 Identify the different types of switches, relays and digital displays		
2	<p><b>Switches, Relays and Displays</b></p> <p><b>Switches:</b></p> <p>2.1 Types of Switches: SPST, SPDT, DPST, DPDT</p> <p>2.2 Construction and application of Toggle, Rotary, push to on &amp; push to off, Rocker switch, slide switch, limit switch, proximity switch, photo sensor switch.</p> <p><b>Relays:</b></p> <p>2.3 Construction and working of electromechanical relay</p> <p>2.4 Construction and working of solid state relay</p> <p><b>Displays:</b></p> <p>2.5 Classifications of displays</p> <p>2.6 Construction, operation &amp; application of LED, Seven segment display-common cathode &amp; common anode display, Dot matrix display, sixteen, fourteen segment display</p> <p>2.7 Construction, operation &amp; applications of Liquid crystal display (LCD)-Dynamic Scattering Display</p> <p>Different types of switches (IKS learning )</p>	04
3	<p><b>Introduction to PCB, SMD and IC</b></p> <p><b>PCB:</b></p> <p>3.1 Concept of PCB ,Advantages &amp; disadvantages of PCB, Types of PCB</p> <p>3.2 Base &amp; Conducting material, types of laminates, Flowchart for preparation of single sided PCB</p> <p><b>SMD:</b></p> <p>3.3 Introduction to SMT,SMD</p> <p>3.4 Advantages &amp; disadvantages of SMD.</p> <p><b>Integrated Circuit:</b></p> <p>3.5 Concept of IC, Advantages &amp; disadvantages of ICs</p> <p>3.6 Classification of IC's, Linear and Digital IC's and its examples, Flowchart for preparation of IC</p>	04

## Section II

Sr. no.	Topics/Subtopics	Learning (Hours)	Classroom learning evaluation Marks
CO: ETH 102.4 Identify the different types of cables and connectors			
4	<b>Cables and Connectors</b> <b>Cables:</b> 4.1 Types of cable 4.2 Construction, and applications of coaxial cable, telephone cable, FRC cable, Twin core cable(Twisted & Shielded type)cable used for CRO. <b>Connectors:</b> 4.3 Types of connectors 4.4 Construction and applications of BNC, TNC, Edge, FRC connectors, Phone Plug & Jacks	04	
CO: ETH 102.5 Use digital meters and transducers in electronics circuits			
5	<b>Digital meters and introduction to transducers.</b> 5.1 Concepts of ADC & DAC only 5.2 Advantages and Disadvantages of Digital Instruments , Comparison of digital and analog instruments 5.3 Resolution, Sensitivity and Accuracy of digital display. 5.4 Digital Multimeter: Measurement of electrical quantities. 5.5 Transducers: Definition, classification: Active, Passive, Primary, Secondary, Analog, Digital 5.6 Construction, Operation, Applications : LVDT, RTD.	06	
CO: ETH102-6 Identify and use oscilloscope and signal generator in electronics circuits.			
6	<b>Oscilloscope and Signal Generator</b> 6.1 CRO-Front panel control of CRO. 6.2 Digital storage oscilloscope: Function of DSO. 6.3 Uses of CRO & DSO- Amplitude, Frequency and Phase measurement 6.4 Signal / Function generator-AF and RF type- Block diagram and Operation only.	06	

\*\* No questions will be asked on IKS learning subtopics in any question papers.

### G. Assessment Criteria

#### i) Formative Assessment of Practical:-

Every assignment shall be assessed for 50 marks as per following criteria:

Domain	Particulars	Marks out of 50
Cognitive	Understanding	10
	Application	10
Psychomotor	Operating Skills	10
	Drawing / drafting skills	10
Affective	Discipline and punctuality	10
<b>TOTAL</b>		<b>50</b>

**ii) Summative Assessment of Practical:**

Every practical assignment shall be assessed for 50 marks as per following criteria

Sr. no	Criteria	Marks allotted
1	Attendance at regular practical	10
2	Preparedness for practical	10
3	Neat & complete Diagram.	10
4	Observations & handling of instrument.	10
5	Oral Based on Lab work and completion of task	10
<b>TOTAL</b>		<b>50</b>

**H. Instructional Methods:**

7. Lectures cum Demonstrations,
8. Class room practices.
9. Use of projector and soft material for demonstration
4. Animation videos
5. Simulation software

**I. Teaching and Learning resources:**

Chalk board, LCD presentations, Demonstrative kits, Demonstrative charts.

**J. Reference Books:**

S.N.	Name of Book	Author	Publication
8	Raghuwanshi B.S.	A Course in Workshop Technology	Dhanpat Rai & Sons, New Delhi, 2017 or latest edition
9	Sarathe A.K.	Engineering Workshop Practice	Khanna Book Publishing Co.(P) Ltd. ,New Delhi; 2021 or latest edition ISBN:978-9391505516
10	Gupta J.K., Khurmi R.S.	A Textbook of Manufacturing Process (Workshop Technology)	S. Chand and Co., New Delhi, 2021 or latest edition, ISBN: 978-8121908689
11	Jones, Thomas H.	Electronic Components Handbook	Reston Publishing, Virginia, US, latest edition, ISBN: 978-0879092221
12	Mehta V.K., Mehta Rohit	Principles of Electronics	S. Chand and Co., New Delhi-110 055, 2014, ISBN: 978-8121924504
13	Glory Priyadarshini J. , Rani K.S.S., Maheswari M.P., Gomathy S.	Engineering Workshop practice on Electrical & Electronics Engineering	Notion Press, Mumbai, 2021 or latest edition, ISBN: 978-1639203819



## K. Learning Website & Software

Sr.No	Link / Portal	Description
1	<a href="http://fireextinguishertraining.com/">http://fireextinguishertraining.com/</a>	Fire extinguisher
2	<a href="http://www.youtube.com/watch?v=WE-SislzSMY">www.youtube.com/watch?v=WE-SislzSMY</a>	Fire extinguisher
3	<a href="https://www.youtube.com/watch?v=lUojO1HvC8c">https://www.youtube.com/watch?v=lUojO1HvC8c</a>	Fire extinguisher
4	<a href="https://www.youtube.com/watch?v=0jbFC8dvTVY">https://www.youtube.com/watch?v=0jbFC8dvTVY</a>	Electrical tools
5	<a href="https://www.electroschematics.com/tools/">https://www.electroschematics.com/tools/</a>	Electronic tools
6	<a href="https://www.youtube.com/watch?v=Fwj_d3uO5g8">https://www.youtube.com/watch?v=Fwj_d3uO5g8</a>	Diodes
7	<a href="http://www.eleccircuit.com">http://www.eleccircuit.com</a>	Electronic circuit
8	<a href="https://mightyohm.com/files/soldercomic/FullSolderComic_EN.pdf">https://mightyohm.com/files/soldercomic/FullSolderComic_EN.pdf</a>	Soldering
9	<a href="https://www.tinkercad.com/">https://www.tinkercad.com/</a>	3D modeling software
10	Multisim Live Online Circuit Simulator	Simulation software
11	EveryCircuit	Mobile Application

**COURSE ID :**

**Course Name : Fundamentals of ICT(CE/ME/EE/MT/ET/IT)**

**Course Code : CCH202**

**Course Abbreviation : HICT**

**A. LEARNING SCHEME:**

Scheme component		Hours	Credits
Actual Contact Hours / week	Classroom Learning	01	02
	Tutorial Learning	00	
	Laboratory Learning	02	
	SLH-Self Learning	01	
	NLH-Notional Learning	04	

**B. ASSESSMENT SCHEME:-**

PAPER DURATION IN HRS	THEORY				BASED ON LL&TL				BASED ON SLA		Total
					Practical						
-	FA-TH	SA-TH	TOTAL		FA -PR		SA-PR		MAX	MIN	
	MAX	MAX	MAX	MIN	MAX	MIN	MAX	MIN			
	-	-	-	-	-	25	10	25@	10	25	10

**C. ABBREVIATIONS:** - CL- Class Room Learning, TL- Tutorial Learning, LL-Laboratory Learning, SLH-Self Learning Hours, NLH-Notional Learning Hours, FA - Formative Assessment, SA -Summative assessment, IKS - Indian Knowledge System, SLA - Self Learning Assessment

**Legends:** @ Internal Assessment, # External Assessment, \*# On Line Examination, @\$ Internal Online Examination.

1. FA-TH represents average of two class tests of 30 marks each conducted during the semester.
2. If candidate is not securing minimum passing marks in FA-PR of any course then the candidate shall be declared as "Detained" in that semester.
3. If candidate is not securing minimum passing marks in SLA of any course then the candidate shall be declared as fail and will have to repeat and resubmit SLA work.
4. Notional Learning hours for the semester are (CL+LL+TL+SL) hrs.\* 15 Weeks
5. 1(one) credit is equivalent to 30 Notional hrs.
6. \* Self learning hours shall not be reflected in the Time Table.

\* Self learning includes micro project / assignment / other activities. (Provide list of all assignments here in tabular format At least 6 to 8 assignments to be given)

**D. RATIONALE:**

In any typical business setup in order to carry out routine tasks related to create business documents, perform data analysis and its graphical representations and making electronic slide show presentations, the student need to learn various software as office automation tools like word processing applications, spreadsheets and presentation tools. They also need to use these tools for making their project reports and presentations. The objective of this course is to develop the basic competency in students for using these office automation tools to accomplish the job. This course also presents an overview of emerging technologies so that students of different discipline can appraise the applications of these technologies in their respective domain.

#### **E. COMPETENCY:**

**Apply Fundamental knowledge of computer system to work with simple applications.**

**Cognitive:** i) State the basic parts of a computer system and relationships among component. ii) State characteristics and functions of CPU's, motherboard, RAM, Storage devices etc.

**Psychomotor:** i) Use computers for Internet services, Electronics Documentation, Data Analysis and Slide Presentation. ii) Appraise Application of ICT based Emerging Technologies in different domain.

**Affective: Attitude** of i) Precision ii) Accuracy iii) Safety iv) Punctuality

#### **F. COURSE OUTCOMES:**

CCH202-1 - Use computer system and its peripherals for given purpose

CCH202-2 - Prepare Business document using Word Processing Tool

CCH202-3 - Analyze Data and represent it graphically using spreadsheet

CCH202-4 - Prepare professional Slide Show presentations

CCH202-5 – Use different types of Web Browsers and Apps

CCH202-6 - Explain concept and applications of Emerging Technologies

### G. COURSE OUTCOMES AND PROGRAMME OUTCOMES (CO-PO) MATRIX

Course Outcomes (COs)	Programme Outcomes (POs)							Programme Specific Outcomes* (PSOs)	
	PO-1 Basic and Discipline Specific Knowledge	PO-2 Problem Analysis	PO-3 Design/Development of Solutions	PO-4 Engineering Tools	PO-5 Engineering Practices for Society, Sustainability and Environment	PO-6 Project Management	PO-7 Life Long Learning	PSO- 1	PSO- 2
CCH202-1	1	-	-	-	-	-	1		
CCH202-2	-	-	-	3	-	-	1		
CCH202-3	-	2	1	3	-	-	1		
CCH202-4	-	-	-	3	-	-	1		
CCH202-5	1	-	-	3	-	-	3		
CCH202-6	1	-	-	3	-	-	3		
Legends :- High:03, Medium:02,Low:01, No Mapping: - *PSOs are to be formulated at institute level									

### H. LABORATORY WORK:

Laboratory experiments and related skills to be developed:

Sr. No.	Title of Experiment	Skills to be developed	Course outcome
1.	a) Work with Computer System, Input/output devices, and peripherals. b) Work with files and folders	1.1 Identify various Input/output devices, connections and peripherals of computer system 1.2 Work with Computer System, Input/output devices,	CCH202-1

2.	Work with document files: a) Create, edit and save document in Word Processing. b) Text, lines and paragraph level formatting	2.1 Create and manage word document. 2.2 Apply formatting features on text at line, paragraph and page level.	CCH202-2
3.	Work with Images and Shapes in Word Processing.	3.1 Insert and edit images, shapes in a document file	CCH202-2
4.	Work with tables in Word Processing.	4.1 Insert table and apply various table formatting features on it.	CCH202-2
5.	Working with layout and printing a) Document page layout, Themes, and printing. b) Use of mail merge with options.	5.1 Apply page layout features in word processing. 5.2 Print a document by applying various print options 5.3 Use mail merge in word processing	CCH202-2
6.	Create, open and edit Worksheet.	6.1 Enter and format data in a worksheet. 6.2 Insert and delete cells, rows and columns 6.3 Apply alignment feature on cell	CCH202-3
7.	Formulas and functions in Worksheet.	7.1 Create formula and "IF" condition on cell data 7.2 Apply various functions and named ranges in worksheet.	CCH202-3
8	Sort, Filter and validate data in Spreadsheet.	8.1 Implement data Sorting, Filtering and Data validation features in a worksheet.	CCH202-3
9	Charts for Visual Presentation in Spreadsheet.	9.1 Create charts using various chart options in spreadsheet.	CCH202-3
10	Worksheet Printing.	10.1 Print the worksheet by applying various print options for worksheet	CCH202-3
11	Make Slide Show Presentation.	11.1 Apply design themes to the given presentation 11.2 Insert pictures text/images/shapes in slide 11.3 Use pictures text/images/shapes editing options.	CCH202-4
12		12.1 Add tables and charts in the slides. 12.2 Run slide presentation in	CCH202-4
	Use Tables and Charts in Slide	different modes 12.3 Print slide presentation as handouts/notes	

13	a) Insert Animation effects to Text and Slides. b) Insert Audio and Video files in presentation	13.1 Apply animation effects to the text and slides 13.2 Add/set audio and video files in the presentation.	CCH202-4
14	a) Internet connection configuration b) Use Internet and Web Services.	14.1 Configure internet connection on a computer system 14.2 Use different web services on internet	CCH202-5
15	Working with Browsers.	15.1 Configure different browser settings 15.2 Use browsers for the given purpose	CCH202-5
16	Prepare Web Forms for Survey.	16.1 Create web forms for survey using different options.	CCH202-6
17	Prepare Web Forms for Quiz	17.1 Create web forms for Quiz using different options	CCH202-6

## I. SUGGESTED MICRO PROJECT / ASSIGNMENT/ ACTIVITIES FOR SPECIFIC LEARNING SKILLS DEVELOPMENT (SELF LEARNING)

### Self Learning

Following are some suggestive self-learning topics: 1) Use ChatGPT/any other AI tool to explore information. 2) Use Calendar to Schedule and edit activities. 3) Use Translate app to translate the given content from one language to another. 4) Use cloud based storage drive to store and share your files.

### Assignment

Prepare journal of practical performed in the laboratory.

### Micro project

The microproject has to be industry application based, internet-based, workshop-based, laboratory-based or based as suggested by Teacher. 1) Perform a survey on various input and output devices available in market and its report. 2) Prepare Time Table, Prepare Notes on Technical Topics, Reports, Biodata with covering letter (The teacher shall assign a document to be prepared by each student) 3) Prepare slides with all Presentation features as: classroom presentation, presentation about department, presentation of Technical Topics. (Subject teacher assign a presentation to be prepared by each student). 4) Student Marksheet, Prepare Pay bills, tax statement, assessment record using spreadsheet. (Teacher shall assign a spreadsheet to be prepared by each student). 5) C Survey on different web browsers. 6) Generate resume for different job profile, survey report of any industry ChatGPT/any other AI tool

## J. LABORATORY EQUIPMENT / INSTRUMENTS / TOOLS / SOFTWARE REQUIRED

Sr.No	Equipment Name with Broad Specifications
1	a) Computer System with all necessary Peripherals and Internet connectivity. b) Any Office Software c) Any Browser (Any General Purpose Computer available in the Institute)

## K. CONTENT:

Sr. No.	Topics / Sub-topics	Lectures (Hours)
<b>Course Outcome CCH202-1 - Use computer system and its peripherals for given purpose.</b>		
<b>1</b>	<b>Unit - I Introduction to Computer System</b> 1.1 Basics of Computer System: Overview of Hardware and Software: block diagram of Computer System, Input/Output unit CPU, Control Unit, Arithmetic logic Unit (ALU), Memory Unit 1.2 Internal components: processor, motherboards, random access memory (RAM), read-only memory (ROM), video cards, sound cards and internal hard disk drives) 1.3 External Devices: Types of input/output devices, types of monitors, keyboards, mouse, printers: Dot matrix, Inkjet and LaserJet, plotter and scanner, external storage devices CD/DVD, Hard disk and pen drive 1.4 Application Software: word processing, spreadsheet, database management systems, control software, measuring software, photo-editing software, video-editing software, graphics manipulation software System Software compilers, linkers, device drivers, operating system 1.5 Network environments: network interface cards, hubs, switches, routers and modems, concept of LAN, MAN, WAN, WLAN, Wi-Fi and Bluetooth 1.6 Working with Operating Systems: Create and manage file and folders, Copy a file, renaming and deleting of files and folders, Searching files and folders, application installation, creating shortcut of application on the desktop.	<b>2</b>
<b>Course Outcome CCH202-2 - Prepare Business document using Word Processing Tool.</b>		
<b>2</b>	<b>Word Processing</b> 2.1 Word Processing: Overview of Word processor Basics of Font type, size, colour, Effects like Bold, italic, underline, Subscript and superscript, Case changing options, Previewing a document, Saving a document, Closing a document and exiting application. 2.2 Editing a Document: Navigate through a document, Scroll through text, Insert and delete text, Select text, Undo and redo commands, Use drag and drop to move text, Copy, cut and paste, Use the clipboard, Clear formatting, Format and align text, Formatting 2.3 Changing the Layout of a Document: Adjust page margins, Change page orientation, Create headers and footers, Set and change indentations, Insert and clear tabs 2.4 Inserting Elements to Word Documents: Insert and delete a page break, Insert page numbers, Insert the date and time, Insert	<b>3</b>
Sr. No.	Topics / Sub-topics	Lectures (Hours)

	<p>special characters (symbols), Insert a picture from a file, Resize and reposition a picture</p> <p>2.5 Working with Tables: Insert a table, Convert a table to text, Navigate and select text in a table, Resize table cells, Align text in a table, Format a table, Insert and delete columns and rows, Borders and shading, Repeat table headings on subsequent page</p> <p>working with Columned Layouts and Section Breaks: a Columns, Section breaks, Creating columns, Newsletter style columns, Changing part of a document layout or formatting, Remove section break, Add columns to remainder of a document, Column widths, Adjust</p>	
<b>Course Outcome CCH202-3: Design files of word processors, spreadsheets, presentation software, and database application.</b>		
<b>3</b>	<p><b>Spreadsheets</b></p> <p>3.1 Working with Spreadsheets: Overview of workbook and worksheet, Create Worksheet Entering sample data, Save, Copy Worksheet, Delete Worksheet, Close and open Workbook.</p> <p>3.2 Editing Worksheet: Insert and select data, adjust row height and column width, delete, move data, insert rows and columns, Copy and Paste, Find and Replace, Spell Check, Zoom In-Out, Special Symbols, Insert Comments, Add Text Box, Undo Changes, - Freeze</p> <p>3.3 Formatting Cells and sheet: Setting Cell Type, Setting Fonts, Text options, Rotate Cells, Setting Colors, Text Alignments, Merge and Wrap, apply Borders and Shades, Sheet Options, Adjust Margins, Page Orientation, Header and Footer, Insert Page Breaks, S</p> <p>3.4 Working with Formula: Creating Formulas, Copying Formulas, Common spreadsheet Functions such as sum, average, min, max, date, In, And, or, mathematical functions such as sqrt, power, applying conditions using IF.</p> <p>3.5 Working with Charts: Introduction to charts, overview of different types of charts, Bar, Pie, Line charts, creating and editing charts. Using chart options: chart title, axis title, legend, data labels, Axes, grid lines, moving chart in a separate sheet. Advanced Operations: Conditional Formatting, Data Filtering,</p> <p>Data Sorting, Using Ranges, Data Validation, Adding Graphics, Printing Worksheets, print area, margins, header, footer and other page setup options.</p>	<b>3</b>
<b>Course Outcome CCH202-4 - Prepare professional Slide Show presentations</b>		
<b>4</b>	<p><b>Presentation Tool</b></p> <p>4.1 Creating a Presentation: Outline of an effective presentation, Identify the elements of the User Interface, Starting a New Presentation Files, Creating a Basic Presentation, Working with textboxes, Apply Character Formats, Format Paragraphs, View a Prese</p> <p>4.2 Inserting Media elements: Adding and Modifying Graphical Objects to a Presentation - Insert Images into a Presentation,</p>	<b>4</b>
<b>Sr. No.</b>	<b>Topics / Sub-topics</b>	<b>Lectures (Hours)</b>



	insert audio clips, video/animation, Add Shapes, Add Visual Styles to Text in a Presentation, Edit Graphical Objects on a Slide, Format 4.3 Working with Tables: Insert a Table in a Slide, Format Tables, and Import Tables from Other Office Applications. Working with Charts: Insert Charts in a Slide, Modify Chart, Import Charts from Other Office Applications.	
<b>Course Outcome</b> <b>CCH202-5 - Use different types of Web Browsers and Apps</b> <b>CCH202-6 - Explain concept and applications of Emerging Technologies</b>		
5	<b>Basics of Internet and Emerging Technologies</b> 5.1 World Wide Web: Introduction, Internet, Intranet, Cloud, Web Sites, web pages, URL, web servers, basic settings of web browsers- history, extension, default page, default search engine, creating and retrieving bookmarks, use search engines effectively for 5.2 Web Services: e-Mail, Chat, Video Conferencing, e-learning, e-shopping, e-Reservation, e-Groups, Social Networking 5.3 Emerging Technologies: IOT, AI and ML, Drone Technologies, 3D Printing. Tools: Docs, Drive, forms, quiz, Translate and other Apps	3

## L. ASSESSMENT METHODOLOGIES/TOOLS

### Formative assessment (Assessment for Learning)

- Lab performance, Assignment, Self-learning and Seminar/Presentation

### Summative Assessment (Assessment of Learning)

- Lab. Performance, viva voce

## M. PROGRESSIVE SKILLS TEST :

### Criteria for Continuous Assessment of Practical work and Progressive skill Test:

Sr. no	Criteria	Marks allotted
1	Attendance at regular practical	05
2	Preparedness for practical	02
3	Neat & complete Diagram.	04
4	Observations & computer handling skill	02
5	Use of toolbar, menu bar and short cut keys.	04
6	Logical thinking and approach	04
7	Oral Based on Lab work and completion of task	04
TOTAL		25

Assessment at semester end practical exam as per **Proforma II.**

**Criteria for assessment at semester end practical exam:**

Sr. no	Criteria	Marks allotted
1.	Technical ability	10
2.	Communication skill	5
3.	Logical approach	10
	<b>TOTAL.</b>	<b>25</b>

**N. INSTRUCTIONAL STRATEGIES:**

**Instructional Methods:**

- a. Lectures cum Discussions
- b. Regular Home Assignments.
- c. Laboratory experiences and laboratory interactive sessions

**Teaching and Learning Resources:**

- i. Chalk board
- 2.Slides(PPT)
3. Self-learning Online Tutorials

**O. REFERENCE MATERIAL:**

**a) Books / Codes**

Sr.No	Author	Title	Publisher
1	Goel Anita	Computer Fundamentals	Pearson Education, New Delhi, 2014, ISBN-13: 978-8131733097
2	Miller Michael	Computer Basics Absolute Beginner's Guide, Windows10	QUE Publishing; 8th edition August 2015, ISBN: 978-0789754516
3	Alvaro Felix	Linux: Easy Linux for Beginners	CreatevSpace Independent Publishing Platform- 2016, ISBN-13: 978-1533683731
4	Johnson Steve	Microsoft Office 2010: On Demand	Pearson Education, New Delhi India, 2010. ISBN :9788131770641
5	Schwartz Steve	Microsoft Office 2010 for Windows: Visual Quick Start	Pearson Education, New Delhi India, 2012, ISBN : 9788131766613
6	Leete Gurdy, Finkelstein Ellen, Mary Leete	OpenOffice.org for Dummies	Wiley Publishing, New Delhi, 2003 ISBN : 978-0764542220

### b) Suggested Websites and Portals

Sr.No	Link / Portal	Description
1	<a href="https://www.microsoft.com/en-in/learning/office-training.aspx">https://www.microsoft.com/en-in/learning/office-training.aspx</a>	Office
2	<a href="http://www.tutorialsforopenoffice.org/">http://www.tutorialsforopenoffice.org/</a>	Open Office
3	<a href="https://s3-ap-southeast-1.amazonaws.com/r4ltue295xy0d/Special_Edition_Using_StarOffice_6_0.pdf">https://s3-ap-southeast-1.amazonaws.com/r4ltue295xy0d/Special_Edition_Using_StarOffice_6_0.pdf</a>	Open Office
4	<a href="https://ashishmodi.weebly.com/uploads/1/8/9/7/18970467/computer_fundamental.pdf">https://ashishmodi.weebly.com/uploads/1/8/9/7/18970467/computer_fundamental.pdf</a>	Computer Fundamental
5	<a href="http://www.tutorialsforopenoffice.org/">http://www.tutorialsforopenoffice.org/</a>	Open Office
6	<a href="https://www.tutorialspoint.com/computer_fundamentals/index.htm">https://www.tutorialspoint.com/computer_fundamentals/index.htm</a>	Computer Fundamental
7	<a href="https://www.tutorialspoint.com/word/">https://www.tutorialspoint.com/word/</a>	Word Processing
8	<a href="https://www.javatpoint.com/ms-word-tutorial">https://www.javatpoint.com/ms-word-tutorial</a>	Word Processing
9	<a href="https://support.microsoft.com/en-au/office/word-for-windows-training-7bcd85e6-2c3d-4c3c-a2a5-5ed8847">https://support.microsoft.com/en-au/office/word-for-windows-training-7bcd85e6-2c3d-4c3c-a2a5-5ed8847</a>	Word Processing
10	<a href="https://www.javatpoint.com/excel-tutorial">https://www.javatpoint.com/excel-tutorial</a>	Spreadsheet
11	<a href="https://support.microsoft.com/en-au/office/excel-video-training-9bc05390-e94c-46af-a5b3-d7c22f6990bb">https://support.microsoft.com/en-au/office/excel-video-training-9bc05390-e94c-46af-a5b3-d7c22f6990bb</a>	Spreadsheet
12	<a href="https://www.javatpoint.com/powerpoint-tutorial">https://www.javatpoint.com/powerpoint-tutorial</a>	Powerpoint Presentation
13	<a href="https://support.microsoft.com/en-au/office/powerpoint-for-windows-training-40e8c930-cb0b-40d8-82c4-b">https://support.microsoft.com/en-au/office/powerpoint-for-windows-training-40e8c930-cb0b-40d8-82c4-b</a>	Powerpoint Presentation
14	<a href="https://www.geeksforgeeks.org/ms-dos-operating-system/">https://www.geeksforgeeks.org/ms-dos-operating-system/</a>	Operating System
15	<a href="https://www.javatpoint.com/windows">https://www.javatpoint.com/windows</a>	Windows Operating System
16	<a href="https://www.javatpoint.com/what-is-linux">https://www.javatpoint.com/what-is-linux</a>	Linux Operating System
17	<a href="https://www.techtarget.com/iotagenda/definition/Internet-of-Things-IoT">https://www.techtarget.com/iotagenda/definition/Internet-of-Things-IoT</a>	IoT
18	<a href="https://www.geeksforgeeks.org/introduction-to-internet-of-things-iot-set-1/">https://www.geeksforgeeks.org/introduction-to-internet-of-things-iot-set-1/</a>	IoT

19	<a href="https://www.javatpoint.com/machine-learning">https://www.javatpoint.com/machine-learning</a>	AI & Machine Learning
<b>Sr.No</b>	<b>Link / Portal</b>	<b>Description</b>
20	<a href="https://www.skillrary.com/blogs/read/introduction-to-drone-technology">https://www.skillrary.com/blogs/read/introduction-to-drone-technology</a>	Drone Technology
21	<a href="https://www.cnet.com/tech/computing/what-is-3d-printing/">https://www.cnet.com/tech/computing/what-is-3d-printing/</a>	3D Printing
22	<a href="https://support.google.com/a/users/answer/9389764?hl=en">https://support.google.com/a/users/answer/9389764?hl=en</a>	Apps

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**COURSE ID:**

**Course Name : YOGA & MEDITATION**  
**Course Code : CCH203**  
**Course Abbreviation : HYAM**

**TEACHING SCHEME:**

**Pre-requisite Course(s) : <nil >**

**Teaching Scheme:**

Scheme component	Hours / week	Credits
Theory	NIL	
Practical	01	

**I. LABORATORY LEARNING OUTCOME AND ALIGNED PRACTICAL / TUTORIAL EXPERIENCES.**

Practical / Tutorial / Laboratory Learning Outcome (LLO)	Sr No	Laboratory Experiment / Practical Titles / Tutorial Titles	Number of hrs.	RelevantCOs
LLO 1.1 Practice warming up for Yoga.	1	Introduction :- Presentations on Introduction to Yoga and its History. Omkar Chanting, prayer, padmasan, siddhasan, Vajrasan. Lab Exp: 1. Perform warming up exercises to prepare the body from head to toe for Yoga. i. Neck movement, ii. Shoulder movement, iii. Trunk movement, iv. Knee movement, v. Ankle movement.	3	CO1
LLO 2.1 Practice Sun salutaion	2	Lab Exp: 2. Perform all the postures of Sun salutation- one by one in a very slow pace, after warm up. Lab Exp 3. Perform multiple Surya Namaskar ( Starting with three and gradually increasing it to twelve) in one go. Experiment 2 to 4 must be followed by Shavasana for self relaxation.	4	CO1 CO2

Practical / Tutorial / Laboratory Learning Outcome (LLO)	Sr No	Laboratory Experiment / Practical Titles / Tutorial Titles	Number of hrs.	RelevantCOs

LLO 3.1 Practice basic Asanas	3	Lab Exp: 4 Perform Sarvangasana, Halasana, Kandharasaa (setubandhasana), uttanpadasana, pavan muktasan. Lab Exp: 5 Perform Bhujangasana, Naukasana, Mandukasan Lab Exp: 6 Perform Shalabhasana, Dhanurasana, Vakrasana, Gomukhasana, Paschimottasana, Ardhamachendrasana. Lab Exp: 7 Perform Veerasana Veerbhadrasana, Vrukshasana, Trikonasana. Follow up experiment 5 to 7 with shavasana for self relaxation	4	CO2
LLO 4.1 Practice basic pranayama	4	Lab Exp: 8 Perform Deep breathing, Anulom Vilom Pranayama Kriya Lab Exp: 9 Practice Kapalbhatai Pranayama Kriya, Bhastrika Lab Exp: 10 Practice Bhramary and sheetali Pranayama.	2	CO3
LLO 5.1 Practice meditation	5	Lab Exp: 11 Perform sitting in Dhyana Mudra and meditating. Start with five minute and slowly increasing to higher durations. Introduction to Vipasshana, Anapan and Chakras. (Trainer will explain the benefits of Meditation before practice)	2	CO3
<b>Note :</b> Note: 1. Start and end of each session can be with appropriate Yoga prayers and chanting of Omkar. 2. Trainers can add similar asanas in practical sessions. 3. Students are to be instructed to practice the experiment performed at least twice a week as part of self learning practices. 4. Live demonstration by the trainer needs to be carried out during practical hours. Yogic Videos can be used as well .				

**SUGGESTED MICRO PROJECT / ASSIGNMENT/ ACTIVITIES FOR SPECIFIC LEARNING / SKILLS DEVELOPMENT (SELF LEARNING)**

**Micro project**

- i. Maintain a diary indicating date wise practice done by the student with a photograph of self in yogic posture.
- ii. Write up details any five asans illustrating steps to perform, posture image, benefits and precautions.

**Assignment**

Prepare Diet and nutrition chart for self

**Self Learning and Practice**

- Practice at least thrice a week.
- Read books on different methods to maintain health, wellness and to enhance mood
- Watch videos on Yoga Practices.





# SEMESTER II COURSES



**COURSE ID :**

**COURSE NAME : APPLIED MATHEMATICS**

**COURSE CODE : CCH301**

**COURSE ABBREVIATION : HAMT**

### A. LEARNING SCHEME:

Scheme component		Hours	Credits
Actual Contact Hours / week	Classroom Learning	04	3
	Tutorial Learning	02	
	Laboratory Learning	-	
	SLH-Self Learning	00	
	NLH-Notional Learning	06	

### B. ASSESSMENT SCHEME :-

PAPER DURAT ION IN HRS	THEORY				BASED ON LL&TL				BASED ON SLA		TOTAL
					Tutorial						100
	FA-TH	SA-TH	TOTAL		FA -PR		SA-PR		MAX	MIN	
MAX	MAX	MAX	MIN	MAX	MIN	MAX	MIN	MAX	MIN		
03	30	70	100	40	--	--	--	--	--	--	

**(Total IKS Hrs for Sem.: 02 Hrs)**

**C: ABBREVIATIONS:-**CL-ClassRoom Learning, TL-Tutorial Learning, LL-Laboratory Learning, SLH-Self Learning Hours, NLH-Notional Learning Hours, FA - Formative Assessment, SA -Summative assessment, IKS - Indian Knowledge System, SLA -Self Learning Assessment

**Legends:**@Internal Assessment, #External Assessment, \*#OnLine Examination, @\$Internal Online Examination( TNR 12 font)

1. FA-TH represents average of two class tests of 30 marks each conducted during the semester.
2. If candidate is not securing minimum passing marks in FA-PR of any course then the candidate shall be declared as "Detained" in that semester.
3. If candidate is not securing minimum passing marks in SLA of any course then the candidate shall be declared as fail and will have to repeat and resubmit SLA work.
4. Notional Learning hours for the semester are (CL+LL+TL+SL) hrs.\*15Weeks
5. 1(one) credit is equivalent to 30 Notional hrs.
6. \*Self learning hours shall not be reflected in the Time Table.  
\*Self learning includes micro project /assignment/other activities. (The list of all assignments are given in tabular format. At least 6 to 8 assignments to be given)

### D. i) RATIONALE:-

Mathematics is an important pre-requisite for the development and understanding of engineering and technological concepts. For an engineer and technologist, knowledge of Mathematics is an effective tool to pursue and to master the applications in the engineering and technological fields. Applied mathematics is designed for its applications in engineering and technology. It includes integration, differential equation,. The connection between applied mathematics and its applications in real life can be understood and appreciated. Integral calculus helps in finding the area . Differential equation is used in finding curve, rectilinear motion. Statistics and probability will help a student to analyze data of large volume in their higher studies. The fundamentals of these topics are directly useful in understanding engineering applications in various fields.

### ii) Competency:

The course should be taught and implemented with the aim to develop the course outcomes (CO's) for the student to acquire the competency needed to apply the mathematical techniques for engineering subjects.

- 1.Cognitive** : Understanding and applying principles of mathematics to engineering problems
- 2. Psychomotor:** To prepare charts displaying the area of irregular shapes using the concept of integration,prepare charts to displaying grouped and ungrouped data .
- 3. Affective** : discipline, consistency, hard work , to concentrate ,accuracy, punctuality, aesthetics

### E. COURSE LEVEL LEARNING OUTCOMES (COS)(TNR 14)

CCH301-1 : To solve examples on integration using various techniques

CCH301-2 : To solve Differential equation of first order and first degree by various methods

CCH301-3 : To find approximate solution of algebraic equations and simultaneous equations by various methods.

CCH301-4:- To solve problems on Probability distributions

CCH301-5 :- Solve examples on Laplace Transform

### Competency, course outcomes and programme outcomes/programme specific outcomes (cp-co-po/pso) matrix

[ Note : Correlation levels : 1: Slight (Low), 2: Moderate (Medium), 3: Substantial (High), "0"

Competency and Cos	Programme Outcomes POs and PSOs								
	PO 1 Basic and Discipline specific knowledge	PO 2 Problem Analysis	PO 3 Design / Development of solutions	PO 4 Engineering Tools, Experimentation and Testing	PO 5 Engineering Practices for society, sustainability and Environment	PO 6 Project Management	PO 7 Life-long Learning	PSO1 Maintain various types of electrical equipments	PSO2 Maintain various sections of electrical power systems
Competency: Use DC machines and transformers.	3	2	1	-	1	-	2		
CCH301-1-CO-1 : To solve examples	3	1	-	-	1	-	1		

Competency and Cos	Programme Outcomes POs and PSOs								
	PO 1 Basic and Discipline specific knowledge	PO 2 Problem Analysis	PO 3 Design / Development of solutions	PO 4 Engineering Tools, Experimentation and Testing	PO 5 Engineering Practices for society, sustainability and Environment	PO 6 Project Management	PO 7 Life-long Learning	PSO1 Maintain various types of electrical equipments	PSO2 Maintain various sections of electrical power systems
on integration using various techniques									
CCH301-2-CO-2 : To solve Differential equation of first order and first degree by various methods	3	1	1	1	1	1	1		
CCH301-3-CO-3 : To find approximate solution of algebraic equations and simultaneous equations by various methods.	2	3	1	1	1	1	1		
CCH301-4-CO-4:- To solve problems on Probability distributions	2	2	2	2	2	1	2		
CCH301-5-CO-5:- Solve examples on Laplace Transform	2	1	1	1	1	1	1		

## F. CONTENT:

### III) Tutorial exercises

Any **TEN** of the following Tutorial exercises shall be conducted in the Tutorial room in tutorial sessions of batches of about 20- 22 students:

Sr. no	Tutorial experiences	CO
1	Solve simple problems of Integration by substitution.	CCH301-1
2	Solve integration using by parts.	CCH301-1
3	Solve examples on Definite Integral based on given methods.	CCH301-1
4	Solve problems on properties of definite integral.	CCH301-1
5	Solve given problems for finding the area under the curve and area between two curves . (Only for civil and mechanical engg. group)	CCH301-1
6	Solve examples on mean value and root mean square value.(only for Computer, Electrical and Electronics engg. group)	CCH301-1
7	Solve first order first degree differential equation using variable separable method.	CCH301-2
8	Solve first order first degree differential equation using exact differential equation and linear differential equation.	CCH301-2
9	Solve engineering application problems using differential equation.	CCH301-2
10	Solve problems on Bisection method, Regula falsi and Newton-Raphson method.	CCH301-3
11	Solve problems on Jacobi's method and Gauss Seidel method.	CCH301-3
12	Use Bakshali iterative methods for finding approximate value of square root.(IKS)	CCH301-3
13	Solve engineering problems using Binomial Distribution, Poisson Distribution and Normal Distribution.	CCH301-4
14	Solve problems on Laplace transform and properties of Laplace transform.	CCH301-5
15	Solve problems on Inverse Laplace transform and properties of Inverse Laplace transform.	CCH301-5

## G. Theory

### Section I

Sr. no.	Topics/Subtopics	Learning (Hours)	Classroom learning evaluation Marks
CO: CCH301-1 : To solve examples on integration using various techniques.			
<b>Unit 1 Indefinite Integration</b>	<b>Indefinite Integration</b> 1.1 Definition, Standard formulae 1.2 Rules of Integration (without proof), Examples 1.3 Integration by substitution 1.4 Integration by parts 1.5 Integration by partial fractions (only linear non repeated factors at denominator of proper fraction)	14	16
CO: CCH301-1 : To solve examples on integration using various techniques			
<b>Unit 2 Definite Integration</b>	<b>Definite Integration</b> 2.1 Definition, Examples 2.2 Properties of Definite Integration ( without proof), Examples based on properties	8	8
CO: CCH301-2 : To solve Differential equation of first order and first degree by various methods			
<b>Unit 3 Differential equation</b>	<b>Differential equation</b> 4.1 Definition of differential equation 4.2 Order & degree of Differential equations 4.3 Methods of solving Differential equations of first order & first degree of following types: 4.3.1 Variable separable form 4.3.2 Exact Differential equations 4.3.3 Linear Differential Equations	8	10

### Section –II

Sr. no.	Topics/Subtopics	Learning Hours	Classroom learning evaluation Marks
CO: CCH301-3 :- To find approximate solution of algebraic equations and simultaneous equations by various methods.			
<b>Unit 4 Numerical Methods</b>	<b>Numerical Methods</b> 4.1 Numerical solution of Algebraic Equations 4.1.1 Bisection Method 4.1.2 Regula- Falsi Method 4.1.3 Newton –Raphson method. 4.2 Numerical solution to simultaneous equations 4.2.1 Jacobi's Method 4.2.2 Gauss-Seidel method <b>Bakhshali iterative method for finding approximate square</b>	10	14

	root.(IKS)		
CO: CCH301-4:- To solve problems on Probability distributions			
<b>Unit 5</b> Probability Distribution	<b>Probability Distribution</b> 5.1 Binomial distribution 5.2 Poisson's distribution 5.3 Normal distribution	8	8
CO: CCH301-5:- Solve examples on Laplace Transform			
<b>Unit 6</b> <b>Laplace Transform</b>	<b>Laplace Transform</b> 6.1 Definition , Linearity property 6.2 Laplace Transforms of Standard functions (without proof) and examples 6.3 First shifting property and examples 6.4 Examples on Multiplication by $t^n$ 6.5 Inverse Laplace Transform, Definition 6.6 Standard formulae(without proof) and examples 6.7 Inverse L.T.by using First shifting property 6.8 Inverse L.T. by using Partial fraction method	12	14

\*\* No questions will be asked on IKS related subtopics in any question paper

## H. Specification table for setting question paper for semester end theory examination

Section / Topic no.	Name of topic	Distribution of marks (level wise)			Total marks	CO
		Remember	Understand	Apply		
I / 1	Indefinite Integration	4	6	6	16	CCH301-1
I / 2	Definite Integration	-	4	4	8	CCH301-1
I / 3	Differential equation	2	4	4	10	CCH301-2
II /4	Numerical Methods	2	4	8	14	CCH301-3
II /5	Probability Distribution	-	4	4	8	CCH301-4
II/6	Laplace Transform	2	6	6	14	CCH301-5
Total Marks					70	

## I. Assessment Criteria

### i) Formative Assessment (Assessment for Learning)

- Tests

### ii) Summative Assessment (Assessment of Learning)

- End term exam

## J. Instructional Methods:

10. Lectures cum Demonstrations
11. Classroom practices
12. Use of projector and soft material for demonstration
13. Use of softwares such as Geogebra



## K. Teaching and Learning resources:

Chalk board, LCD presentations, Demonstrative kits, Demonstrative charts.

## L. Reference Books:

S.N.	Name of Book	Author	Publication
14	Higher Engineering Mathematics	Grewal B.S.	Khanna publication New Delhi, 2013 ISBN: 8174091955
15	A textbook of Engineering Mathematics	Dutta.D.	New age publication New Delhi, 2006 ISBN: 978-81-224-1689-3
16	Advance Engineering Mathematics	Kreyszig, Ervin	Wiley publication New Delhi, 2016 ISBN: 978-81-265-5423-2
17	Advance Engineering Mathematics	Das H.K.	S Chand publication New Delhi, 2008 ISBN: 978-81-219-0345-5
18	Introductory Methods of Numerical Analysis	S.S.Sastry	PHI Learning Private Limited, New Delhi. ISBN: 978-81-203-4592-8
19	Studies in the History of Indian Mathematics	C.S.Seshadri	Hindustan Book Agency (India) P 19 Green Park Extension New Delhi. ISBN 978-93-80250-06-9
20	Calculus & Its Applications	Marvin L. Bittinger David J. Ellenbogen Scott A. Surgent	Addison-Wesley 10 <sup>th</sup> Edition ISBN-13: 978-0-321-69433-1
21	An Introduction to Statistical Learning with Application in R	Gareth James, Hastie Robert & Tibshirani	Springer New York Heidelberg Dordrecht London ISBN: 978-1-4614-7138-7 (eBook)

## M. Learning Website & Software

- a) <http://nptel.ac.in/courses/106102064/1>
- b) <https://www.woframalpha.com/>
- c) <http://www.sosmath.com/>
- d) <http://mathworld.wolfram.com>
- e) <https://www.brilliant.org/>
- f) <https://ocw.mit.edu/index.htm>

**COURSE ID :**

**COURSE NAME : ENGINEERING CHEMISTRY.**

**COURSE CODE : CCH 103**

**COURSE ABBREVIATION : HCHA**

### A. LEARNING SCHEME:

Scheme component		Hours	Credits
Actual Contact Hours / week	Classroom Learning	04	4
	Tutorial Learning	00	
	Laboratory Learning	02	
	SLH-Self Learning	02	
	NLH-Notional Learning	08	

### B. ASSESSMENT SCHEME :-

PAPER DURATION IN HRS	THEORY				BASED ON LL&TL				BASED ON SLA		TOTAL
					Practical						175
	FA-TH	SA-TH	TOTAL		FA -PR		SA-PR				
	MAX	MAX	MAX	MIN	MAX	MIN	MAX	MIN	MAX	MIN	
1.5	30 *#	70*#	100	40	25	10	25 @	10	25	10	

**(Total IKS Hrs for Sem. : 04 Hrs)**

### C. ABBREVIATIONS:-

CL- Class Room Learning , TL- Tutorial Learning, LL-Laboratory Learning, SLH-Self Learning Hours, NLH-Notional Learning Hours, FA - Formative Assessment, SA -Summative assessment, IKS - Indian Knowledge System, SLA - Self Learning Assessment

**Legends:** @ Internal Assessment, # External Assessment, \*# On Line Examination,  
@\$ Internal Online Examination.( TNR 12 font)

1. FA-TH represents average of two class tests of 30 marks each conducted during the semester.
2. If candidate is not securing minimum passing marks in FA-PR of any course then the candidate shall be declared as "Detained" in that semester.
3. If candidate is not securing minimum passing marks in SLA of any course then the candidate shall be declared as fail and will have to repeat and resubmit SLA work.
4. Notional Learning hours for the semester are (CL+LL+TL+SL) hrs.\* 15 Weeks
5. 1(one) credit is equivalent to 30 Notional hrs.
6. \* Self learning hours shall not be reflected in the Time Table.

\* Self learning includes micro project / assignment / other activities. (Provide list of all assignments here in tabular format At least 6 to 8 assignments to be given)

### **D. i) RATIONALE:-**

Basic science such as Chemistry is the fundamental of Engineering & technology. It is most essential to learn the basic science to understand the fundamental concepts in Engineering & technology. Engineering chemistry deals with the study of structure, composition & properties of the materials, which form the core of the fundamental science. Many processes are based on principle of Chemistry in various industries. Topics such as Water, Electrochemistry, Corrosion, & protection of metals from corrosion are some of the direct applications of chemistry in engineering. Hence, the knowledge of chemistry is essential to the aspiring engineers of all branches in their field. Engineering materials like Steel, Rubber, Plastic, Thermocole, Glass wool, Paints, Lubricants are the backbone of various industries, machines, equipment & processes.

### **ii) INDUSTRY / EMPLOYER EXPECTED OUTCOME**

Apply principles of advanced chemistry to solve engineering problems.

**Cognitive:** Understanding concepts of chemistry for applications in the area of engineering.

**Psychomotor:**

- i) Sketching and labeling the diagrams for extraction of copper
- ii) Experimentally analyzing the water samples for preparing portable water by different methods.
- iii) Preparing chart of showing percentage, composition, properties and industrial applications of solders.
- iv) Handling & use of glassware & chemicals.

**Affective:** i) Accuracy ii) Safety iii) Punctuality iv. Attitude.

### **E. COURSE LEVEL LEARNING OUTCOMES (COS)**

**CCH103-1** Apply the basic knowledge of atom, molecules and compounds in Engineering Chemistry.

**CCH103-2** Apply the concepts of Electrochemistry to interpret the reasons of corrosion with its remedies.

**CCH103-3** Select the relevant catalyst, insulators, adhesives, composite materials, plastic and rubber for different applications in the field of engineering.

**CCH103-4** Use of water in Domestic purpose, Industrial purpose and its relevant treatment to solve industrial problems.

**CCH103-5** Explain the method of Extraction of Copper and select proper types of alloys, solders for various purposes.

**CCH103-6** Apply the basic knowledge of Cells and Batteries in Industrial applications.

**Competency, course outcomes and programme outcomes/programme specific outcomes  
(cp-co-po/ps) matrix**

[ Note : Correlation levels : 1: Slight (Low), 2: Moderate (Medium), 3: Substantial (High), “0”

Competency and Cos	Programme Outcomes POs and PSOs								
	PO 1 Basic and Discipline specific knowledge	PO 2 Problem Analysis	PO 3 Design / Development of solutions	PO 4 Engineering Tools, Experimentation and Testing	PO 5 Engineering Practices for society, sustainability and Environment	PO 6 Project Management	PO 7 Life- long Learning	PSO1	PSO2
CCH103-1 CO-1 Apply the basic knowledge of atom, molecules and compounds in Engineering Chemistry.	3.0	2.0	-	1.0	3.0	1.0	3.0	1.0	1.0
CCH103 -2 CO-2 Apply the concepts of Electrochemistry to interpret the reasons of corrosion with its remedies.	3.0	2.0	-	1.0	2.0	1.0	3.0	-	-
CCH103 -3 CO-3 Select the relevant catalyst, insulators, adhesives, composite materials, plastic and rubber for different applications in the field of engineering.	3.0	1.0	-	-	2.0	1.0	3.0	-	-
CCH103 – 4 CO-4 Use of water in Domestic purpose, Industrial purpose and its relevant treatment to solve industrial problems.	3.0	2.0	-	1.0	3.0	1.0	3.0	-	-
CCH103-5 CO-5 Explain the method of Extraction of Copper and select proper types of alloys, solders for various purposes.	3.0	1.0	-	-	2.0	1.0	3.0	-	-
CCH103- 6 CO-6 Apply the basic knowledge of cells and Batteries in Industrial applications.	3.0	2.0	-	1.0	2.0	1.0	3.0	-	-

## F. CONTENT:

### A. Practical exercises

The following practical exercises shall be conducted in the *Laboratory for Engineering Chemistry developed* by the Institute in practical sessions of batches of about 20- 22 students:

Sr. no	Laboratory experiences	CO
1	Introduction to Chemistry laboratory	CCH103-1
2	Volumetric analysis of solution.	CCH103-1
3	Preparation of 1 N, 0.5 N & 0.1 N Solutions of different chemicals like NaOH, HCl, Oxalic acid, FeSO <sub>4</sub> , etc.	CCH103-1
4	Titration of strong acid and strong bases ( HCl X NaOH)	CCH103-1
5	Double titration of strong acid, strong base & weak acid (HCl X NaOH X H <sub>2</sub> C <sub>2</sub> O <sub>4</sub> .H <sub>2</sub> O)	CCH103-1
6	Titration of weak base , strong acid & strong base (Na <sub>2</sub> CO <sub>3</sub> X H <sub>2</sub> SO <sub>4</sub> X KOH)	CCH103-1
7	Estimation of chloride content in water by Mohr' s method	CCH103-4
8	Determination of amount of Ca and Mg ions present in given sample of water by E.D.T.A method	CCH103-4
9	Estimation of viscosity of oils/solutions by Ostwald's method	CCH103-1
10	Estimation of Ca in limestone.	CCH103-4
11	Titration of KMnO <sub>4</sub> & FeSO <sub>4</sub> (Redox titration)	CCH103-1
12	Estimation of % of Fe in given sample of steel.	CCH103-1
13	Determination of alkalinity of water.	CCH103-4
14	Determination of Electrochemical equivalent (ECE) by copper volt meter.	CCH103-2
15	To estimate volumetrically the percentage of copper in a given sample of Brass.	CCH103- 5
16	To demonstrate the different types of Solders.	CCH103-5

## II. Theory

### Section I

Sr. no.	Topics/Subtopics	Learning (Hours)	Classroom learning evaluation Marks
CO - CCH103-1 Apply the basic knowledge of atom, molecules and compounds in Engineering Chemistry.			
1	<b>ATOMIC STRUCTURE AND CHEMICAL BONDING</b> 1.1 Philosophy of atom by Acharya Kanad. 1.2 Atom, Fundamental particles, Nature of atom. 1.3 Atomic Number, Mass Number, Isotopes and isobars. 1.4 Bohr's theory of atom. 1.5 Statement of Aufbau's principle, Hund's rule of maximum multiplicity, Pauli's exclusion principle. 1.6 Lewis and Langmuir's concept of stable electronic configuration.	07	08

Sr. no.	Topics/Subtopics	Learning (Hours)	Classroom learning evaluation Marks
	1.7 Electrovalency and Co-valency. 1.8 Formation Of electrovalent compounds- NaCl, CaCl <sub>2</sub> . 1.9 Formation of Covalent compounds- H <sub>2</sub> O, CO <sub>2</sub> .		
CO - CCH103-2 Apply the concepts of Electrochemistry to interpret the reasons of corrosion with its remedies.			
2	<b>ELECTROCHEMISTRY AND CORROSION.</b> 2.1 Definitions- Cathode, Anode, Conductor, Electrolyte, Electrode, Ionisation, Electrolysis. 2.2 Arrhenius Theory Of Ionisation. 2.3 Degree of Ionisation & Factors affecting degree of ionisation. 2.4 Statement of Faraday's first and second law of electrolysis. 2.5 Relation between CE and ECE. 2.6 Electrolysis of molten NaCl. 2.7 Electrolysis of CuSO <sub>4</sub> solution by using Cu-Electrodes. 2.8 Industrial applications of electrolysis. 2.8.1 Electroplating. 2.8.2 Electro refining of Cu. 2.9 Definition & types of corrosion. 2.10 Dry or Atmospheric corrosion , Oxide Film Formation & its types, Factors affecting atmospheric corrosion. 2.11 Wet or electrochemical corrosion 2.12 Factors influencing immersed corrosion 2.13 Methods of protection of metal from corrosion - Hot dipping (Galvanizing & Tinning) ,Metal spraying, Metal cladding, Cementation or sherardizing.	10	10
CO - CCH103-3 Select the relevant catalyst, insulators, adhesives, composite materials, plastic and rubber for different applications in the field of engineering.			
3	<b>CHEMISTRY OF ENGINEERING MATERIALS AND CATALYSIS.</b>  <b>3.1 INSULATORS</b>  3.1.1 Definition & Characteristics of insulator. 3.1.2 Preparation, properties & uses of Glass wool, Thermocole.  <b>3.2 COMPOSITE MATERIALS</b> 3.2.1 Definition. 3.2.2 Classification, Properties & Application of composite materials.  <b>3.3 PLASTICS</b>	13	16

Sr. no.	Topics/Subtopics	Learning (Hours)	Classroom learning evaluation Marks
	3.3.1 Definition of Polymer, Polymerization. 3.3.2 Types of polymerization – Addition & Condensation polymerization. 3.3.3 Classification of plastic - Thermosoftening & Thermosetting plastic. 3.3.4 Engineering properties & applications of plastic. <b>3.4 RUBBER</b> 3.4.1 Elastomer 3.4.2 Drawbacks of Natural rubber. 3.4.3 Vulcanization of rubber. 3.4.4 Engineering properties & uses of rubber. <b>3.5 ADHESIVES</b> 3.5.1 Definition of adhesives. 3.5.2 Characteristics of good adhesive. 3.5.3 Properties of adhesive. <b>3.6 CATALYSIS</b> 3.6.1 Definition. 3.6.2 Types of Catalyst with example. - Positive catalyst - Negative catalyst 3.6.3 Types of Catalysis. - Homogeneous catalysis. - Heterogeneous catalysis 3.6.4 Catalytic Promoters. 3.6.4 Catalytic Inhibitors 3.6.5 Autocatalysis.		

## Section –II

Sr. no.	Topics/Subtopics	Learning (Hours)	Classroom learning evaluation Marks
CO - CCH103-4 Use of water in Domestic purpose, Industrial purpose and its relevant treatment to solve industrial problems.			
4	<b>WATER</b> 4.1 Impurities in natural water. 4.2 Hard water & Soft water. 4.3 Hardness of water- Temporary & Permanent. 4.4 Reactions of hard water with soap. 4.5 Disadvantages of hard water for domestic & Industrial purpose - Textile Industry, Sugar Industry, Paper Industry Dying Industry. 4.6 Sterilization of water - Chlorination –by Cl <sub>2</sub> , bleaching powder, Chloramines with chemical reactions. 4.7 Ion Exchange method to remove total hardness of	09	12

	Water.		
CO - CCH103-5 Explain the method of Extraction of Copper and select proper types of alloys, solders for various purposes.			
5	<b>METALLIC CONDUCTORS AND SOLDERS</b> <b>5.1 METALLIC CONDUCTORS</b> 5.1.1 Occurrence of metals 5.1.2 Distinction between mineral & ore 5.1.3 Definition of flux, Gangue & Slag 5.1.4 Steps involved in metallurgy-Flow chart Concentration of ores— A) Physical Methods 1. Gravity Separation Method 2. Electromagnetic separation 3. Froth floatation method B) Chemical Methods 1. Calcination 2. Roasting 5.1.6 Important ores of copper Metallurgy of copper-Extraction of copper from copper pyrites by concentration, roasting, smelting, Bessemerisation, Electrorefining. 5.1.7 Physical properties & uses of Copper.  <b>5.2 SOLDERS</b> 5.2.1 Definition of alloy, classification of alloys & purposes of making alloy. 5.2.2 Composition, properties & applications of Soft solder. A) Tinmann's solder, B) Brazing alloy , C) Plumber's solder D) Rose metal E) Woods metal	14	16
CO - CCH103-6 Apply the basic knowledge of Cells and Batteries in Industrial applications.			
6	<b>CELL AND BATTERIES</b> 5.1 Definition of Electrochemical cell, Battery, Charge, Discharge, Closed Circuit Voltage, Electrochemical couple, Internal resistance, Open Circuit Voltage, Separator, E.M.F. 5.2 Classification of Batteries such as – Primary & Secondary Batteries 5.3 Construction, Working and Applications of a Primary Cell such as Dry Cell , Secondary Cell such as Lead Acid Storage Cell 5.4 Charging and Discharging of Lead Acid Storage Cell 5.5 Hydrogen-Oxygen fuel cell, its chemical reactions & advantages 5.6 Introduction of solar cell	07	08

\*\* No questions will be asked on IKS learning subtopics in any question papers.



## G. List of Assignments under SLA (25 marks)

**\*\* From the above any two assignments to be completed by the students.**

Sr.No	List of Assignment (under SLA) (Any one of the following)	Hrs Allotted
1	Prepare distinguish chart for Isotopes & Isobars, Electrovalent & Covalent bond	02
2	Prepare Charts of Bohr's Theory, Lewis & Langmuir's theory.	02
3	Faraday's First & Second law statements & formula.	02
4	Electroplating & Electrefining with diagram	02
5	Note on corrosion due to Oxygen & its types	02
6	With neat labelled diagram explain the process of 1. Galvanizing, 2. Tinning, 3. Metal spraying, 4. Metal Cladding, 5. Sherardizing	02
7	Properties of Plastics, rubber, insulator, composite materials & adhesives.	02
8	Uses/Applications of Plastics, rubber, insulator, composite materials & adhesives.	02
9	Draw diagram of Ion Exchange method	02
10	Note on Impurities present in Natural Water.	02
11	Disadvantages of hard water in Domestic purposes	02
12	Disadvantages of hard water in Industrial purposes	02
13	Flow chart of Metallurgical processes	02
14	With neat labelled diagram explain 1. Gravity separation method. 2. Electromagnetic separation method. 3. Froth floatation method.	02
15	Distinguish between Calcination & Roasting	02
16	Smelting process of Copper with diagram	02
17	Bessemerisation of Copper with diagram	02
18	Physical properties & uses of copper.	02
19	Definition & classification of alloys	02
20	Purposes of making of alloys	02
21	Composition, properties & applications of 1. Soft solder, 2. Tinmann's solder, 3. Brazing alloy, 4. Plumber's solder, 5. Rose metal, 6. Wood's metal	02
22	Definitions of Electrochemical cell, Battery, Charge, Discharge, Closed circuit voltage, Open circuit voltage, Electrochemical couple, internal resistance, Separator, EMF.	02
23	Distinguish between Primary & Secondary batteries	02
24	Construction of Dry cell	02
25	Working & applications of Dry cell	02
26	Construction of Lead acid storage cell	02
27	Working & applications of Lead acid storage cell	02
28	Construction of H <sub>2</sub> -O <sub>2</sub> fuel cell with Chemical reactions & advantages	02
29	Construction & working of solar cell	02

## H. Specification table for setting question paper for semester end theory Examination.

Section / Topic no.	Name of topic	Distribution of marks (level wise)			Total marks	CO
		Remember	Understand	Apply		
I / 1	Atomic Structure and Chemical Bonding	4	2	2	08	CCH103-1
I / 2	Electrochemistry & Corrosion	4	4	2	10	CCH103-2
I / 3	Chemistry of Engineering materials & catalysis	6	6	4	16	CCH103-3
II / 4	Water	4	4	4	12	CCH103-4
II / 5	Metallic conductors & solders	6	6	4	16	CCH103-5
II / 6	Cell & Batteries	4	2	2	8	CCH103-6
	Total Marks				70	

## I. Assessment Criteria

### i) Formative Assessment of Practical / Self learning assessment :-

Every assignment shall be assessed for 25 marks as per following criteria:

Domain	Particulars	Marks out of 25
Cognitive	Understanding	05
	Application	05
Psychomotor	Operating Skills	05
	Drawing / drafting skills	05
Affective	Discipline and punctuality	05
<b>TOTAL</b>		<b>25</b>

### ii) Summative Assessment of Practical :-

Every practical assignment shall be assessed for 25 marks as per following criteria:

Sr. no	Criteria	Marks allotted
1	Attendance at regular practical	05
2	Preparedness for practical	05
3	Neat & complete Diagram.	05
4	Observations & handling of instrument.	05
5	Oral Based on Lab work and completion of task	05
<b>TOTAL</b>		<b>25</b>

### J. Instructional Methods:

14. Lectures cum Demonstrations,
15. Class room practices.
16. Use of projector and soft material for demonstration
4. Charts
5. Simulation videos

### K. Teaching and Learning resources:-

Chalk board, LCD presentations, Demonstrative kits, Demonstrative charts.

### L. Reference Books:

Sr. No.	Author	Title	Publisher
1.	Jain & Jain	Engineering chemistry	Dhanpatrai publishing co.
2.	S. C. Rangawala	Engineering materials	Engineering publication
3.	Jain & Agarwal	Metallurgical Analysis	Agarwal publications
4.	O. P. Khanna	Material science & technology	Khanna publication on 2006
5.	Rollason	Metallurgy for Engineers	ASM publication
6.	J. C. Kuriacose	Chemistry in Engineering & Vol. 1 & 11	-
7.	P. C. Jain	Chemistry of Engineering Materials	-
8.	S. S. Dara	A text of Engineering Chemistry	-
9.	R.Gopalan, D.Venkappa	Engineering Chemistry	Vikas Publishing House.

### M. Learning Website & Software

- a. [www.substech.com](http://www.substech.com)
- b. [www.kentchemistry.com](http://www.kentchemistry.com)
- c. [www.chemcollective.org](http://www.chemcollective.org)
- d. [www.wqa.org](http://www.wqa.org)
- e. [www.chemistryteaching.com](http://www.chemistryteaching.com)
- f. [www.ancient-origins.net/hisotry-famous-people/indian-sage-acharya-kanad-001399](http://www.ancient-origins.net/hisotry-famous-people/indian-sage-acharya-kanad-001399)

**COURSE ID:**

**COURSE NAME : COMMUNICATION SKILLS**  
**COURSE CODE : CCH201**  
**COURSE ABBREVIATION : HCMS**

**A. LEARNING SCHEME:**

Scheme component		Hours	Credits
Actual Contact Hours / week	Classroom Learning	04	4
	Tutorial Learning	00	
	Laboratory Learning	02	
	SLH-Self Learning	02	
	NLH-Notional Learning	08	

**B. ASSESSMENT SCHEME :-**

PAPER DURATION IN HRS	THEORY				BASED ON LL&TL				BASED ON SLA		TOTAL
					Practical						150
	FA-TH	SA-TH	TOTAL		FA -PR		SA-PR				
	MAX	MAX	MAX	MIN	MAX	MIN	MAX	MIN	MAX	MIN	
03	30	70	100	40	25	10	-	-	25	10	

**(Total IKS Hrs for Sem. : 00 Hrs)**

**C. ABBREVIATIONS:-** CL- Class Room Learning , TL- Tutorial Learning, LL-Laboratory Learning, SLH-Self Learning Hours, NLH-Notional Learning Hours, FA - Formative Assessment, SA -Summative assessment, IKS - Indian Knowledge System, SLA - Self Learning Assessment  
**Legends:** @ Internal Assessment, # External Assessment, \*# On Line Examination , @\$ Internal Online Examination.

1. FA-TH represents average of two class tests of 30 marks each conducted during the semester.
2. If candidate is not securing minimum passing marks in FA-PR of any course then the candidate shall be declared as "Detained" in that semester.
3. If candidate is not securing minimum passing marks in SLA of any course then the candidate shall be declared as fail and will have to repeat and resubmit SLA work.
4. Notional Learning hours for the semester are (CL+LL+TL+SL)hrs.\* 15 Weeks
5. 1(one) credit is equivalent to 30 Notional hrs.
6. \* Self learning hours shall not be reflected in the Time Table.

\* Self learning includes micro project / assignment / other activities. (Provide list of all assignments here in tabular format At least 6 to 8 assignments to be given)

## D. i) RATIONALE:-

Communication, being an integral part of every human activity, plays a fundamental role in education, science and technology. The communication skills are essential for engineering professionals to carry out routine tasks at workplace. These skills are also required for professional activities like dialogue, persuasion and negotiation. Considering the age group and socio-economical background of the students of the Institute, this course has been designed with a skill-oriented content with some necessary theoretical foundation. Thus, this course has been designed to enhance the skills to communicate effectively and skillfully at workplace.

## ii) INDUSTRY / EMPLOYER EXPECTED OUTCOME

The aim of this course is to help the student to attain the following industry identified outcome through various learning experiences:

1. “Communicate in written and oral form of English effectively at workplace.”

## E. COURSE LEVEL LEARNING OUTCOMES (COs)

CCH201-1 Use Contextual words in English appropriately.

CCH201-2 Comprehend the concept of communication and identify communication barriers.

CCH201-3 Prepare and participate in dialogue, conversation, elocution and debate.

CCH201-4 Make effective use of body language & graphical communication.

CCH201-5 Write letters, reports, e-mails and technical description in correct language.

CCH201-6 Prepare and present effective media aided presentation.

## COMPETENCY, COURSE OUTCOMES AND PROGRAMME OUTCOMES (CP-CO-PO) MATRIX:

[ Note : Correlation levels : 1: Slight (Low), 2: Moderate (Medium), 3: Substantial (High), “0”

	PO 1 Basic and Discipline specific knowledge	PO 2 Problem Analysis	PO 3 Design / Development of solutions	PO 4 Engineerin g Tools, Experiment ation and Testing	PO 5 Engineering Practices for society, sustainability and Environment	PO 6 Project Manage ment	PO 7 Life- long Learnin g	PSO1	PSO2
<b>Industry/Employer Expected Outcome: Communicate in written and oral form of English effectively and informal scenario</b>	2	-	-	-	-	1	2		
CCH201-1 Use Contextual words in English appropriately.	1	1	-	-	-	2	1		
CCH201-2 Comprehend the concept of communication and identify communication barriers	2	1	-	-	-	2	2		
CCH201-3 Prepare and participate in dialogue, conversation, elocution and debate.	2	1	-	-	-	2	1		
CCH201-4 Make effective use of body language & graphical communication.	2	-	-	-	-	2	2		

CCH201-5 Write letters, reports, e-mails and technical description in correct language.	2	-	-	-	-	2	1		
CCH201-6 Prepare and present effective media aided presentation.	1	1	-	-	-	1	1		

## F. CONTENT:

### I) Practical Exercises

The following practical exercises shall be conducted in the Laboratory for *Communication Skills* developed by the Institute in practical sessions of batches of about 20- 22 students:

Sr No.	Title of Practical Exercise	Course Outcome
1.	Vocabulary Building: Affixation	CCH201-1
2.	Vocabulary Building: Homophones	CCH201-1
3.	Vocabulary Building: Synonyms-Antonyms and Collocations	CCH201-1
4.	Communication Cycle and Communication Barriers	CCH201-2
5.	Oral Communication: Transcription	CCH201-3
6.	Oral Communication: Prepared Speech	CCH201-3
7.	Oral Communication: Conversation	CCH201-3
8.	Oral Communication: Group Discussion	CCH201-3
9.	Oral Communication: Group Debate	CCH201-3
10.	Non-verbal Communication: Graphic Communication	CCH201-4
11.	Non-verbal Communication: Body Language	CCH201-4
12.	Written Communication: Writing formal Letters	CCH201-5
13.	Written Communication: Writing Reports	CCH201-5
14.	Written Communication: Drafting of E-mail	CCH201-5
15.	Written Communication: Technical Writing	CCH201-5
16.	Presentation Aids	CCH201-6

## I. Theory

### Section I

Sr. No.	Topics/Subtopics	Learning (Hours)	Classroom learning evaluation Marks
CO: CCH201-1 Use Contextual words in English appropriately.			
1	<b>Vocabulary Building</b> 1.1 Affixation: Prefix and Suffix, Definition and Examples, List of common Prefixes and Suffixes 1.2 Synonyms and antonyms: Vocabulary Expansion, Context and Usage 1.3 Homophones: Identifying Homophones, Meaning and Contest, Vocabulary Expansion 1.4 Collocation: Definition and Identification, Types of Collocations	8	08
CO: CCH201-2 Comprehend the concept of communication and identify communication barriers.			
2	<b>Introduction to Communication</b> 2.1 Definition and Importance of Communication 2.2 Model of Communication 2.3 Principles of Effective Communication 2.4 Types of Communication: Formal, Informal, Oral, Written, Verbal, Non-Verbal, Horizontal, Upward, Downward and Diagonal Communication 2.5 Barriers to communication: Physical, Mechanical, Psychological and Language Barriers	14	16
CO: CCH201-3: Prepare and participate in dialogue, conversation, elocution and debate.			
3	<b>Oral Communication</b> 3.1 Characteristics of Oral Communication. 3.2 Phonetics: IPA, Vowels(12), Consonants(24) and Diphthongs (12) 3.3 Tone, Pronunciation and Accents. 3.4 Spoken English: Prepared and Extempore speeches 3.5 Role Play: Conversation and Dialogue 3.6 Group Discussion and Debate	8	10

### Section II

Sr. No.	Topics/Subtopics	Learning (Hours)	Classroom learning evaluation Marks
CO: CCH201-4: Make effective use of body language & graphical communication.			

4	<b>Non-verbal Communication</b> 4.1 Importance of Non-Verbal Communication. 4.2 Aspects of Body Language: Facial Expressions, Eye Contact, Vocalics, Gestures, Posture, Dress, Appearance and Personal Grooming and Haptics. 4.3 Non-Verbal Codes: Proxemics, chroemics, artefacts 4.4 Graphical Communication: 4.4.1 Advantages and Disadvantages of Graphical Communication. 4.4.2 Tabulation of Data and its depiction in the form of Bar Graphs and Pie Charts	08	12
CO: CCH201-5 Write letters, reports, e-mails and technical description in correct language.			
5	<b>Written Communication</b> 5.1 Characteristics of Written Communication. 5.2 Letter Writing: Application with Resume, Enquiry Letter, Order Letter and Complaint Letter 5.3 Writing Reports: Accident, Fall in Production Reports and Micro Project 5.4 Email Writing 5.5 Technical Writing: Object Description, Picture Description, Diary Writing 5.6 Paragraph Writing: Narrative, Descriptive and Technical	16	20
CO: CCH201-6 Prepare and present effective media aided presentation.			
6	<b>Media-Aided Presentations</b> 6.1 Media aids for Presentation: Strengths and Precautions 6.2 Planning, Preparing and Making a Presentation 6.3 Use of Presentation Media	06	04

\*\* No questions will be asked on IKS learning subtopics in any question papers

### G. List of Assignments/ Activities/Micro-project under SLA

\*\*A learner should complete at least on major activity mentioned in the above list under the guidance of subject teacher.

Sr. No	List of Assignment (under SLA)	Hrs Allotted
1	Report different types of episodes and anecdotes	02
2	Seminar preparation and Presentation	04
3	Make a pod cost episode based on Indian freedom fighters.	02
4	Present summary of the editorial column of English news paper	02
5	Write review of on any one: short story, novel, film	02
6	Prepare a booklet on Indian scientist/ eminent persons	04
7	Prepare blog, vlogs and pod cast	04
8	Prepare questionnaire for interview on any one: industry personnel, social worker, entrepreneur and conduct interview.	02
9	Prepare charts/tables of vowels, diphthongs, consonant, organs of speech, vocabulary in English	02
10	Prepare charts/tables of types of communication, barrier in communication, aspects of body language	02
11	Prepare a micro project on a given topic.	04



## H. Specification Table for Setting Question Paper for Semester End Theory Examination

Section/ Topic No.	Name of topic	Distribution of marks (level wise)			Total marks	CO
		Remember	Understand	Apply		
I / 1	Vocabulary Building	02	02	04	08	CCH201-1
I / 2	Introduction to Communication	04	06	06	16	CCH201-2
I / 3	Oral Communication	04	02	04	10	CCH201-3
II / 4	Non-verbal Communication	04	02	06	12	CCH201-4
II / 5	Written Communication	04	04	12	20	CCH201-5
II / 6	Media-aided Presentations	-	02	02	04	CCH201-6
Total Marks					70	

### I. Assessment Criteria

#### i) Formative Assessment of Practical:-

Every assignment shall be assessed for 25 marks as per following criteria:

Domain	Particulars	Marks out of 25
Cognitive	Understanding	05
	Application	05
Psychomotor	Operating Skills	05
	Drawing / drafting skills	05
Affective	Discipline and punctuality	05
<b>TOTAL</b>		<b>25</b>

#### ii) Summative Assessment of Practical:

Every practical assignment shall be assessed for 25 marks as per following criteria:

Sr. No.	Criteria	Marks allotted
1	Attendance at regular practical	NA
2	Preparedness for practical	NA
3	Neat & complete Diagram.	NA
4	Observations & handling of instrument.	NA
5	Oral Based on Lab work and completion of task	NA
<b>TOTAL</b>		

### I. Instructional Methods:

17. Lecture cum Demonstration,
18. Class room practices.
19. Use of projector and soft material for demonstration

**J. Teaching and Learning Resources:**

Chalk board, LCD presentations, Demonstrative kits, Demonstrative charts.

**K. Reference Books:**

S.N.	Name of Book	Author	Publication
22	Communication Skills	Sanjay Kumar ad Pushp Lata	Oxford University Press
23	Personality Development and Soft Skills	Brun K. Mitra	Oxford University Press
24	Effective Communication Skills	M Ashraf Rizvi	Tata McGraw-Hill
25	Human Communication	Burgoon Michael	SAGE Publication Inc.
26	101 Ways to Better Communication	Elizabeth Hiemey	Pustak Mahal
27	Technical Writing and Professional Communication	Thomas Huckin and Leslie	McGraw-Hill College Division

**Learning Website & Software**

- e. [www.nptel.com/iitm/](http://www.nptel.com/iitm/)
- f. <https://www.britishcouncil.in/english/learn-online>
- g. <https://www.vocabulary.com>
- h. [www.newagegolden.com](http://www.newagegolden.com)
- i. <https://www.internationalphoneticassociation.org>

**COURSE ID :**

**COURSE NAME : APPLIED ELECTRONICS**

**COURSE CODE : ETH103**

**COURSE ABBREVIATION : HATX**

### A. LEARNING SCHEME:

Scheme component		Hours	Credits
Actual Contact Hours / week	Classroom Learning	04	03
	Tutorial Learning	00	
	Laboratory Learning	02	
	SLH-Self Learning	00	
	NLH-Notional Learning	06	

### B. ASSESSMENT SCHEME:-

PAPER DURATION IN HRS	THEORY				BASED ON LL&TL				BASED ON SLA		Total
					Practical						
03	FA-TH	SA-TH	TOTAL		FA -PR		SA-PR		MAX	MIN	
	MAX		MAX	MIN	MAX	MIN	MAX	MIN			
	30	70	100	40	25	10	25@	10	-	-	150

**C. ABBREVIATIONS:** - CL- Class Room Learning, TL- Tutorial Learning, LL-Laboratory Learning, SLH-Self Learning Hours, NLH- Notional Learning Hours, FA - Formative Assessment, SA -Summative assessment, IKS - Indian Knowledge System, SLA - Self Learning Assessment

**Legends:** @ Internal Assessment, # External Assessment, \*# On Line Examination, @\$ Internal Online Examination .

1. FA-TH represents average of two class tests of 30 marks each conducted during the semester.
2. If candidate is not securing minimum passing marks in FA-PR of any course then the candidate shall be declared as "Detained" in that semester.
3. If candidate is not securing minimum passing marks in SLA of any course then the candidate shall be declared as fail and will have to repeat and resubmit SLA work.
4. Notional Learning hours for the semester are (CL+LL+TL+SL) hrs.\* 15 Weeks
5. 1(one) credit is equivalent to 30 Notional hrs.
6. \* Self learning hours shall not be reflected in the Time Table.

\* Self learning includes micro project / assignment / other activities. (Provide list of all assignments here in tabular format At least 6 to 8 assignments to be given)

## D. i) RATIONALE: -

Diploma engineers must deal with the various electronic components while maintaining various electronic equipment/systems. The use of basic electronics components and handling of various electronics systems will help them troubleshoot electronics equipment used in industry or in the consumer market etc. This course is developed to empower the students to apply their knowledge to solve broad electronic engineering application problems.

## ii) INDUSTRY / EMPLOYER EXPECTED OUTCOME

The aim of this course is to attend following industry identified competency through various teaching learning experiences: • Maintain electronic equipment/systems comprising of discrete electronic components.

## E. COURSE LEVEL LEARNING OUTCOMES (COs)

ETH103-1 Use transistor as an amplifier.

ETH103-2 Use transistor as a power amplifier.

ETH103-3 Comprehend feedback amplifiers.

ETH103-4 Analyze BJT waveform generator

ETH103-5 Analyze BJT Switching Circuits.

ETH103-6 Demonstrate and analyze linear and nonlinear wave shaping circuits.

## Course outcomes and programme outcomes/ programme specific outcomes (CO- PO/PSO) matrix

[Note: Correlation levels: 1: Slight (Low), 2: Moderate (Medium), 3: Substantial (High), “-“ : No correlation]

Cos	Programme Outcomes POs and PSOs								
	PO 1 Basic and Discipline specific knowledge	PO 2 Problem Analysis	PO 3 Design / Development of solutions	PO 4 Engineering Tools, Experimenta tion and Testing	PO 5 Engineering Practices for society, sustainability and Environment	PO 6 Project Managem ent	PO 7 Life- long Learning	PSO1 Operate and Maintain	PSO2 Supervision and Providing Solution
ETH103-1	2	-	-	1	-	-	2	1	1
ETH103-2	3	-	-	2	2	2	3	3	2
ETH103-3	3	-	-	2	-	1	2	2	2
ETH103-4	3	-	-	2	2	2	1	1	1
ETH103-5	3	-	-	1	-	2	2	1	1
ETH103-6	3	-	-	2	2	1	3	2	2

## F. CONTENT:

### I) Practical exercises

The following practical exercises shall be conducted in the *Laboratory for Applied Electronics* developed by the Institute in practical sessions of batches of about 20- 22 students: (Any 10 )

Sr. No.	Laboratory experiences	CO
1.	Build and test the performance of single stage low power common emitter amplifiers.	ETH103-1

Sr. No.	Laboratory experiences	CO
2.	Build and test the performance of single stage common source FET amplifiers.	ETH103-1
3.	Build and test the performance of two stage direct coupled amplifier through any simulation software tool.	ETH103-1
4.	Build class A power amplifier with resistive load and check output of Circuit.	ETH103-2
5.	Build class B power amplifier in complementary symmetry configuration and check output of circuit.	ETH103-2
6.	Build single tuned voltage amplifier and check output and draw response on semilog graph paper.	ETH103-2
7.	Build and test the performance of voltage series feedback amplifier.	ETH103-3
8.	Build and test the performance of voltage shunt feedback amplifier.	ETH103-3
9.	Build and test the performance of Hartley Oscillator. Calculate the oscillator frequency and verify calculated frequency with actual observed frequency	ETH103-4
10.	Build and test the performance of Colpitts Oscillator. Calculate the oscillator frequency and verify calculated frequency with actual observed frequency	ETH103-4
11.	Build and test the performance of RC phase shift Oscillator. Calculate the oscillator frequency and verify calculated frequency with actual observed frequency	ETH103-4
12.	Build and Test the performance of Astable multivibrator using transistor: time measurement and calculations.	ETH103-5
13.	Build and test the performance of Monostable multivibrator using transistor: time measurement and calculations through any simulation software tool.	ETH103-5
14.	Build RC circuit as Integrator/ differentiator and Check applications of differentiator as wave shaping circuit (response of the circuit for square input)	ETH103-6
15.	Build Positive and negative clipper circuit and Observe Clipping waveform on CRO.	ETH103-6
16.	Build Positive and negative Clamper circuit and Observe Clamping waveform on CRO.	ETH103-6

## II) Theory

### Section I

Sr. no.	Topics/Subtopics	Learning (Hours)	Classroom learning evaluation Marks
	<b>ETH103-1 Use transistor as an amplifier.</b>		
<b>1</b>	<b>Amplifier</b> 1.1 Classification of amplifier, BJT as an amplifier. 1.2 Single stage CE amplifier, frequency response, gain, bandwidth.	<b>11</b>	<b>12</b>

	<p>1.3 Types of BJT coupling: Circuit diagram, Operation, frequency response and application of RC, transformer, and direct coupling.</p> <p>1.4 Multistage amplifier: General Multistage amplifier BJT based.</p> <p>1.5 FET Amplifier: Common Source amplifier, Working Principle, and application.</p> <p>1.6 Tuned Amplifier: Need of tuned amplifier, basic tuned circuit, circuit diagram, Operating principle, and frequency response of Single tuned, Double tuned and stagger tuned amplifiers.</p>		
	<b><i>ETH103-2 Use transistor as a power amplifier.</i></b>		
<b>2</b>	<p><b>Power Amplifiers</b></p> <p>2.1 Power Amplifiers: Comparison between small signal amplifier and power amplifier, performance parameter of power amplifiers: bandwidth, gain, frequency band, efficiency.</p> <p>2.2 Classification of Power Amplifier</p> <p>2.3 Circuit Diagram, Working, Characteristics, Efficiency of following:</p> <p>2.3.1 Class A Power Amplifier- Transformer coupled</p> <p>2.3.2 Class B Power Amplifier and Crossover distortion, Class AB Power Amplifier</p> <p>2.3.3 Class B Push pull amplifier</p> <p>2.3.4 Complementary symmetry Class B Push Pull amplifier</p> <p>2.4. Class C amplifier(only introduction)</p>	<b>11</b>	<b>12</b>
	<b><i>ETH 103-3 Comprehend feedback amplifiers.</i></b>		
<b>3</b>	<p><b>Feedback Amplifier</b></p> <p>3.1 Principle of feedback amplifier</p> <p>3.2 Types of feedback: Negative and positive feedback, Advantages of negative feedback and its effects</p> <p>3.3 Types of negative feedback (block diagram, Circuit diagram, Operation and comparison)</p> <p>3.3.1 Current series feedback</p> <p>3.3.2 Voltage series feedback</p> <p>3.3.3 Voltage shunt feedback</p> <p>3.3.4 Current shunt feedback</p> <p>3.3.5 Darlington pair, Darlington amplifier (only introduction)</p> <p>3.4 Numericals problems based on feedback formula.</p>	<b>8</b>	<b>10</b>
	<b>Sub-total</b>	<b>30</b>	<b>34</b>

## Section –II

Sr. no.	Topics/Subtopics	Learning (Hours)	Classroom learning evaluation Marks
	<b><i>ETH103-4 Analyze BJT waveform generator</i></b>		
<b>4</b>	<b>Wave form Generators</b> 4.1 Oscillators: Need, Comparison of oscillator and amplifier. 4.2 Condition for oscillation (Barkhausen's Criteria), classification of oscillators 4.3 Circuit Diagram, Working, frequency formula of following oscillators: - 4.3.1 Tuned circuit Oscillators 4.3.2 Hartley Oscillator 4.3.3 Colpitts oscillator 4.3.4 RC phase-shift oscillator 4.3.5 Wein Bridge oscillator 4.4 Frequency stability consideration 4.5 Sweep generator: Miller sweep, Bootstrap circuit, current time base generator. 4.6 Numericals based on above	<b>9</b>	<b>12</b>
	<b><i>ETH103-5 Analyze BJT Switching Circuits</i></b>		
<b>5</b>	<b>BJT Switching Circuits</b> 5.1 Transistor as a switch, Transistor Switching Times 5.2 Transistorized Multivibrators and its types: - Circuit Diagram, Operation, timing equations & applications of following: - 5.2.1 Astable multivibrator 5.2.2 Monostable multivibrator 5.2.3 Bistable Multivibrator 5.2.4 Schmitt Trigger 5.3 Numericals based on timing equations of above circuits	<b>9</b>	<b>10</b>
	<b><i>ETH103-6 Demonstrate and analyze linear and nonlinear wave shaping circuits.</i></b>		
<b>6</b>	<b>Wave shaping Circuits</b> Circuit diagram, waveforms and operation of following:- 6.1 Linear wave shaping circuits. 6.1.1 Differentiator - High pass RC circuits- Response to triangular input & square wave 6.1.2 Integrator- Low pass RC circuit – Response to square input & rectangular input 6.2 Nonlinear wave shaping 6.2.1 Clippers 6.2.1.1 Positive clipper 6.2.1.2 Negative clipper 6.2.1.3 Combinational clipper 6.2.2 Clampers 6.2.2.1 Positive clampers 6.2.2.2 Negative clampers 6.2.2.3 Voltage doublers and triplers.	<b>12</b>	<b>14</b>
	<b>Sub-total</b>	<b>30</b>	<b>36</b>

## G. Specification table for setting question paper for semester end theory assessment

Section / Topic no.	Name of topic	Distribution of marks (level wise)			Total marks	CO
		Remember	Understand	Apply		
I / 1	Low Power Amplifier	2	6	4	12	ETH103-1
I / 2	High Power Amplifier	2	6	4	12	ETH103-2
I / 3	Feedback amplifier	2	2	6	10	ETH103-3
II / 4	Wave form Generators	2	6	4	12	ETH103-4
II / 5	BJT Switching Circuits	-	4	6	10	ETH103-5
II / 6	Wave shaping Circuits	-	4	10	14	ETH103-6
Total Marks		8	28	34	70	

## H. Assessment Criteria

### i) Formative Assessment of Practical: -

Every assignment shall be assessed for 25 marks as per following criteria:

Domain	Particulars	Marks out of 25
Cognitive	Understanding	05
	Application	05
Psychomotor	Operating Skills	05
	Drawing / drafting skills	05
Affective	Discipline and punctuality	05
<b>TOTAL</b>		<b>25</b>

### ii) Summative Assessment of Practical:

Every practical assignment shall be assessed for 25 marks as per following criteria:

Sr. no	Criteria	Marks allotted
1	Attendance at regular practical	05
2	Preparedness for practical	05
3	Neat & complete Diagram.	05
4	Observations & handling of instrument.	05
5	Oral Based on Lab work and completion of task	05
<b>TOTAL</b>		<b>25</b>

## I. Instructional Methods:

1. Lectures cum Demonstrations
2. Class room practices
3. Use of projector and soft material for demonstration



4. Virtual Laboratory

**J. Teaching and Learning resources:**

Chalk board, LCD presentations, Demonstrative kits, Demonstrative charts

**K. Reference Books:**

Sr. No.	Name of Book	Author	Publication
1	V. K. Mehta	Principles of Electronics	S.Chand
2	B. L. Theraja	Basic Electronics	S.Chand
3	R.S.Sedha	A text book of Applied Electronics	S.Chand
4	G. K. Mithal	Applied Electronics	Khanna Publication
5	A. Motershed	Electronics Devices & Circuits	PHI Publication
6	Malvino	Electronics Principles	McGraw Hill
7	Bell, Devid	Fundamental of Electronics Devices and circuits	Oxford University

**L. Learning Website & Software**

- 1) [www.nptel.iitm.ac.in](http://www.nptel.iitm.ac.in)
- 2) [www.learningaboutelectronics.com](http://www.learningaboutelectronics.com)
- 3) [www.electronics-tutorials.com](http://www.electronics-tutorials.com)
- 4) <https://circuitdigest.com/electronic-circuits>
- 5) [https://www.tutorialspoint.com/basic\\_electronics/basic\\_electronics\\_transistors.htm](https://www.tutorialspoint.com/basic_electronics/basic_electronics_transistors.htm)
- 6) [https://www.youtube.com/watch?v=O\\_pqCNPs6xw](https://www.youtube.com/watch?v=O_pqCNPs6xw)
- 7) <https://www.youtube.com/watch?v=0nXEUKFBd8A>

**COURSE ID:**

**COURSE NAME** : Electrical Engineering (IE/ET)

**COURSE CODE** : ETH104

**COURSE ABBREVIATION** : HEEG

**A. LEARNING SCHEME:**

Scheme component		Hours	Credits
Actual Contact Hours / week	Classroom Learning	03	3
	Tutorial Learning		
	Laboratory Learning	02	
	SLH-Self Learning	01	
	NLH-Notional Learning	06	

**B. ASSESSMENT SCHEME:**

PAPER DURATION IN HRS	THEORY				BASED ON LL&TL				BASED ON SLA		TOTAL
					Practical						175
	FA-TH	SA-TH	TOTAL		FA-PR		SA-PR		MAX	MIN	
	MAX	MAX	MAX	MIN	MAX	MIN	MAX	MIN	MAX	MIN	
03	30	70	100	40	25@	10	25	10	25	10	

**C. ABBREVIATIONS:**

CL- Class Room Learning , TL- Tutorial Learning, LL-Laboratory Learning, SLH-Self Learning Hours, NLH- Notional Learning Hours, FA - Formative Assessment, SA -Summative assessment, IKS - Indian Knowledge System, SLA - Self Learning Assessment

Legends: @ Internal Assessment, # External Assessment, \*# On Line Examination, @\$ Internal Online Examination  
Note : ( TNR 11 font)

7. FA-TH represents average of two class tests of 30 marks each conducted during the semester.
8. If candidate is not securing minimum passing marks in FA-PR of any course then the candidate shall be declared as "Detained" in that semester.
9. If candidate is not securing minimum passing marks in SLA of any course then the candidate shall be declared as fail and will have to repeat and resubmit SLA work.
10. Notional Learning hours for the semester are (CL+LL+TL+SL)hrs.\* 15 Weeks
11. 1(one) credit is equivalent to 30 Notional hrs.
12. \* Self learning hours shall not be reflected in the Time Table.

\* Self learning includes micro project / assignment / other activities. (Provide list of all assignments here in tabular format At least 6 to 8 assignments to be given)

## D. i) RATIONALE:-

The subject deals in understanding the basics of laws, working principle, construction, operation and applications of the various equipment, instruments and machines in electrical engineering.

## ii) INDUSTRY / EMPLOYER EXPECTED OUTCOME

The aim of this course is to help the student to attain the following industry identified outcome through various learning experiences:

**“Apply the basic principles of electrical engineering to solve engineering problems”.**

## E. COURSE LEVEL LEARNING OUTCOMES (COS)

### COURSE OUTCOMES:

ETH104-1. Apply basic laws and principles of electrical engineering to electrical applications.

ETH104-2. Use principles of magnetic circuits to calculate various parameters in magnetic circuits.

ETH104-3. Interpret basic principles of electromagnetic induction.

ETH104-4. Apply basic principles of AC circuits in electrical devices.

ETH104-5. Interpret circuit parameters in AC circuits.

ETH104-6. Apply basic laws of electromagnetic induction principles in transformer & electric machines.

### COURSE OUTCOMES AND PROGRAMME OUTCOMES (CP-CO-PO) MATRIX

[ Note : Correlation levels : 1: Slight (Low), 2: Moderate (Medium), 3: Substantial (High), “-” : no correlation ]

PO →	PO 1	PO 2	PO 3	PO 4	PO 5	PO 6	PO 7	PS O1	PS O2
CO									
ETH104-1	3	1	0	1	0	0	0	0	0
ETH104-2	3	1	0	1	0	0	0	0	0
ETH104-3	3	1	0	1	0	0	0	0	0
ETH104-4	3	1	0	1	0	0	0	0	0
ETH104-5	3	1	0	1	1	0	0	0	0
ETH104-6	3	1	0	1	1	0	1	1	0

## F. CONTENT:-

### I) Practical exercises

The following practical exercises shall be conducted in the *Laboratory for Electrical Engineering* developed by the Institute in practical sessions of batches of about 20- 22 students:

Sr.No	Title of Experiment	Skills to be developed	Course outcome
1.	Calculate the resistance of DC circuit by measuring voltage and current and verify using multi-meter. (Verify Ohm's law)	1. Identify different components in electrical Laboratory 2. Use voltmeter and ammeter.	1
2.	To measure the voltages across resistances in the circuit and verify the readings using Kirchhoff's Voltage Law.	Implement Kirchhoff's voltage law to solve electrical circuits.	1
3.	To measure the currents across resistances in the circuit and verify the readings using Kirchhoff's Current Law.	Implement Kirchhoff's current law to solve electrical circuits.	1
4.	Connect resistances in series and parallel connection and measure its resistances by using Ohm's law.	Connect electrical loads in series and parallel.	1
5.	Determine the permeability of magnetic material by plotting its B-H curve.	1. Measure magnetic flux density and electric field intensity. 2. Plot B-H curve of a material.	2
6.	Observe and identify the direction induced emf in the coil with the moving magnet and moving coil. (Verify Faraday's law of electromagnetic induction and Lenz law)	Identify direction of induced emf in given environment.	3
7.	Measure frequency, amplitude, time period, peak to peak value of alternating quantity.	Use CRO to measure different parameters.	5
8.	Calculate R, L and power factor of series RL circuit by measuring voltages and currents in circuit.	Identify AC meters. Measure AC quantities.	5
9.	Calculate R, C and power factor of series RC circuit by measuring voltages and currents in circuit.	Identify AC meters. Measure AC quantities.	5
10.	Calculate R, L, C and power factor of series RLC circuit by measuring voltages and currents in circuit.	1. Identify AC meters. 2. Measure AC quantities.	5
11.	Use transformer as step up and step down of single phase transformer.	Measure primary and secondary side voltages of transformer.	6

## II) Theory

### Section I

Sr. no.	Topics/Subtopics	Learning (Hours)	Classroom learning evaluation Marks
<b><i>ETH104-1: Apply basic laws and principles of electrical engineering to electrical applications.</i></b>			
<b>1</b>	<b>BASIC LAWS AND PRINCIPLES</b> <ol style="list-style-type: none"> <li>1. Basic terms:-electric current and potential difference. Concept of voltage drop and terminal voltage.</li> <li>2. Concept of resistance and conductance Laws of resistance (Simple Numerical)Concept of resistivity and conductivity.</li> <li>3. Classification of electric current: Direct current (DC) &amp; alternating current (AC)</li> <li>4. Concept of power and energy with simple numerical. (in DC circuit)</li> <li>5. Series and parallel connection of resistances. (Simple numerical)</li> <li>6. Theorems for DC circuits: Ohm's Law (Simple Numerical) Kirchhoff's Laws (Simple Numerical with maximum two equations)</li> </ol>	<b>06</b>	<b>10</b>
<b><i>ETH104-2: Use principles of magnetic circuits to calculate various parameters in magnetic circuits.</i></b>			
<b>2</b>	<b>MAGNETIC CIRCUITS</b> <ol style="list-style-type: none"> <li>1. Magnetic Circuit - Ohm's law of magnetic circuit.</li> <li>2. Definitions concerning magnetic circuit: Magnetomotive-Force (MMF), Ampere Turns (AT), Reluctance, Permeance, Reluctivity.</li> <li>3. Comparison between electric and magnetic circuit.</li> <li>4. Calculations of ampere-turns for simple series magnetic circuit (Simple Numerical)</li> <li>5. Concept of magnetization curve (B - H Curve) Magnetization curve for magnetic and non-magnetic materials.</li> <li>6. Concepts of magnetic hysteresis, hysteresis loop. Significance of area of hysteresis loop, hysteresis loss. (No Derivation and No Numerical), Definition of eddy current loss and its formula.</li> <li>8. Concepts of permanent magnet and electromagnet.</li> </ol>	<b>08</b>	<b>14</b>
<b><i>ETH104-3: Understand basic principles of electromagnetic induction.</i></b>			

<b>3</b>	<b>ELECTROMAGNETIC INDUCTION</b> <ol style="list-style-type: none"> <li>Faraday's laws of electromagnetic induction. (Simple Numerical)</li> <li>Induced E.M.F: Statically induced E.M.F., dynamically induced E.M.F.(Simple Numerical)</li> <li>Direction of induced E.M.F. and currents. Fleming's right hand rule, Fleming's left hand rule. Lenz's law.</li> <li>Basic concepts of self induction and mutual induction. (No numerical)</li> <li>Basic principle of elementary alternator.</li> <li>Energy stored in magnetic field (No Derivation and No Numerical)</li> <li>Lorentz force principle (Simple numerical).</li> </ol>	<b>7</b>	<b>10</b>
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## Section II

<i><b>ETH104-4: Apply basic principles of AC circuits in electrical devices.</b></i>			
<b>4</b>	<b>AC FUNDAMENTALS</b> <ol style="list-style-type: none"> <li>Generation of alternating EMFs.</li> <li>Some important terms.: cycle, time period, frequency, amplitude, average values, rms value.</li> <li>Equations of alternating voltages and currents.</li> <li>Concept of effective or root mean square (R.M.S.) value of sinusoidal current or voltage.</li> <li>Peak factor and form factor.</li> <li>Phasor representation of alternating quantities.</li> <li>Phase and phase difference, concept of lagging and leading</li> <li>Addition and subtraction of sinusoidal alternating quantities. (Simple Numerical)</li> <li>Multiplication and division of sinusoidal alternating quantities. (Simple Numerical)</li> </ol>	<b>10</b>	<b>12</b>
<i><b>ETH104-5: Understand circuit parameters in AC circuit.</b></i>			
<b>5</b>	<b>AC CIRCUITS (NO NUMERICAL)</b> <ol style="list-style-type: none"> <li>Polyphase Generation</li> <li>Three phase power equation</li> <li>Star and delta connections of resistive load. (No derivation) Comparison between star and delta connections of load.</li> <li>A.C. circuits Purely resistive A.C. circuit. Purely inductive A.C. circuit. Purely capacitive A.C. circuit.</li> <li>Series A.C. circuits Circuit with resistance and inductance in series (Concept of power factor) Circuit with resistance and capacitance in series (Concept of power factor) Circuit with resistance inductance and capacitance in series (Concept of power factor) Active and reactive power in single phase series circuit.</li> </ol>	<b>08</b>	<b>10</b>
<i><b>ETH104-6: Apply basic laws of electromagnetic induction principles in electric machines.</b></i>			

6	<b>TRANSFORMER &amp; MACHINE (NO NUMERICAL)</b> <ol style="list-style-type: none"> <li>1. Basic principle of working of a single phase transformer.</li> <li>2. Construction of a single phase transformer.</li> <li>3. Types of transformer based on Construction of core of transformers Number of phases Voltage level Functions of transformer (instrument, power, isolation)</li> <li>4. Application of transformers in electronic circuit.</li> <li>5. Basic principle of working of single phase induction motor.</li> <li>6. Basic principle of working of DC motor. Compare shunt and series DC motors.</li> <li>7. Stepper Motor- reluctance type stepper motor – working and application</li> <li>8. Basic principle of earthing, necessity of earthing, types of earthing (pipe earthing and plate earthing)</li> </ol>	06	14
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### G. List of Assignments under SLA(Microproject/activity)

Sr.No	List of Assignment (under SLA)
1	Simple problems on Ohms law, Law of resistance, Power and energy.
2	Survey of different magnetic material.
3	Simple problems on magnetic circuits.
4	Simple problems on of induced E.M.F: Statically induced E.M.F., dynamically induced E.M.F.
5	Simple problems on Addition, subtraction, multiplication and division of sinusoidal alternating quantities.
6	Survey the electrical appliances which represents different types of load.(resistive, inductive and capacitive)
7	Compare types of transformer as per voltage level, construction, number of phases, applications.
8	Survey different types of earthing.

### H. Specification table for setting question paper for semester end theory examination

Section / Topic no.	Name of topic	Distribution of marks (level wise)			Total marks	CO
		Remember	Understand	Apply		
I / 1	Basic Laws & Principles	4	4	2	10	ETH104-1
I / 2	Magnetic Circuits	4	4	6	14	ETH104-2
I / 3	Electromagnetic Induction	4	4	2	10	ETH104-3
II / 4	AC Fundamentals	4	6	2	12	ETH104-4
II / 5	AC Circuits	4	4	2	10	ETH104-5
II / 6	Transformer & Machine	4	6	4	14	ETH104-6
	Total Marks				70	

## I. Assessment Criteria

The assessment need to be done as per Proforma I & II

### i) Formative Assessment of Practical and SLA:-

Every assignment/ Practical shall be assessed for 25 marks as per following criteria:

Domain	Particulars	Marks out of 25
Cognitive	Understanding	05
	Application	05
Psychomotor	Operating Skills	05
	Drawing / drafting skills	05
Affective	Attendance/Discipline and punctuality	05
<b>TOTAL</b>		<b>25</b>

### ii) Summative Assessment of Practical:

At the time of Practical Examination assessed for 25 marks as per following criteria:

Sr. no	Criteria	Marks allotted
1	Knowledge about the course	05
2	Preparedness for practical /Oral	05
3	Neat & complete Diagram/write up	05
4	Observations/Handling of instrument/ Communication/Presentation	05
5	Oral Based on Lab work and completion of task	05
<b>TOTAL</b>		<b>25</b>

## J. Instructional Methods:

3. Lectures cum Demonstrations,
4. Class room practices.
5. Use of projector and soft material for demonstration

## K. Teaching and Learning resources:

Chalk board, LCD presentations, Demonstrative kits, Demonstrative charts.

## L. Reference Books:

S.N.	Name of Book	Author	Publication
11	Basic Electrical Engineering,	Mittle and Mittal	McGraw Education, New Delhi, 2015, ISBN :978-0-07-0088572-5
12	Electrical Technology Vol – I	Theraja, B. L	S. Chand Publications, New Delhi, 2015
13	Electrical Technology Vol – II,	Theraja, B. L	S. Chand Publications, New Delhi, 2015
14	Basic Electrical Engineering	V.K Mehta Author), Rohit	S. Chandpublications.



		Mehta	
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## M. Learning Website & Software

- a. [www.nptel.com/iitm/](http://www.nptel.com/iitm/)
- b. [www.howstuffworks.com/](http://www.howstuffworks.com/)
- c. [www.virtual lab.com](http://www.virtual lab.com)
- d. [www.sskphdmm.com](http://www.sskphdmm.com)
- e. <http://www.youtube.com/watch?v=RAc1RYilugI>

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**COURSE ID:**

**COURSE NAME : C PROGRAMMING**

**COURSE CODE : ETH105**

**COURSE ABBREVIATION : HCPR**

**A. LEARNING SCHEME:**

Scheme component		Hours	Credits
Actual Contact Hours / week	Classroom Learning	02	02
	Tutorial Learning	00	
	Laboratory Learning	02	
	SLH-Self Learning	00	
	NLH-Notional Learning	04	

**B. ASSESSMENT SCHEME:-**

Paper Duration (Hrs)	Assessment Scheme								Based on Self Learning		Total Marks
	Theory				Based on LL & TL						
	FAT H	SA TH	Total		FA-PR		SA-PR		SLA		
	Max	Max	Max	Min	Max	Min	Max	Min	Max	Min	
-	-	-	-	-	25	10	25@	10	-	-	50

**(Total IKS Hrs for Sem. : 00 Hrs)**

**C. ABBREVIATIONS: -**

CL- Class Room Learning, TL- Tutorial Learning, LL-Laboratory Learning, SLH-Self Learning Hours, NLH-Notional Learning Hours, FA - Formative Assessment, SA -Summative assessment, IKS - Indian Knowledge System, SLA - Self Learning Assessment

**Legends:** @ Internal Assessment, # External Assessment, \*# On Line Examination, @\$ Internal Online Examination.

1. FA-TH represents average of two class tests of 30 marks each conducted during the semester.
2. If candidate is not securing minimum passing marks in FA-PR of any course then the candidate shall be declared as "Detained" in that semester.
3. If candidate is not securing minimum passing marks in SLA of any course then the candidate shall be declared as fail and will have to repeat and resubmit SLA work.
4. Notional Learning hours for the semester are (CL+LL+TL+SL) hrs.\* 15 Weeks
5. 1(one) credit is equivalent to 30 Notional hrs.
6. \* Self learning hours shall not be reflected in the Time Table.

\* Self learning includes micro project / assignment / other activities. (Provide list of all assignments here in tabular format At least 6 to 8 assignments to be given)

### D. i) RATIONALE:

This course is designed to develop programming attitude and attract the interest of the students in the C Language. C is a very powerful, widely used, efficient and compact, which combines features of high-level language and low-level language. It is used in many scientific programming situations. It forms the core of the modern languages Java and C++. Almost every set up in software Engineering domain chooses C as a first priority programming language.

### ii) INDUSTRY / EMPLOYER EXPECTED OUTCOME

Development of programming skills to solve engineering problems in procedural way. Understanding and implementing concepts of procedural programming. Operating Computer system efficiently. Development of attitude of precision, accuracy, safety, punctuality and aesthetic presentation.

### E. COURSE OUTCOMES:

**ETH105-1** Identify C expressions with character set and operators.

**ETH105-2** Apply decision making and branching and looping constructs in programming.

**ETH105-3** Implement user defined functions and arrays.

**ETH105-4** Implement library functions for string handling.

**ETH105-5** Develop C programs using structures and pointers.

### COMPETENCY, COURSE OUTCOMES AND PROGRAMME OUTCOMES (CP-CO-PO) MATRIX

[ Note : Correlation levels : 1: Slight (Low), 2: Moderate (Medium), 3: Substantial (High), “-” : No correlation

Competency and COs	PO								
	PO 1 Basic and discipline specific knowledge	PO 2 Proble m analysi s	PO 3 design/ develop ment of solution s	PO 4 Enginee ring Tools, experim entation and testing	PO 5 Engineering practice for society, sustainabilit y and environmen t	PO 6 Project manag ement	PO 7 Life-long learning	PSO1 Design and development	PSO2 Database and Network management
Competency: Apply concepts of C Programming to solve engineering problems	3	3	3	2	2	2	1	3	-
ETH105-1	2	1	2	2	2	1	1	-	-
ETH105-2	2	3	3	3	2	1	1	2	-
ETH105-3	2	2	3	3	1	1	1	2	-
ETH105-4	2	2	3	3	1	1	1	2	-
ETH105-5	2	2	2	3	2	1	1	2	-

**PSO 1: Operate and Maintain:** Competency to apply the concepts of Industrial Electronics in the operation and maintenance of engineering application systems.

**PSO 2: Supervision and providing solution:** Ability to supervise work and reach appropriate solution to simple practical problems in Industrial Electronics engineering industry.

## **F. CONTENT:**

### **I) SUGGESTED PRACTICAL'S/ EXERCISE**

The following practical exercises shall be conducted as practical's and assess the student for attainment of the competency. (any 10 out of 13 experiments)

<b>Sr. No.</b>	<b>Title of Experiment</b>	<b>Skills to be developed</b>	<b>Course Outcome</b>
01	Study of Flowchart and Algorithm	<ul style="list-style-type: none"> <li>• Understanding an Algorithm</li> <li>• Understanding the Flowchart</li> <li>• Study of various Flowchart Symbols</li> <li>• To draw Flowchart on any Practical routine</li> </ul>	ETH105-1
02	Installation of C/C++ Compiler and study of its IDE	<ul style="list-style-type: none"> <li>• Understanding Integrated development environment of any one C compiler</li> <li>• To create and save and compile a program file</li> <li>• To run the compiled program and see the output</li> </ul>	
03	Usage of C character set, keywords, identifiers, variables, constants, and expressions	<ul style="list-style-type: none"> <li>• Study of character set of C language</li> <li>• Study of identifiers, variables, constant, and Keyword</li> <li>• Rules for valid variables, identifiers, constants.</li> <li>• Identify valid and invalid variables,</li> <li>• Study of expressions and different types</li> </ul>	ETH105-1
04	Usage of Operators	<ul style="list-style-type: none"> <li>• Classification of operators in C</li> <li>• Understanding use of C different types operators</li> <li>• Writing simple C programs</li> </ul>	ETH105-1

		illustrating use of all category of C operators	
05	To use input and output library functions	<p>Writing simple programs to illustrate the use of-</p> <ul style="list-style-type: none"> <li>• Standard Input function- scanf()</li> <li>• Standard Output function-printf()</li> <li>• Character input and output functions getchar() ,putchar()</li> <li>• String input and output functions gets(), puts()</li> </ul>	ETH105-1
06	Implementation of decision Making and branching using if, if-else, Nested if, ladder if-else structure	<p>Writing simple programs to illustrate the use of-</p> <ul style="list-style-type: none"> <li>• If statement</li> <li>• If-else statement</li> <li>• Nested if..else</li> <li>• else..if ladder</li> </ul>	ETH105-2
07	Implementation of multiple decision making using switch statement	<p>Writing program to illustrate the use of-</p> <ul style="list-style-type: none"> <li>• switch statement</li> <li>• break statement</li> <li>• default statement</li> </ul>	ETH105-2
08	Implementation of looping using for Statement	<p>Writing a program to illustrate the use of-</p> <ul style="list-style-type: none"> <li>• for statement to implement loop</li> <li>• Nested for loop</li> </ul>	ETH105-2
09	Implementation of looping using while and do---while statement	<ul style="list-style-type: none"> <li>• Exit control and Entry control loop</li> <li>• program based on while loop and do-while</li> </ul>	ETH105-2
10	To create and use of one dimensional and multi-dimensional array	<p>Writing a program to illustrate-</p> <ul style="list-style-type: none"> <li>• creating one and multi-dimensional array</li> <li>• Manipulation of elements of an array</li> </ul>	ETH105-3
11	Program based on User Defined Functions	<p>Writing a program to illustrate-</p> <ul style="list-style-type: none"> <li>• User defined function declaration</li> </ul>	ETH105-3

		or prototype <ul style="list-style-type: none"> <li>• User defined function definition</li> <li>• Function call or reference</li> <li>• Passing parameters to a function-call by value and call by reference</li> </ul>	
12	Strings and string manipulation functions	Writing a program to illustrate- <ul style="list-style-type: none"> <li>• Declaration and initialization of string variable</li> <li>• reading and writing a string from and to terminal.</li> <li>• String- handling Functions - strlen(), strcmp(), strcpy(), strcat(),strupr(), strlwr(), strrev() etc.</li> </ul>	ETH105-4
13	Study of Structure	Writing a program to illustrate- <ul style="list-style-type: none"> <li>• Defining a structure</li> <li>• Declaring and initialization of structure variable</li> <li>• Accessing members of structure variable</li> </ul>	ETH105-6
13	Implementation of Pointer	Writing a program to illustrate- <ul style="list-style-type: none"> <li>• Declaration of pointer</li> <li>• Initializing pointer variable</li> <li>• Accessing data using pointer variable</li> </ul>	ETH105-6

## II) THEORY :

### SECTION I

Sr. No.	Topics / Sub-topics	Lectures (Hours)
<b>Course Outcome ETH105 – 1</b> Identify C expressions with character set and operators.		
<b>1</b>	<b>C FUNDAMENTALS</b> 1.1 History of c 1.2 C character set 1.3 Identifiers & Keywords, 1.4 Data types	<b>05</b>

	1.5 Variables 1.6 Declarations 1.7 Constants 1.8 Expressions 1.9 C Instructions 1.10 The first C program 1.11 Compilation & Execution	
<b>2</b>	<b>OPERATORS&amp; DATA INPUT AND OUTPUT FUNCTIONS</b> 2.1 Operators 2.1.1 Arithmetic Operators 2.1.2 Assignment Operator 2.1.2 Unary operators 2.1.3 Relational & Logical Operators, 2.1.4 Conditional & Comma Operator 2.2 Input and Output Library Functions 2.2.1 printf()                2.2.2 scanf() 2.2.3 getchar()            2.2.4 putchar() 2.2.5 gets()                2.2.6 puts()	<b>05</b>
<b>Course Outcome ETH105 -2</b> Apply decision making and branching and looping constructs in programming.		
<b>3</b>	<b>CONTROL STATEMENTS</b> 3.1 Decision making and branching 3.1.1 if Statement(if, if-else, if-else ladder, nested if-else) 3.1.2 Switch, break, continue, goto statement 3.2 Decision making and looping 3.2.1 While, do – while, for Statements 3.2.2 Nested loops	<b>06</b>

## SECTION II

Sr. No.	Topics / Subtopics	Lectures (Hours)
<b>Course Outcome ETH105 -3</b> Implement user defined functions and arrays.		
<b>4.</b>	<b>ARRAYS &amp; FUNCTIONS</b> 4.1 Defining an array, 4.2 One dimensional array ,Declaration and Initialization of Arrays, 4.3 Two Dimensional Arrays Declaration and Initialization of Arrays, 4.4 Defining a Function, Accessing a function,	<b>06</b>

	4.5 Passing arguments to a Function(call by value and call by reference), Specifying argument data types 4.6 Scope and lifetime of variables 4.7 Function prototypes 4.8 Recursion	
<b>Course Outcome ETH105 -4</b> Implement library functions for string handling.		
<b>5.</b>	<b>CHARACTERS &amp; STRINGS</b> 5.1 The char data type, using character variables, using string 5.2 Declaring and initializing string variables 5.3 Reading strings from terminal 5.4 Writing Strings to screen, putting strings together. 5.5 Comparison of two strings 5.6 String- handling Functions - strcmp(), strlen(), strcpy(), strcat(),strupr(), strlwr(), strrev()	<b>05</b>
<b>Course Outcome ETH105 -5</b> Develop C programs using structures and pointers.		
<b>6.</b>	<b>Structures and Pointers</b> 6.1 Simple structures (Defining & declaring structures, accessing structure members) 6.2 Complex structures (structures that contain arrays) 6.3 Understanding pointers, declaring pointer variable, initialization of pointer variable, accessing address of a variable	<b>5</b>

## G. ASSESSMENT CRITERIA FOR PRACTICAL ASSIGNMENTS AND PRACTICAL EXAMINATION

### a) Formative Assessment of Practical:

Every practical assignment shall be assessed for 25 marks as per the following criteria:

Domain	Particulars	Marks out of 25
Cognitive	Technical preparedness for practical	05
Psychomotor	Operating skills/Algorithm/ flowchart	05
	Observation/Logic/ Program/Result	05
Affective	Discipline and punctuality	05
	Procedure/ Safety Measures/ Decency/ Presentation	05
<b>TOTAL</b>		<b>25</b>



**b) Summative Assessment of Practical:**

Every student has to perform one practical within 3 hours at semester end practical assessment which shall be assessed as per following criteria.

Sr. No	Criteria	Marks allotted
1	Neat & complete circuit Diagram / schematic Diagram/ Algorithm/ Flowchart/ Program	5
2	Procedure followed to achieve the result	5
3	Observations, Result, Output, Sample Calculations with relevant formulae	5
4	Proper Graphs, workmanship and Safety measures	5
5	Oral	5
	<b>Total</b>	<b>25</b>

**H. INSTRUCTIONAL STRATEGIES:**

**Instructional Methods:**

1. Online/Offline Lectures cum Discussions
2. Regular home assignments
3. Laboratory work

**Teaching and Learning Resources:**

1. Chalk and Board
2. Video Clips
3. PPTs
4. Question Bank
5. Charts

**I. REFERENCE MATERIAL:**

**a) Books / Codes**

Sr. No.	Author	Title	Publisher
1.	E.Balgurusamy	Programming in ANSI C	Tata McGraw Hill Education
2.	Yashwant Kanetkar	Let us C	BPB Publication
3	Bryon Gottfried	Programming with C	Schaum's Outlines Series
4	kerninghan& Ritchie	The C Programming language	Prentice Hall

**b) Websites**

- 1) <https://www.w3schools.in/c-tutorial/>
- 2) [www.cprogramming.com](http://www.cprogramming.com)
- 3) [www.learn-c.org](http://www.learn-c.org)
- 4) [www.tutorialspoint.com/cprogramming](http://www.tutorialspoint.com/cprogramming)
- 5) [https://www.tutorialspoint.com/compile\\_c\\_online.php](https://www.tutorialspoint.com/compile_c_online.php)

**COURSE ID** :  
**COURSE NAME** : **SOCIAL AND LIFE SKILLS**  
**COURSE CODE** : **CCH204**  
**COURSE ABBREVIATION** : **HSLs**

### A. LEARNING SCHEME:

Scheme component		Hours	Credits
Actual Contact Hours / week	Classroom Learning	00	1
	Tutorial Learning	00	
	Laboratory Learning	00	
	SLH-Self Learning	02	
	NLH-Notional Learning	02	

### B. ASSESSMENT SCHEME :-

PAPER DURAT ION IN HRS	THEORY				BASED ON LL&TL				BASED ON SLA		TOTAL
					Practical						50
	FA-TH	SA-TH	TOTAL		FA –PR		SA-PR		MAX	MIN	
MAX	MAX	MAX	MIN	MAX	MIN	MAX	MIN				
00	00	00	00	00	00	00	-	-	50	20	

**(Total IKS Hrs for Sem. : 00 Hrs)**

**C. ABBREVIATIONS:-** CL- Class Room Learning , TL- Tutorial Learning, LL-Laboratory Learning, SLH-Self Learning Hours, NLH-Notional Learning Hours, FA - Formative Assessment, SA -Summative assessment, IKS - Indian Knowledge System, SLA - Self Learning Assessment  
**Legends:** @ Internal Assessment, # External Assessment, \*# Online Examination, @\$ Internal Online Examination.

1. FA-TH represents average of two class tests of 30 marks each conducted during the semester.
2. If candidate is not securing minimum passing marks in FA-PR of any course then the candidate shall be declared as "Detained" in that semester.
3. If candidate is not securing minimum passing marks in SLA of any course then the candidate shall be declared as fail and will have to repeat and resubmit SLA work.
4. Notional Learning hours for the semester are (CL+LL+TL+SL)hrs.\* 15 Weeks
5. 1(one) credit is equivalent to 30 Notional hrs.
6. \* Self learning hours shall not be reflected in the Time Table.

\* Self learning includes micro project / assignment / other activities. (Provide list of all assignments here in tabular format At least 6 to 8 assignments to be given)

#### **D. i) RATIONALE:-**

Life skills can be defined as abilities that enable an individual to deal effectively with the demands and challenges of life. Social skills are a subset of life skills that are needed for successful, healthy relationships to easily adapt when moving from one social situation to the next. They help regulate our emotions effectively and develop enduring, supportive relationships, we're happier and healthier. This is why developing life skills and eventually social skills is key not only to being successful in life, it's key for our health and well-being. Thus, Teaching of Social and life skills provide students with essentials of knowing, understanding attitudes, values, morals, social skills and better equip them to handle stress and build their self-efficacy, self-esteem and self-confidence.

Note: The course offers four different alternatives (modules) for achieving above outcomes. Students must complete any one module from the following given options.

- A) MODULE-I : Unnat Maharashtra Abhiyan (UMA)
- B) MODULE-II : National Service Scheme (NSS)
- C) MODULE-III : Universal Human Values
- D) MODULE-IV: Value Education (Unati Foundation)
- E) MODULE-V : Financial Literacy (NABARD)

The institute can choose to offer any one MODULE to the groups of the students by taking into consideration the resources required and resources available in the institute. Different group of students may be offered different MODULE based on their choices.

#### **ii) INDUSTRY / EMPLOYER EXPECTED OUTCOME**

Exhibit psychosocial competencies, workplace ethics, resilience, positive attitude, integrity and self-confidence.

#### **E. COURSE LEVEL LEARNING OUTCOMES (COs)**

Students will be able to achieve & demonstrate the following COs on completion of course based learning

- CCH204-1 - Develop ability to adapt to new challenges.
- CCH204-2 - Manage emotions effectively.
- CCH204-3 - Follow workplace ethics and practices.
- CCH204-4 - Manage time effectively.
- CCH204-5 - Increased self-confidence to handle stress.

**COMPETENCY, COURSE OUTCOMES AND PROGRAMME OUTCOMES (CP-CO-PO) MATRIX:**

[ Note : Correlation levels : 1: Slight (Low), 2: Moderate (Medium), 3: Substantial (High), “0”

	PO 1 Basic and Discipline specific knowledge	PO 2 Problem Analysis	PO 3 Design / Development of solutions	PO 4 Engineerin g Tools, Experiment ation and Testing	PO 5 Engineering Practices for society, sustainability and Environment	PO 6 Project Manage ment	PO 7 Life- long Learnin g	PSO1	PSO2
<b>Competency:</b> Exhibit psychosocial competencies, workplace ethics, resilience, positive attitude, integrity and self-confidence.									
CCH204-1 Develop ability to adapt to new challenges.						1	2		
CCH204-2 Manage emotions effectively.						1	2		
CCH204-3 Follow workplace ethics and practices.						1	2		
CCH204-4 Manage time effectively.						2	2		
CCH204-5 Increased self-confidence to handle stress.						2	2		

## F. CONTENT:

### I) Practical Exercises:

Not Applicable

### II) Theory

Sr. No.	Theory Learning Outcomes (TLOs) Aligned to COs.	Learning content mapped with Theory Learning Outcomes (TLO's) and CO's.	Suggested Learning Pedagogies.
	<p>TLO 1.1 Explain developmental needs and connection of various stakeholders</p> <p>TLO 1.2 Enlist the local problems</p> <p>TLO 1.3 Design a methodology for fieldwork</p> <p>TLO 1.4 Select the attributes of engineering and social system for measurement, quantification, and documentation</p> <p>TLO 1.5 Measure &amp; quantify the quantities / systems parameters</p> <p>TLO 1.6 Write a report using information collected. Study the data collected from fieldwork and conclude the observations.</p>	<p><b>Unit - I MODULE I : Activities Under Unnat Maharashtra Abhiyan (UMA)</b></p> <p>1.1 Introduction to Societal Needs and respective stakeholders : Regional societal issues that need engineering intervention</p> <p>1.2 Multidisciplinary approach-linkages of academia, society and technology</p> <p>1.3 Stakeholders' involvement</p> <p>1.4 Introduction to Important secondary data sets available such as census, district economic surveys, cropping pattern, rainfall data, road network data etc</p> <p>1.5 Problem Outline and stakeholders : Importance of activity and connection with Mapping of system components and stakeholders (engineering / societal)</p> <p>1.6 Key attributes of measurement</p> <p>1.7 Various instruments used for data collection - survey templates, simple measuring equipments</p> <p>1.8 Format for measurement of identified attributes/ survey form and piloting of the same</p> <p>1.9 Fieldwork : Measurement and quantifications of local systems such as agriculture produce, rainfall, Road network, production in local industries, Produce /service which moves from A to B</p> <p>1.10 Analysis and Report writing Report writing containing-</p> <ol style="list-style-type: none"> <li>1. Introduction of the topic</li> <li>2. Data collected in various formats such as table, pie chart, bar graph etc</li> </ol> <p>Observations of field visits and data collected.</p>	<p>Implementation Methodology: Considering the nature of the course designed, following points shall be considered while implementing the course.</p> <p>i) Regroup in the batches of 5-6 students for conducting the fieldwork from the bigger group.</p> <p>ii) Assign a few batches of the students for this course to all the faculty members.</p> <p>iii) A group of course teachers will visit local governance bodies such as Municipal Corporations, Village Panchayats, Zilla Parishads, Panchayat Samitis to assess the small technological / engineering needs in their area of work.</p> <p>iv) The group of course teachers will carry out initial field visits to evaluate the various possibilities of field visits / various scenarios wherein students can conduct field work to measure / quantify the parameters / attributes.</p> <p>v) The course will be implemented in eight sessions and fieldwork.</p> <p>a) Session I - Introduction to development paradigm, fieldwork</p>

			<p>and case study as pedagogy</p> <p>b) Session II - VII - Society, stakeholders and value creation, measurements, rudimentary analysis and reporting</p> <p>c) Session VIII - Final closure session feedback and assessment</p> <p>d) Field work -</p> <p>1. Pilot Visit - Pilot of survey instrument</p> <p>Survey Visit 1 - Data gathering / Information Collection</p> <p>3. Survey Visit 2 - Datagathering</p> <p>Summary Visit - Closure after analysis</p>
2	<p>TLO 2.1 Adoption of Village or Slum</p> <p>TLO 2.2 Survey and Problem Identification</p> <p>TLO 2.3 Conduct Project / Programs in the selected village / slum</p> <p>TLO 2.4 Undertake Special Camping Programme</p>	<p><b>Unit - II MODULE II : National Service Scheme (NSS)</b></p> <p>2.1 Contacting Village/Area Leaders</p> <p>2.2 Primary socio economic survey of few villages in the vicinity of the institute.</p> <p>2.3 Selection of the village for adoption - conduct of activities</p> <p>2.4 Comprehensive Socio Economic Survey of the Village/Area</p> <p>2.5 Identification of Problem(s)</p> <p>2.6 Dissemination of information about the latest developments in agriculture, watershed management, wastelands development, non-conventional energy, low cost housing, sanitation, nutrition and personal hygiene, schemes for skill development, income generation, government schemes, legal aid, consumer protection and allied fields.</p> <p>A liaison between government and other development agencies for the implementation of various development schemes in the selected village / slum.</p>	<p>(i) The teachers should visit the village / slum before adopting it for NSS activities.</p> <p>(ii) The selected area should be compact.</p> <p>(iii) The community people should be receptive to the ideas of improving their living standard. They should also be ready to coordinate and involve in the projects undertaken by the NSS for their up-liftment</p> <p>(iv) The areas where political conflicts are likely to arise should be avoided by the NSS units.</p> <p>The area should be easily accessible to the NSS volunteers to undertake frequent visits to slums;</p>
3	<p>TLO 3.1 Love and Compassion (Prem and Karuna)</p> <p>TLO 3.2 Truth (Satya)</p> <p>TLO 3.3 Non-Violence (Ahimsa)</p> <p>TLO 3.4 Righteousness (Dharma)</p> <p>TLO 3.5 Peace (Shanti)</p> <p>TLO 3.6</p>	<p><b>Unit - III MODULE-III : Universal Human Values</b></p> <p>3.1 Love and Compassion (Prem and Karuna): Introduction, Practicing Love and Compassion (Prem and Karuna)</p> <p>3.2 Truth (Satya) : Introduction, Practicing Truth (Satya)</p> <p>3.3 Non-Violence (Ahimsa) : Introduction, Practicing Non-Violence (Ahimsa)</p> <p>3.4 Righteousness (Dharma) : Introduction, Practicing Righteousness (Dharma)</p>	<p>i) Lectures</p> <p>ii) Demonstration</p> <p>iii) Case Study</p> <p>iv) Role Play</p> <p>v) Observations</p> <p>vi) Portfolio Writing</p> <p>vii) Simulation</p> <p>viii) Motivational talks by Practitioners</p>

	Service (Seva)TLO 3.7 Renunciation (Sacrifice) Tyaga TLO 3.8 Gender Equality and Sensitivity	3.5 Peace (Shanti) : Introduction, Practicing Peace (Shanti) 3.6 Service (Seva) : Introduction, Practicing Service (Seva) 3.7 Renunciation (Sacrifice) Tyaga : Introduction, Practicing Renunciation (Sacrifice) Tyaga Gender Equality and Sensitivity: Introduction, Practicing Gender Equality andSensitivity	Site/Industry Visit
4	TLO 4.1 Punctuality TLO 4.2 Cleanliness, Hygiene and Orderliness TLO 4.3 Responsibility TLO 4.4 Gratitude andAppreciations TLO 4.5 Determination& Persistence TLO 4.6 Respect TLO 4.7 Team Spirit TLO 4.8 Caring & Sharing TLO 4.9 Honesty TLO 4.10 Forgive andForget	<b>Unit - IV MODULE-IV: Value Education (Unnati Foundation)</b> 4.1 Punctuality, Icebreaker and Simple Greeting, Understanding & Managing Emotions, Introducing Self, The power of a Positive Attitude, Talking about one's Family, Talking about one's Family, Making a Positive Impression, Give word list for a Word based 4.2 Cleanliness , Hygiene and Orderliness , Likes and Dislikes, Developing Confidence in Self and Others, Strengths and Weaknesses, Listening Skills , Greeting gestures, Gender Equality and Sensitivity 4.3 Responsibility, OCSEM- Visual Comprehension and Word Based Learning, Goal Setting – Make it happen, Follow, Like &Share Unnati Social Media - Facebook / Instagram/ Twitter Introducing Others, Time Management, Talking about the daily routine, Money Management 4.4 Gratitude and Appreciation , Asking Simple Questions & Asking for the price , Stress Management, Student Referral process ,Comprehending & Paraphrasing Information,A Plate of Rice and Dignity of Labour, Topicsfor Public Speaking, Placement Process , OCSEM- E-Newspaper, Critical Thinking to overcome challenges 4.5 Determination and Persistence, Guiding and Giving Directions, Language Etiquette & Mannerism, . Unnati Philosophy , b. Unnati Branding - Follow, Like & Share Unnati Social Media - Facebook / Instagram/ Twitter, Simple instructions to follow procedures, Assertiveness, Give topics for Debate, Describing a person/Objects, Refusal Skills, Word List for Word based Learning 4.6 Respect, Comparing , OCSEM - Public Speaking, Student referral process, Attending a phone call, Being a Good Team Player , Placement Process, At a Restaurant, Workplace ethics 4.7 Team Spirit, Inviting someone, OCSEM - Picture Reading & Word, a. Unnati Philosophy& b. Unnati Branding - Follow, Like & Share Unnati Social Media - Facebook / Instagram/ Twitter, Apologizing, Apologizing, Dealing effectively with Criticism, Introduce Importance	i) Video Demonstrations ii) Flipped Classroom iii) Case Study iv) Role Play v) Collaborative learning vi) Chalk-Board

		<p>of Self Learning and up skilling Caring and Sharing , Handling Customer queries, Flexibility &amp; Adaptability, Student referral process, Writing a Resume, OCSEM-Public Speaking, Placement Process, Meditation/ Affirmation &amp; OCSEM-Debate, Introduce Certif-ID, how to create Certif-ID Project , 4.9 Honesty, Email etiquette &amp; Official Email communication, Alcohol &amp; Substance use &amp; abuse, Describing a known place , Leadership Skills, Describing an event, OSCEM-Picture Reading &amp; Visual Comprehension Forgive and Forget, Facing and Interview, OSCEM-Public Speaking , Attending a telephonic/Video interview &amp; Mock Interview , Affirmation , Pat-a-Back &amp; Closure (Valediction , Unnati Branding, Student Testimonials), Meditation/ Affirmation &amp; Sponsor connect (Speak to UNXT HO)</p>	
5	<p>TLO 5.1 Literacy About Savings and Investments TLO 5.2 Literacy About Financial Planning TLO 5.3 Literacy About Transactions TLO 5.4 Literacy About Income, expenditure and budgeting TLO 5.5 Literacy About Inflation TLO 5.6 Literacy About Loans TLO 5.7 Literacy About the Importance of Insurance TLO 5.8 Literacy About the Dos and Don'ts in finances</p>	<p><b>Unit - V MODULE-V : Financial Literacy</b> 5.1 Introduction - Life Goals and financial goals 5.2 Savings and Investments - Three pillars of investments, Popular asset classes, Government schemes, Mutual Funds, Securities markets (Shares and bonds), Gold, Real Estate, Do's and Don'ts of investments 5.3 Retirement planning 5.4 Cashless transactions 5.5 Income, expenditure and budgeting – Concepts and Importance 5.6 Inflation- Concept, effect on financial planning of an individual 5.7 Loans – Types, Management of loans, Tax benefits 5.8 Insurance – Types, Advantages, selection Dos and Don'ts in Financial planning and Transactions</p>	<p>i) Online/Offline Mode of Instructions ii) Video Demonstrations iii) Presentations iv) Case Study v) Chalk-Board Collaborative learning</p>

\*\* No questions will be asked on IKS learning subtopics in any question papers.



## **G. List of Assignments/ Activities/Micro-project under SLA**

### **Suggestive list of activities during Regular as well as Special Camping (NSS Activities)**

Following list is only an illustrative list of the type of activities that can be undertaken. Under the programme. It would be open to each NSS Unit to undertake one of these programmes or any other activity which may seem desirable to them according to local needs. The NSS Unit should aim at the integrated development of the area selected for its operation which could be a village or a slum. It has also to be ensured that at least a part of the programme does involve manual work.

#### **(a) Environment Enrichment and Conservation:**

The activities under this sub-theme would inter-alia, include:

- (i) plantation of trees, their preservation and upkeep
- (ii) Construction & maintenance of village streets, drains
- (iii) Cleaning of village ponds and wells;
- (iv) Popularization and construction of Gobar Gas Plants, use of non-conventional energy;
- (v) Disposal of garbage & composting;
- (vi) Prevention of soil erosion and work for soil conservation,
- (vii) Watershed management and wasteland development
- (viii) Preservation and upkeep of monuments, and creation of consciousness about the preservation of cultural heritage among the community.

#### **(b) Health, Family Welfare and Nutrition Programme:**

- (i) Programme of mass immunization;
- (ii) Working with people in nutrition programmes with the help of Home Science and medical college students;
- (iii) Provision of safe and clean drinking water;
- (iv) Integrated child development programmes;
- (v) Health education, AIDS Awareness and preliminary health care.
- (vi) Population education and family welfare programme;
- (vii) Lifestyle education centres and counselling centres.

© Programmes aimed at creating an awareness for improvement of the status of women: (i) programmes of educating people and making them aware of women's rights both constitutional and legal;

- (ii) creating consciousness among women that they too contributed to economic and social well-being of the community;
- (iii) creating awareness among women that there is no occupation or vocation which is not open to them provided they acquire the requisite skills; and
- (iv) imparting training to women in sewing, embroidery, knitting and other skills wherever possible.

#### **(d) Social Service Programmes:**

- (i) work in hospitals, for example, serving as ward visitors to cheer the patients, help the patients, arranging occupational or hobby activities for long term patients; guidance service for out-door-patients including guiding visitors about hospital's procedures, letter writing and reading for the patients admitted in the hospital; follow up of patients discharged from the hospital by making home visits and places of work, assistance in running dispensaries etc.
- (ii) work with the organisations of child welfare;
- (iii) work in institutions meant for physically and mentally handicapped;

- (iv) organising blood donation, eye pledge programmes;
- (v) work in Cheshire homes, orphanages, homes for the aged etc.;
- (vi) work in welfare organisations of women;
- (vii) prevention of slums through social education and community action;

(e) Production Oriented Programmes:

- (i) working with people and explaining and teaching improved agricultural practices;
- (ii) rodent control land pest control practices;
- (iii) weed control;
- (iv) soil-testing, soil health care and soil conservation;
- (v) assistance in repair of agriculture machinery;
- (vi) work for the promotion and strengthening of cooperative societies in villages;
- (vii) assistance and guidance in poultry farming, animal husbandry, care of animal health etc.;
- (viii) popularisation of small savings and assistance in procuring bank loans

(f) Relief & Rehabilitation work during Natural Calamities:

- (i) assisting the authorities in distribution of rations, medicine, clothes etc.;
- (ii) assisting the health authorities in inoculation and immunisation, supply of medicine etc.;
- (iii) working with the local people in reconstruction of their huts, cleaning of wells, building roads etc.;
- (iv) assisting and working with local authorities in relief and rescue operation;
- (v) collection of clothes and other materials, and sending the same to the affected areas;

(g) Education and Recreations: Activities in this field could include:

- (i) adult education (short-duration programmes);
- (ii) pre-school education programmes;
- (iii) programmes of continuing education of school drop outs, remedial coaching of students from weaker sections;
- (iv) work in crèches;
- (v) participatory cultural and recreation programmes for the community including the use of mass media for instruction and recreation, programmes of community singing, dancing etc.;
- (vi) organisation of youth clubs, rural land indigenous sports in collaboration with Nehru Yuva Kendras;
- (vii) programmes including discussions on eradications of social evils like communalism, castism, regionalism, untouchability, drug abuse etc.;
- (viii) non- formal education for rural youth and Legal-literacy, consumer awareness.

## **H. Specification Table for Setting Question Paper for Semester End Theory**

**Examination:** Not Applicable

### **I. Assessment Criteria**

#### **i) Formative Assessment of Practical:-**

Formative assessment (Assessment for Learning) report and presentation of fieldwork activities, self-learning (Assignment)

## ii) Summative Assessment of Practical:

(Assessment of Learning)

## J. Instructional Methods:

20. Group Discussion, Flipped Classroom
21. Demonstration, Case Study, Role Play, Collaborative Learning, Cooperative Learning
22. Field Visit, Survey
23. Use of projector and soft material for Demonstration (ppt, audio ,video etc)

## K. Teaching and Learning Resources:

Chalk board, LCD presentations, Demonstrative kits, Demonstrative charts.

## L. Reference Books:

S.N.	Name of Book	Author	Publication
28	Compendium of Training Materials for the Capacity Building of the Faculty and Students of Engineering Colleges on 'IMPROVING THE PERFORMANCE OF RURAL WATER SUPPLY AND SANITATION SECTOR IN MAHARASHTRA' Districts Economic survey reports	IRAP, Hyderabad, CTARA, IIT Bombay and UNICEF, Mumbai	UNICEF
29	Central Public Health and Environmental Engineering Organisation	Manual on Water Supply and Treatment	Ministry of Urban Development, New Delhi
30	Specifications And Standards Committee	Indian Standards (IS) Codes and Indian Roads Congress (IRC) Codes	Bureau of Indian Standards and The Indian Road Congress
31	Prepared by each district administration	Districts Economic survey reports	Govt. of Maharashtra
32	Local college students,UMA staffs	Sample Case Studies on UMA website	IITB-UMA team

## M. Learning Website & Software

- j. <https://gr.maharashtra.gov.in/Site/Upload/Government%20Resolutions/English/201601131501523808.pdf> (Government Resolution of Government of Maharashtra regarding Unnat Maharashtra Abhiyan)
- k. <https://gr.maharashtra.gov.in/Site/Upload/Government%20Resolutions/English/201606151454073708.pdf> (Government Resolution of Government of Maharashtra regarding Unnat Maharashtra Abhiyan Guidelines)
- l. <https://censusindia.gov.in/census.website/> (A Website of Census of India)
- m. <https://gsda.maharashtra.gov.in/english/> (A Website of Groundwater Survey and Development Agency, GoM)

- n. <https://mrsac.gov.in/MRSAC/map/map> (A Website where district-wise maps showcasing different attributes developed by Maharashtra Remote Sensing Applications Centre.)
- o. <https://ejalshakti.gov.in/jjmreport/JJMIndia.aspx> (A Website of Jal Jivan Mission, Government of India)
- p. <https://cpcb.nic.in/> (A Website of Central Pollution Control Board, Government of India)
- q. <http://www.mahapwd.com/#> (A Website of Public Works Department, GoM)
- r. <http://tutorial.communitygis.net/> (A Website for GIS data sets developed by Unnat Maharashtra Abhiyan)
- s. <https://youtu.be/G71maumVZ1A?si=TzDTxKUpLYaRos7U> (A video record of lecture by Prof. Milind Sohoni, IIT Bombay, on Engineering, Development and Society)
- t. <https://youtu.be/TUcPNwtdKyE?si=wnSWrhGc9dJTC-ac> (A keynote talk by Prof. Milind Sohoni, IIT Bombay, on Interdisciplinary Engineering: The Road Ahead)

# **SEMESTER III COURSES**



**COURSE ID :**

**COURSE NAME : CIRCUITS AND NETWORKS**

**COURSE CODE : ETH301**

**COURSE ABBREVIATION : HCKN**

### A. LEARNING SCHEME:

Scheme component		Hours	Credits
Actual Contact Hours / week	Classroom Learning	03	03
	Tutorial Learning	00	
	Laboratory Learning	02	
	SLH-Self Learning	01	
	NLH- Notional Learning	06	

### B. ASSESSMENT SCHEME:-

PAPER DURATION IN HRS	THEORY				BASED ON LL&TL				BASED ON SLA		Total
					Practical						
03	FA-TH	SA-TH	TOTAL		FA -PR		SA-PR		MAX	MIN	175
	MAX		MAX	MIN	MAX	MIN	MAX	MIN			
	30	70	100	40	25	10	25@	10	25	10	

**C. ABBREVIATIONS:** - CL- Class Room Learning, TL- Tutorial Learning, LL-Laboratory Learning, SLH-Self Learning Hours, NLH- Notional Learning Hours, FA - Formative Assessment, SA -Summative assessment, IKS - Indian Knowledge System, SLA - Self Learning Assessment

**Legends:** @ Internal Assessment, # External Assessment, \*# On Line Examination, @\$ Internal Online Examination .

1. FA-TH represents average of two class tests of 30 marks each conducted during the semester.
2. If candidate is not securing minimum passing marks in FA-PR of any course then the candidate shall be declared as "Detained" in that semester.
3. If candidate is not securing minimum passing marks in SLA of any course then the candidate shall be declared as fail and will have to repeat and resubmit SLA work.
4. Notional Learning hours for the semester are (CL+LL+TL+SL) hrs.\* 15 Weeks
5. 1(one) credit is equivalent to 30 Notional hrs.
6. \* Self learning hours shall not be reflected in the Time Table.

\* Self learning includes micro project / assignment / other activities. (Provide list of all assignments here in tabular format At least 6 to 8 assignments to be given)

#### D. i) RATIONALE: -

Diploma engineers must deal with the electronic circuit while maintaining various electronic equipment/systems in the industry. This course will help the students to use principles of circuit and analyse to maintain the electric circuit/network.

#### ii) INDUSTRY / EMPLOYER EXPECTED OUTCOME

The aim of this course is to attain following industry identified competencies through various teaching learning experiences:

Build and analyze various electronic circuits and Networks

#### E. COURSE LEVEL LEARNING OUTCOMES (COs)

ETH301-1 Apply basic laws of Circuit analysis to determine circuit parameters.

ETH301-2 Apply Nodal and Mesh analysis to determine circuit parameters.

ETH301-3 Apply various circuit theorems to calculate circuit parameters .

ETH301-4 Calculate the electrical parameters of single phase A.C. circuit

ETH301-5 Evaluate circuit parameters at series and parallel resonant frequency .

ETH301-6 Determine the circuit parameters of two port network.

#### Course outcomes and programme outcomes/ programme specific outcomes (CO- PO/PSO) Matrix

[Note: Correlation levels: 1: Slight (Low), 2: Moderate (Medium), 3: Substantial (High), “-“ : No correlation]

Cos	Programme Outcomes POs and PSOs								
	PO 1 Basic and Discipline specific knowledge	PO 2 Problem Analysis	PO 3 Design / Development of solutions	PO 4 Engineering Tools, Experimenta tion and Testing	PO 5 Engineering Practices for society, sustainability and Environment	PO 6 Project Managem ent	PO 7 Life- long Learning	PSO1 Operate and Maintain	PSO2 Supervision and Providing Solution
ETH301-1	3	3	2	3	1	1	2	3	2
ETH301-2	3	3	2	3	1	1	2	3	1
ETH301-3	3	3	2	3	1	1	2	3	1
ETH301-4	3	1	3	2	1	1	1	3	2
ETH301-5	3	2	3	2	1	1	1	3	1
ETH301-6	3	2	3	2	1	1	1	3	1



## F. CONTENT:

### I) Practical Exercises

The following practical exercises shall be conducted in the *Laboratory* in practical sessions in batches of about 20- 22 students:

Sr. No.	Laboratory experiences	CO
1.	Verify equivalent resistance formulae in series connection and parallel connection of Resistors .	ETH 301-1
2.	Measure current flowing through a resistor and voltage across that resistor and verify it using OHM's law	ETH 301-1
3.	Measure the voltage across resistors in resistive circuit and verify it, using Kirchhoff's Voltage law (KVL).	ETH 301-1
4.	Measure current in various branches of the given circuit and verify it, using Kirchhoff's current law (KCL).	ETH 301-1
5.	Calculate power dissipated in a resistor using $I^2R$ and $V^2/R$ formula	ETH 301-1
6.	Measure voltage across resistors in resistive circuit and Verify it using Voltage divider Rule	ETH 301-1
7.	Measure current in various branches of the resistive circuit and verify it using Current divider Rule	ETH 301 -1
8.	Measure voltage at particular node and current through branch of network and verify it by nodal analysis	ETH 301-2
9.	Measure current through and voltage across given branch of electric network and verify it by mesh analysis.	ETH 301-2
10.	Measure current through given branch of network and verify it applying Superposition theorem.	ETH 301-3
11.	Measure open circuit voltage and thevenin's resistance of the given circuit and verify it using Thevenin's theorem.	ETH 301-3
12.	Vary load resistance to transfer Maximum power in the given circuit using maximum power transfer theorem.	ETH 301-3
13.	Measure short circuit current and Norton's resistance of the given circuit and verify it using Norton's theorem.	ETH 301-3
14.	Verify equivalent capacitor formulae in series and parallel connected capacitors	ETH 301-4
15.	Measure initial and final voltage across the capacitor before and after switching input supply.	ETH 301-4
16.	Measure voltage and current in the given RLC series circuit and calculate resonance frequency and impedance at resonance using variable supply frequency.	ETH 301-5
17.	Measure current of given RLC parallel circuit and calculate resonance frequency and impedance at resonance by varying supply frequency.	ETH 301-5
18.	Develop RC low pass filter on breadboard and plot its frequency response.	ETH 301-6
19.	Develop RC high pass filter on breadboard and plot its frequency response.	ETH 301-6
20.	Develop RC band pass filter on breadboard and plot its frequency response.	ETH 301-6
21.	Perform minimum 5 practicals from above list using MultiSIM software	ETH 301-1,2,3,4,5,6

## II. SUGGESTED MICRO PROJECT / ASSIGNMENT/ ACTIVITIES FOR SPECIFIC LEARNING /SKILLS DEVELOPMENT (SELF LEARNING)

### Micro projects:

- Prepare a report on real life applications of resonance (e.g. musical instruments)
- Prepare power point presentation on source transformation, star- delta transformation, mesh and nodal analysis and give presentation in the class.
- Build a circuit on breadboard with multiple resistors connected in series and measure voltage across each resistor. Verify using KVL.
- Prepare a chart for comparison of single phase series RLC and parallel RLC circuit and draw input and output waveforms also.
- Build a circuit on breadboard with multiple resistors connected in series and measure voltage across and current through each resistor . Verify using Ohms law .
- Build a circuit on breadboard with multiple resistors connected in parallel and measure current across each resistor. Verify using KCL.

### Assignments:

- Find circuit parameters of Single Phase AC series (R-L, R-C, R-L-C) and parallel (R-L, R-C, R-L-C) circuit, also draw its phasor diagram.
- Find the resonance condition for the specified series and RLC circuit and calculate current, voltage, bandwidth, quality factor . Observe the behaviour of R, L and C with change in frequency for series circuit.
- Identify the number of loops and nodes in the given circuit and solve the circuit using Nodal analysis and Mesh analysis.
- Simplify complex circuit using Thevenin's theorem, Norton's theorem and draw equivalent circuit for given circuit.

### Activities For Specific Learning / Skills Development

- Verification of various network analysis and theorems in Virtual Laboratory (<https://asnm-iitkgp.vlabs.ac.in/>).
- Verification of various network analysis and theorems using Simulation Software (MATLAB, MultiSIM).
- Perform R-L-C circuit analysis in Virtual Laboratory (<https://asnm-iitkgp.vlabs.ac.in/exp/rlc-circuit-analysis/>).
- Experimental verification of frequency response of R-L-C series Circuit (<https://asnm-iitkgp.vlabs.ac.in/exp/rlc-series-circuit/>).
- Test the resonance in Series RLC circuit using Simulation Software (MATLAB, MultiSIM).

### Note

- Above is just a suggestive list of microprojects and assignments; faculty must prepare their own bank of microprojects, assignments, and activities in a similar way.
- The faculty must allocate judicious mix of tasks, considering the weaknesses and / strengths of the

student in acquiring the desired skills.

- If a microproject is assigned, it is expected to be completed activity. SLA marks shall be awarded as per the continuous assessment record.
- If the course does not have associated SLA component, above suggestive listings is applicable to Tutorials and maybe considered for FA-PR evaluations.

### III. Theory

#### Section I

Sr. no.	Topics/Subtopics	Learning (Hours)	Theory evaluation Marks
	<b><i>ETH301-1 Apply basic laws of Circuit analysis to determine circuit parameters</i></b>		
<b>1</b>	<b>BASIC THEORY</b> 1.1 Electric charge and current , voltage , Power and Energy 1.2 Voltage and current sources , Independent and Dependent sources . 1.3 Ohm's law and its limitations . 1.4 Definitions of Node, Branch, Loop, Mesh 1.5 Series circuits - Kirchhoff's voltage law, Voltage divider rule , Total resistance of Series connected resistors . 1.6 Parallel circuits - Kirchhoff's current law, Current divider rule , Total resistance of Parallel connected resistors . 1.7 Concept of Ground , Open circuit and short circuit <b>( Numericals based on above concepts )</b>	<b>8</b>	<b>12</b>
	<b><i>ETH301-2 Apply Nodal and Mesh analysis to determine circuit parameters.</i></b>		
<b>2</b>	<b>Methods of Circuit analysis</b> 2.1 Conversion of voltage to current source and current to voltage source 2.2 Series and parallel connection of Sources 2.3 Nodal Analysis method 2.4 Super Node analysis 2.5 Mesh Analysis method 2.6 Super Mesh Analysis <b>( Numericals based on above concepts )</b>	<b>6</b>	<b>10</b>
	<b><i>ETH 301-3 Apply various circuit theorems to calculate circuit parameters</i></b>		

<b>3</b>	<b>Network Theorems</b>  3.1 Linearity property. 3.2 Superposition Theorem 3.3 Thevenin's theorem 3.4 Norton's theorem 3.5 Maximum power transfer theorem 3.6 Millman's theorem 3.7 Tellegen's theorem  <b>( Numericals based on above concepts )</b>	<b>8</b>	<b>12</b>
	<b>Sub-total</b>	<b>22</b>	<b>34</b>

## Section II

Sr. no.	Topics/Subtopics	Learning (Hours)	Theory evaluation Marks
	<b><i>ETH301-4 Calculate the electrical parameters of single phase A.C. circuit</i></b>		
<b>4</b>	<b>Single phase AC circuits</b> 4.1 Current , Voltage and energy expressions in inductor and capacitor . 4.2 Series and parallel connection of inductor and capacitor 4.3 Phase relationship between current and voltage in an inductor and capacitor . 4.4 Representation of complex number in polar and rectangular form 4.5 Impedance and admittance calculations in AC circuits 4.6 Time constant in RL and RC circuit 4.7 Charging and discharging equations and curves in inductor and capacitor 4.8 Power in inductor and capacitor <b>( Numericals based on above concepts )</b>	<b>8</b>	<b>12</b>
	<b><i>ETH301-5 Evaluate circuit parameters at series and parallel resonant frequency</i></b>		
<b>5</b>	<b>Resonance in Series and Parallel circuits</b> 5.1 Resonance in series circuit: Impedance, phase angle, voltage, current, bandwidth, Quality factor (Q), magnification factor for series resonance circuit 5.2 Behaviour of RLC series circuit with change in input frequency 5.3 Resonance in Parallel Circuit: Impedance, phaseangle, voltage, current, bandwidth, Quality factor (Q), magnification factor for parallel resonance circuit 5.4 Behaviour of RLC parallel circuit with change in input Frequency 5.5 Applications of resonant circuits  <b>( Numericals based on above concepts )</b>	<b>8</b>	<b>12</b>

	<b>ETH301-6 Determine the circuit parameters of two port network.</b>		
<b>6</b>	<b>Analysis of two port network</b> 6.1 Network Types: Active and Passive, Bilateral and Unilateral, Linear and Nonlinear, Symmetrical and Asymmetrical, Single port and Two port network 6.2 Attenuators: Definition, types-T and Pi, features, frequency response, applications, comparison 6.3 Passive Filters: Definition, types- Low pass filter(LPF), high pass filter (HPF), band pass filter(BPF) and band stop filter (BSF), features, frequency response, applications, comparison  <b>( Numericals based on above concepts )</b>	<b>7</b>	<b>12</b>
	<b>Sub-total</b>	<b>23</b>	<b>36</b>

### G. Specification table for setting question paper for semester end theory assessment

Section / Topic no.	Name of topic	Distribution of marks (level wise)			Total marks	CO
		Remember	Understand	Apply		
I / 1	Basic Theory	-	4	8	12	ETH301-1
I / 2	Methods of Circuit analysis	-	4	6	10	ETH301-2
I / 3	Network Theorems	-	4	8	12	ETH301-3
II / 4	Single phase AC circuits	2	6	4	12	ETH301-4
II / 5	Resonance in Series and parallel circuits	4	4	4	12	ETH301-5
II / 6	Analysis of Two Port Networks	4	4	4	12	ETH301-6
Total Marks		14	26	30	70	

### H. ASSESSMENT METHODOLOGIES/TOOLS-

#### I) Formative Assessment (Assessment for learning)

- Two offline unit tests of 30 marks and average of two-unit test marks will be considered for out of 30 marks.
- Each practical will be assessed considering 60% weightage to process, 40% weightage to product.
- For formative assessment of laboratory learning 25 marks

#### II) Summative Assessment (Assessment of Learning)

- End semester summative assessment is of 25 marks

for laboratory learning

- End semester assessment is of 70 marks.

## I. Reference Books:

Sr. No.	Name of Book	Author	Publication
1	Electric circuits	Alexander and Sadiku	McGraw Hill
2	Circuit Analysis	Hayt and Kemmerly	McGraw Hill
3	Electrical Networks	Ravish Singh	McGraw Hill
4	Circuit and network	Sudhakar and Shyammoan	McGraw Hill
5	Electric Circuit Analysis	P. Ramesh Babu	SciTech Publication (India) Pvt. Ltd

## J. LEARNING WEBSITES & Portals

	Link / Portal	Description
1	<a href="http://www.scilab.org/scilab">www.scilab.org/scilab</a>	Open-source simulator for simulation of theorems
2	<a href="http://www.ni.com/multisim">www.ni.com/multisim</a>	Open-source simulator for simulation of theorems and circuit analysis
3	<a href="https://www.nptelvideos.com/course.php?id=462">https://www.nptelvideos.com/course.php?id=462</a>	NPTEL Circuit Theory Video Lectures
4	<a href="https://asnm-iitkgp.vlabs.ac.in/">https://asnm-iitkgp.vlabs.ac.in/</a>	Virtual laboratory link for theorems, R-L-C circuit analysis and its frequency response
5	<a href="https://www.udemy.com/course/electrical-circuit-for-electrical-electronics-engineering/">https://www.udemy.com/course/electrical-circuit-for-electrical-electronics-engineering/</a>	Basics, circuit element, circuit solving, network theorems, transient analysis
6	<a href="https://everycircuit.com/app">https://everycircuit.com/app</a>	Online and mobile app to design and simulate electronic circuits

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**COURSE ID:**

**COURSE NAME : LINEAR INTEGRATED CIRCUITS**

**COURSE CODE : ETH302**

**COURSE ABBREVIATION : HLIC**

**A. LEARNING SCHEME:**

Scheme component		Hours	Credits
Actual Contact Hours / week	Classroom Learning	03	04
	Tutorial Learning	00	
	Laboratory Learning	04	
	SLH-Self Learning	01	
	NLH-Notional Learning	08	

**B. ASSESSMENT SCHEME:-**

PAPER DURA TION IN HRS	THEORY				BASED ON LL&TL				BASED ON SLA		Total
					Practical						
03	FA-TH	SA-TH	TOTAL		FA -PR		SA-PR		MAX	MIN	
	MAX	MAX	MAX	MIN	MAX	MIN	MAX	MIN			
	30	70	100	40	25	10	25#	10	25	10	175

**C. ABBREVIATIONS: -** CL- Class Room Learning, TL- Tutorial Learning, LL-Laboratory Learning, SLH-Self Learning Hours, NLH-Notional Learning Hours, FA - Formative Assessment, SA -Summative assessment, IKS - Indian Knowledge System, SLA - Self Learning Assessment  
**Legends:** @ Internal Assessment, # External Assessment, \*# On Line Examination, @\$ Internal Online Examination.

1. FA-TH represents average of two class tests of 30 marks each conducted during the semester.
2. If candidate is not securing minimum passing marks in FA-PR of any course then the candidate shall be declared as "Detained" in that semester.
3. If candidate is not securing minimum passing marks in SLA of any course then the candidate shall be declared as fail and will have to repeat and resubmit SLA work.
4. Notional Learning hours for the semester are (CL+LL+TL+SL) hrs.\* 15 Weeks
5. 1(one) credit is equivalent to 30 Notional hrs.
6. \* Self learning hours shall not be reflected in the Time Table.

\* Self learning includes micro project / assignment / other activities. (Provide list of all assignments here in tabular format At least 6 to 8 assignments to be given)

### D. i) RATIONALE: -

The physical world is inherently analog. To process analog signals, in many electronic systems, Linear Integrated Circuits are used as basic building blocks. Operational amplifier (OPAMP) is most versatile linear integrated circuit used to develop various applications in electronic circuit and equipment. Hence this course is intended to develop the skills to build, test, diagnose and rectify OPAMP based electronic circuits. It also covers IC555 and its applications which plays major role in industry, consumer and domestic applications.

### ii) INDUSTRY / EMPLOYER EXPECTED OUTCOME

The aim of this course is to help the student to attain the following industry identified outcome through various learning experiences:

Maintain electronics circuits consisting of linear integrated circuits.

### E. COURSE LEVEL LEARNING OUTCOMES (COs)

ETH302-1 Select suitable OPAMP with appropriate parameters for any application.

ETH302-2 Construct various configurations of OPAMP for different applications.

ETH302-3 Make use of OPAMP for various non-linear applications.

ETH302-4 Maintain various waveform generator circuits.

ETH302-5 Design various active filters using OPAMP

ETH302-6 Use IC555 to develop various applications.

### Course outcomes and programme outcomes/ programme specific outcomes (CO- PO/PSO) matrix

[Note: Correlation levels: 1: Slight (Low), 2: Moderate (Medium), 3: Substantial (High), “-“ : No correlation]

COs	Programme Outcomes POs and PSOs								
	PO 1 Basic and Discipline specific knowledge	PO 2 Problem Analysis	PO 3 Design / Development of solutions	PO 4 Engineering Tools, Experimenta tion and Testing	PO 5 Engineering Practices for society, sustainability and Environment	PO 6 Project Managem ent	PO 7 Life- long Learning	PSO1 Operate and Maintain	PSO2 Supervision and Providing Solution
ETH302-1 Select suitable OPAMP with appropriate parameters for any application.	3	-	-	1	-	-	--	2	--
ETH302-2 Construct various configurations of OPAMP for different applications.	3	2	2	3	--	--	--	3	1
ETH302-3 Make use of OPAMP for various non-linear applications.	3	2	2	3	-	--	--	3	1
ETH302-4 Maintain various waveform	3	-	-	3	--	--	--	3	--



COs	Programme Outcomes POs and PSOs								
	PO 1 Basic and Discipline specific knowledge	PO 2 Problem Analysis	PO 3 Design / Development of solutions	PO 4 Engineering Tools, Experimenta tion and Testing	PO 5 Engineering Practices for society, sustainability and Environment	PO 6 Project Managem ent	PO 7 Life- long Learning	PSO1 Operate and Maintain	PSO2 Supervision and Providing Solution
generator circuits.									
ETH302-5 Design various active filters using OPAMP	3	3	2	3	-	--	--	3	1
ETH302-6 Use IC555 to develop various applications.	3	-	2	3	--	--	--	3	--

## F. CONTENT:

### D) Practical exercises

The following practical exercises shall be conducted in the laboratory in practical sessions of batches of about 20- 22 students. Any 20 experiments should be performed. All COs should be covered.

Sr. No.	Laboratory Experiments	CO
1.	Measure output voltage swing of IC741.	ETH302-1
2.	Build the circuit to measure input offset voltage and output offset voltage of IC741	ETH302-1
3.	Build the circuit of Inverting and non-inverting amplifier with feedback using IC741 and measure its gain.	ETH302-2
4.	Build and test the circuit of inverting adder using IC741.	ETH302-2
5.	Build and test the circuit of non-inverting adder using IC741.	ETH302-2
6.	Build and test the circuit of subtractor using IC741.	ETH302-2
7.	Build and test the circuit of integrator using IC741.	ETH302-2
8.	Build and test the circuit of differentiator using IC741.	ETH302-2
9.	Build V to I converter circuit using IC741 and measure output current.	ETH302-3
10.	Build the circuit of Zero crossing detector using IC741 and test the output.	ETH302-3
11.	Build and test performance of non-inverting comparator with positive and negative reference voltage.	ETH302-3
12.	Build and test performance of inverting comparator with positive and negative reference voltage.	ETH302-3
13.	Build the circuit of RC phase shift oscillator using IC741 and measure output frequency.	ETH302-4

Sr. No.	Laboratory Experiments	CO
14.	Build the circuit of Wein bridge oscillator using IC741 and measure output frequency.	ETH302-4
15.	Simulate the working of Quadrature oscillator using IC741 using Multisim or relevant software.	ETH302-4
16.	Build and test Astable multivibrator using IC741.	ETH302-4
17.	Simulate the circuit of Monostable multivibrator using IC741.	ETH302-4
18.	Build the circuit of first order low pass filter using IC741 and measure the bandwidth and cutoff frequency.	ETH302-5
19.	Simulate the circuit of first order high pass filter using IC741 and measure the bandwidth and cutoff frequency.	ETH302-5
20.	Build notch filter using IC741 and measure cutoff frequency.	ETH302-5
21.	Build and test Astable multivibrator using IC555.	ETH302-6
22.	Simulate the circuit of Monostable multivibrator using IC555.	ETH302-6
23.	Build and test voltage-controlled oscillator using IC555.	ETH302-6
24.	Build and test Schmitt trigger using IC555.	ETH302-6

## II) Theory

### Section I

Sr. no.	Topics/Subtopics	Learning (Hours)	Classroom learning evaluation Marks
	<b><i>ETH302-1 Select suitable OPAMP with appropriate parameters for any application.</i></b>		
<b>1</b>	<b>Basics of Operational Amplifier (OPAMP)</b> 1.1 Differential amplifier: circuit diagram and description of Dual input balanced output, Dual input unbalanced output, Single input balanced output, Single input unbalanced output differential amplifier. 1.2 Block diagram of OPAMP. 1.3 Symbol and equivalent circuit of OPAMP. 1.4 IC741 pin configuration and description 1.5 Ideal OPAMP electrical characteristics, ideal voltage transfer curve 1.6 Electrical parameters of OPAMP and its value for IC741: Input offset voltage, Input offset current, Input bias current, Differential input resistance, Input capacitance, Input voltage range, Common mode rejection ratio	<b>04</b>	<b>08</b>

	(CMRR), Supply voltage rejection ratio (SVRR), Large signal voltage gain, Output voltage swing, Output resistance, Slew rate, Supply current, Power consumption, Gain bandwidth product.		
<b><i>ETH302-2 Construct various configurations of OPAMP for different applications.</i></b>			
<b>2</b>	<b>OPAMP configurations and feedback</b> 2.1 Open loop configurations of OPAMP: Inverting, Non inverting and Differential amplifier. 2.2 Virtual ground and virtual short concept of OPAMP 2.3 Block diagram representation of feedback configurations: Voltage series, voltage shunt, current series, current shunt 2.4 Closed loop configurations of OPAMP: Voltage series feedback amplifier (Non Inverting amplifier), Voltage shunt feedback amplifier (Inverting amplifier). (Circuit diagram and derivations for voltage gain, effect of feedback on gain, input resistance, output resistance and bandwidth.) 2.5 Voltage follower, Inverter (sign changer) 2.6 Differential amplifier with one OPAMP (subtractor) 2.7 Summing, Scaling, and averaging amplifier in inverting and non-inverting configuration 2.8 Basic and practical differentiator 2.9 Basic and practical integrator. Circuit diagram, working, input output waveforms, derivation for output voltage and simple numericals based on it for all above applications of OPAMP.	<b>10</b>	<b>14</b>
<b><i>ETH302-3 Make use of OPAMP for various non-linear applications.</i></b>			
<b>3</b>	<b>OPAMP Applications</b> 3.1 Comparators: Inverting and non-inverting comparators with positive reference voltage, negative reference voltage, Zero crossing detector. 3.2 Window comparator (detector) 3.3 Schmitt trigger 3.4 Voltage to current (V to I) converter with floating load and grounded load, Current to voltage (I to V) converter. 3.5 Precision rectifier half wave and full wave. 3.6 Instrumentation amplifier using three OPAMP. 3.7 Sample and hold circuit 3.8 Peak detector Circuit diagram, working, input output waveforms, derivation for output voltage and simple numericals based on it for all above applications of OPAMP.	<b>8</b>	<b>12</b>
	<b>Sub-total</b>	<b>22</b>	<b>34</b>

## Section –II

Sr. no.	Topics/Subtopics	Learning (Hours)	Classroom learning evaluation Marks
	<b><i>ETH302-4 Maintain various waveform generator circuits.</i></b>		
<b>4</b>	<b>Waveform Generator</b> 4.1 Astable multivibrator using OPAMP. 4.2 Monostable multivibrator using OPAMP. 4.3 Bistable multivibrator using OPAMP. 4.4 Triangular wave generator using OPAMP. 4.5 Sawtooth wave generator using OPAMP. 4.6 RC phase shift oscillator using OPAMP. 4.7 Wein bridge oscillator using OPAMP. 4.8 Quadrature oscillator using OPAMP. 4.9 Voltage controlled Oscillator IC566, block diagram, pin diagram.  Circuit diagram, operation, waveforms, equations for time periods /frequency and simple numericals based on it.	<b>10</b>	<b>14</b>
	<b><i>ETH302-5 Design various active filters using OPAMP</i></b>		
<b>5</b>	<b>Active Filters</b> 5.1 Introduction to filters, classification of filters 5.2 Advantages of active filter over passive filter 5.3 Ideal and practical frequency response curve of low pass, high pass, band pass and band reject filters 5.4 Terms related to filters: order of filter, cutoff frequency, center frequency, pass band, stop band, roll off rate, bandwidth and Q factor. 5.5 First order Butterworth low pass and high pass filter. 5.6 Second order Butterworth low pass and high pass filter. 5.7 Band pass filter (wide band pass and narrow band pass). 5.8 Band reject filter (wide band and narrow band or notch filter) 5.9 All pass filter  Circuit diagram, operation, frequency response, equation for gain and cutoff frequency (no derivation). Simple numericals based on design of low pass and high pass.	<b>7</b>	<b>12</b>
	<b><i>ETH302-6 Use IC555 to develop various applications.</i></b>		
<b>6</b>	<b>Timer IC 555</b> 6.1 Block diagram of IC555.	<b>6</b>	<b>10</b>

	6.2 Pin diagram and specifications of IC555 6.3 Astable multivibrator using IC555 6.4 Monostable multivibrator using IC555 6.5 Bistable multivibrator using IC555 6.6 Schmitt trigger using IC555 6.7 Voltage control oscillator using IC555 6.8 Water level controller using IC555  Circuit diagram, operation, waveforms, equations for time periods /frequency and simple numerals based on it.		
	<b>Sub-total</b>	<b>23</b>	<b>36</b>

## G. List of Assignments under SLA

Sr. No.	Microprojects
16.	Develop clap switch using IC741
17.	Build automatic light operated switch using LDR and IC741
18.	Build automatic evening lamp using IC555
19.	Develop square wave generator using IC741
20.	Develop low pass filter for cutoff frequency 2KHz
21.	Build notch filter for frequency 5 KHz
22.	Develop water level controller using IC555
23.	Automatic street light using IC555
24.	Simple metal detector using IC555
25.	Light sensitive switch using IC741

Above is a suggestive list of microprojects. Faculty must prepare their own bank of microprojects, assignments and activities in a similar way. The faculty must allocate judicious mix of tasks, considering the weaknesses and / strengths of the student in acquiring the desired skills. If a microproject is assigned, it is expected to be completed in a group of 2 or 3 students.

## H. Specification table for setting question paper for semester end theory assessment

Section / Topic no.	Name of topic	Distribution of marks (level wise)			Total marks	CO
		Remember	Understand	Apply		
I / 1	Basics of Operational amplifier (OPAMP)	4	4		08	ETH302-1
I / 2	OPAMP configurations and feedback	4	4	6	14	ETH302-2

I / 3	OPAMP Applications		4	8	12	ETH302-3
II / 4	Waveform Generator		6	8	14	ETH302-4
II / 5	Active filters	2	4	6	12	ETH302-5
II / 6	Timer IC555		4	6	10	ETH302-6
Total Marks		10	26	34	70	

## I. Assessment Criteria

### i) Formative Assessment of Practical: -

Every assignment shall be assessed for 25 marks as per following criteria:

Domain	Particulars	Marks out of 25
Cognitive	Understanding	05
	Application	05
Psychomotor	Operating Skills	05
	Drawing / drafting skills	05
Affective	Discipline and punctuality	05
<b>TOTAL</b>		<b>25</b>

### ii) Summative Assessment of Practical:

Every practical assignment shall be assessed for 25 marks as per following criteria:

Sr. no	Criteria	Marks allotted
1	Attendance at regular practical	05
2	Preparedness for practical	05
3	Neat & complete Diagram.	05
4	Observations & handling of instrument.	05
5	Oral Based on Lab work and completion of task	05
<b>TOTAL</b>		<b>25</b>

## J. Instructional Methods:

24. Lectures cum Demonstrations
25. Class room practices
26. Use of projector and soft material for demonstration
4. Virtual Laboratory

## K. Teaching and Learning resources:

Chalk board, LCD presentations, Demonstrative kits, Demonstrative charts

### L. Reference Books:

Sr. No	Name of Book	Author	Publication
1	OPAMPs and Linear Integrated Circuits	Ramakant A Gayakwad	PHI New Delhi
2	Integrated Circuits	Botkar K R	Khanna Publisher
3	Linear Integrated Circuits	D Roy Choudhary	New Age International Publisher

### M. Learning Website & Software

- a. <https://www.monolithicpower.com/en/operational-amplifiers>
- b. <https://ae-iitr.vlabs.ac.in/List%20of%20experiments.html>
- c. [https://how2electronics.com/op-amp-ic-lm741-basics-characteristics-pins-applications/#google\\_vignette](https://how2electronics.com/op-amp-ic-lm741-basics-characteristics-pins-applications/#google_vignette)
- d. <https://archive.nptel.ac.in/courses/108/108/108108114/>
- e. <https://www.hackatronic.com/voltage-controlled-oscillator-circuit-using-566-ic/>
- f. [https://www.electronics-tutorials.ws/filter/filter\\_5.html#](https://www.electronics-tutorials.ws/filter/filter_5.html#)

**COURSE ID:**

**COURSE NAME : DIGITAL TECHNIQUES & APPLICATIONS**

**COURSE CODE : ETH303**

**COURSE ABBREVIATION : HDTA**

**A. LEARNING SCHEME:**

Scheme component		Hours	Credits
Actual Contact Hours / week	Classroom Learning	03	04
	Tutorial Learning	00	
	Laboratory Learning	04	
	SLH-Self Learning	01	
	NLH- Notional Learning	08	

**B. ASSESSMENT SCHEME: -**

PAPER DURA TION IN HRS	THEORY				BASED ON LL&TL				BASED ON SLA		Total
					Practical						
1.5	FA- TH	SA- TH	TOTAL		FA -PR		SA-PR		MA X	MI N	
	MAX	MAX	MA X	MI N	MAX	MI N	MAX	MI N			
	30*#	70*#	100	40	25	10	25#	10	25	10	175

**C. ABBREVIATIONS: -** CL- Class Room Learning, TL- Tutorial Learning, LL-Laboratory Learning, SLH-Self Learning Hours, NLH- Notional Learning Hours, FA - Formative Assessment, SA -Summative assessment, IKS - Indian Knowledge System, SLA - Self Learning Assessment

**Legends:** @ Internal Assessment, # External Assessment, \*# On Line Examination, @\$ Internal Online Examination .

1. FA-TH represents average of two class tests of 30 marks each conducted during the semester.
2. If candidate is not securing minimum passing marks in FA-PR of any course then the candidate shall be declared as "Detained" in that semester.
3. If candidate is not securing minimum passing marks in SLA of any course then the candidate shall be declared as fail and will have to repeat and resubmit SLA work.
4. Notional Learning hours for the semester are (CL+LL+TL+SL) hrs.\* 15 Weeks
5. 1(one) credit is equivalent to 30 Notional hrs.
6. \* Self learning hours shall not be reflected in the Time Table.



\* Self learning includes micro project / assignment / other activities. (Provide list of all assignments here in tabular format At least 6 to 8 assignments to be given)

#### D. i) RATIONALE: -

It is essential to know the basics of digital techniques for understanding the applications of digital systems. This course will help the student to comprehend logic and circuit design. Digital technique course provides a foundation for understanding embedded systems and microcontroller-based applications. By encouraging hands-on experimentation and project-based learning, the digital electronics course can inspire students to think creatively and apply their knowledge to develop innovative technologies and applications. This course ensures that diploma students are prepared to work with the latest digital technologies, such as microcontrollers, programmable logic devices, embedded systems, and digital communication systems.

#### ii) INDUSTRY / EMPLOYER EXPECTED OUTCOME

The aim of this course is to help the student to attain the following industry identified outcome through various learning experiences:

Test digital systems by applying principles of digital techniques

#### E. COURSE LEVEL LEARNING OUTCOMES (COs)

**ETH303-1:** Use number system and codes for interpreting working of digital system

**ETH303-2:** Use Boolean expressions to realize logic circuits

**ETH303-3:** Understand different Logic families

**ETH303-4:** Build combinational circuits, multiplexers, demultiplexers and decoders

**ETH303-5:** Build sequential circuits

**ETH303-6:** Test data converters in digital electronics systems

#### Course outcomes and Programme outcomes/ Programme specific outcomes (CO- PO/PSO) matrix

[Note: Correlation levels: 1: Slight (Low), 2: Moderate (Medium), 3: Substantial (High), “-“ : No correlation]

Cos	Programme Outcomes POs and PSOs								
	PO 1 Basic and Discipline specific knowledge	PO 2 Problem Analysis	PO 3 Design / Development of solutions	PO 4 Engineering Tools, Experimenta tion and Testing	PO 5 Engineering Practices for society, sustainabilit y and Environmen t	PO 6 Project Managemen t	PO 7 Life- long Learning	PSO1 Operate and Maintain	PSO2 Supervisi on and Providing Solution
<b>ETH303-1</b> (Use number system and codes for interpreting working of digital system)	2	3	2	1	1	-	1	1	-
<b>ETH303-2</b> (Use Boolean expressions to realize logic circuits)	2	3	3	1	1	-	1	3	2
<b>ETH303-3</b> (Understand different Logic families)	2	1	-	1	1	-	1	3	1
<b>ETH303-4</b> (Build combinational circuits, multiplexers,	2	3	3	1	1	-	1	3	3

Cos	Programme Outcomes POs and PSOs								
	PO 1 Basic and Discipline specific knowledge	PO 2 Problem Analysis	PO 3 Design / Development of solutions	PO 4 Engineering Tools, Experimenta tion and Testing	PO 5 Engineering Practices for society, sustainabilit y and Environmen t	PO 6 Project Managemen t	PO 7 Life- long Learning	PSO1 Operate and Maintain	PSO2 Supervisi on and Providing Solution
demultiplexers and decoders)									
<b>ETH303-5</b> (Build sequential circuits)	2	3	3	1	1	-	1	3	3
<b>ETH303-6</b> (Test data converters in digital electronics systems)	2	2	3	1	1	-	1	3	3

## F. CONTENT:

### I) Practical exercises

The following practical exercises shall be conducted in the *Laboratory for Digital Techniques* in practical sessions of batches of about 20- 22 students.

Sr. No.	Laboratory experiences	COs
1	Test the functionality of NOT, AND, OR logic gates using breadboard. (IC 7404,7408,7432)	ETH303-2
2	Test the functionality of NAND & NOR logic gates using breadboard. ( IC 7400 & IC 7402)	ETH303-2
3	Test the functionality of EX-OR & EX-NOR logic gates using breadboard.	ETH303-2
4	Construct AND, OR & NOT gates using Universal gates NAND & NOR and verify their truth tables.	ETH303-2
5	Build and test the logic circuit on breadboard to check De Morgan's theorems for 2 input variables.	ETH303-2
6	Design Half adder & Full adder using Boolean expressions	ETH303-4
7	Design Half subtractor & Full subtractor using logic gates	ETH303-4
8	Construct & test BCD to 7-segment decoder using IC 7447/7448	ETH303-4
9	Design and implementation of 4-bit binary adder/subtractor and BCD adder using IC 7483	ETH303-4
10	Build & test function of MUX using IC 74151/74150/any other equivalent IC	ETH303-4
11	Build & test function of DeMUX using IC 74138/74139/any other equivalent IC	ETH303-4
12	Design 8:1 MUX using 4:1 MUX using circuit simulator.	ETH303-4
13	Build & test function of decoder IC 74155/74154/any other IC	ETH303-4
14	To study and Implement 4-Bit Magnitude Comparator using IC- 74LS85	ETH303-4
15	Build/ test function of RS flip flop using NAND gate	ETH303-5
16	Build & test function of JK flip flop using IC 7476	ETH303-5
17	Build and test the functionality of D flip flop	ETH303-5
18	Build and test the functionality of T flip flop	ETH303-5

19	Build 4-bit Right Shift Register using D flip flop IC 7474	ETH303-5
20	Implement 4-bit ripple counter using IC 7476	ETH303-5
21	Build and test MOD-10 counter using IC 7490	ETH303-5
22	Build and test UP/DOWN counter using IC 74193	ETH303-5
23	Build an R-2R ladder DAC circuit using precision resistors	ETH303-6
24	Build weighted resistor DAC circuit using resistors	ETH303-6
25	Construct a Dual slope ADC circuit and apply an analog input voltage	ETH303-6

## II) Theory

### Section I

Sr. No.	Topics	Teaching hours	Marks
<b>ETH303-1: Use number system and codes for interpreting working of digital system.</b>			
<b>1</b>	<b>Number system &amp; codes</b> 1.1 Introduction to number system: Binary, Octal, Decimal Hexadecimal, unsigned and signed numbers 1.2 Conversion from one number system to other number system 1.3 Binary arithmetic operations: rules of binary addition, binary subtraction, binary subtraction using 2's complement. 1.4 Computer codes: BCD, ASCII Code, Gray code and Excess-3 code 1.5 Code Conversion: Gray to binary and binary to gray conversion (up to 4 bits) 1.6 BCD Arithmetic: BCD addition, BCD subtraction using 9's and 10's complement. 1.7 Parity: even and odd parity	<b>06</b>	<b>10</b>
<b>ETH303-2: Use Boolean expressions to realize logic circuits.</b>			
<b>2</b>	<b>Boolean Algebras:</b> 2.1 Boolean algebra (rules, laws, Boolean expressions) 2.2 De Morgan's Theorem 2.3 Logic gates: Symbol, logical expression, truth table and ICs of basic logic gates (AND, OR, NOT) Universal gates (NAND and NOR) and Special purpose gates (EX- OR, EX-NOR), realization of basic gates using universal gates. 2.4 Standard Boolean representation: Sum of Product (SOP) and Product of Sum (POS), Min-term and Max-term, Conversion between SOP and POS forms 2.5 K map reduction technique: Minimization of Boolean functions up to 4 variables (SOP and POS form), realization using NAND/NOR gates, don't care condition	<b>10</b>	<b>16</b>
<b>ETH303-3: Understand different Logic families</b>			
<b>3</b>	<b>Digital Logic Families</b> 3.1 Characteristics of logic families: fan in, fan out,	<b>06</b>	<b>08</b>

	propagation delay, power dissipation, noise margin 3.2 TTL family: Circuit diagram & working of TTL NAND gate 3.3 CMOS family: a) Circuit diagram & working of CMOS inverter b) Circuit diagram & working of CMOS NAND & NOR gates( 2 inputs) 3.4 Comparison of logic families TTL & CMOS.		
	<b>Sub total ( Section I)</b>	<b>22</b>	<b>34</b>

### Section II

Sr. No.	Topics	Teaching hours	Marks
<b>ETH303-4: Build combinational circuits, multiplexers, demultiplexers and decoders</b>			
<b>4.</b>	<b>Combinational circuits</b> 4.1 Design of arithmetic circuits using K-map: Half and full Adder, half and full Subtractor 4.2 Adder and subtractor using IC7483, BCD adder / subtractor 4.3 BCD to 7 segment decoder/driver (IC 7447 /7448) 4.4 Binary to gray code converter and gray to binary code converter. 4.5 Comparator: 1 bit and 2 bit (design and realization using K map), 4 bit comparator using IC7485. 4.6 <b>Multiplexer:</b> Necessity, types of multiplexers 2:1, 4:1, 8:1, 16:1 (realization using gates and ICs 74150, 74151), Multiplexer tree 4.7 <b>Demultiplexer:</b> Necessity, types of demux 1:2, 1:4, 1:8, 1:16 (realization using gates), Demultiplexer tree 4.8 <b>Decoder:</b> concept of decoder, 2 line to 4 line, 3 line to 8 line, 4 line to 16 line decoder (realization using gates and ICs 74154, 74155)	<b>10</b>	<b>16</b>
<b>ETH303-5: Build Sequential circuits</b>			
<b>5</b>	<b>Sequential Logic Circuits</b> 5.1 <b>Basic memory cell:</b> RS-latch using NAND and NOR 5.2 <b>Triggering Methods:</b> Edge trigger and level trigger 5.3 <b>SR Flip Flops:</b> SR-flip flop, clocked SR flip flop with preset and clear 5.4 <b>JK Flip Flops:</b> Clocked JK Flip flop with preset and clear, race around condition in JK flip flop, Master slave JK flip flop. D flip flop and T type flip flop, Block schematic and function table of IC-7474, IC-7475 5.5 Shift Register Logic diagram of 4- bit Shift registers: Serial Input Serial Output, Serial Input Parallel Output, Parallel Input Serial Output, Parallel Input Parallel Output, 4 Bit Universal Shift register	<b>08</b>	<b>12</b>

	5.6 <b>Counters:</b> Asynchronous counter using T flipflop: 4-bit Ripple counter, 4 bit up/down Counter, modulus of counter, Block schematic of IC 7490, Decade counter, IC 7490 as MOD N Counter (using 4 bits)		
<b>ETG 306-6: Test data converters in digital electronics systems</b>			
<b>6.</b>	<b>Data converters</b> 6.1 DAC Types – Weighted resistor method and R-2R method, specifications of DAC 6.2 Types of ADC, specifications of ADC, block diagram and working of Dual slope ADC and Successive approximation ADC 6.3 Numericals on DAC and ADC	<b>05</b>	<b>08</b>
	<b>Sub total ( Section II)</b>	<b>23</b>	<b>36</b>

### Assignments under SLA

Sr. No.	List of Assignment (under SLA)	Hours allotted
1.	Convert the decimal number 156 to binary, octal, and hexadecimal.	02
2.	Convert the binary number 110101 to decimal, octal, and hexadecimal.	02
3.	Convert the octal number 245 to binary, decimal, and hexadecimal.	02
4.	Convert the hexadecimal number 3F2 to binary, decimal, and octal.	02
5.	Implement a 2-input AND gate using logic gates and create its truth table.	02
6.	Implement a 3-input OR gate using logic gates and create its truth table.	02
7.	Implement a 4-input XOR gate using logic gates and create its truth table.	02
8.	Design and implement an SR flip-flop using NAND gates.	02
9.	Design and implement a D flip-flop using NOR gates.	02
10.	Design and implement a 4-bit shift register using D flip-flops.	02
11.	Design a 4-to-1 multiplexer using logic gates and verify its functionality.	02
12.	Design a 3-bit binary counter using flip-flops and verify its counting sequence.	02

\*\*Out of 12, eight assignments covering all six COs are compulsory. As per the requirement course teacher can modify the assignments.

### G. Specification table for setting question paper for semester end theory assessment

Section / Topic no.	Name of topic	Distribution of marks (level wise)			Total marks	CO
		Rememb er	Understan d	Apply		
<b>I / 1</b>	<b>Number system &amp; codes</b>	2	2	6	10	<b>ETH101-1</b>
<b>I / 2</b>	<b>Boolean Algebra</b>	2	6	8	16	<b>ETH101-2</b>

<b>I / 3</b>	<b>Digital Logic Families</b>	<b>2</b>	<b>6</b>	<b>-</b>	<b>08</b>	<b>ETH101-3</b>
<b>II / 4</b>	<b>Combinational Circuits, Multiplexers, demultiplexers and decoders</b>	<b>2</b>	<b>6</b>	<b>8</b>	<b>16</b>	<b>ETH101-4</b>
<b>II / 5</b>	<b>Sequential Logic Circuits</b>	<b>2</b>	<b>4</b>	<b>6</b>	<b>12</b>	<b>ETH101-5</b>
<b>II / 6</b>	<b>Data converters</b>	<b>2</b>	<b>2</b>	<b>4</b>	<b>08</b>	<b>ETH101-6</b>
<b>Total Marks</b>		<b>12</b>	<b>26</b>	<b>32</b>	<b>70</b>	

## H. Assessment Criteria:-

### i) Formative Assessment of Practical: -

Every assignment shall be assessed for 25 marks as per following criteria:

<b>Domain</b>	<b>Particulars</b>	<b>Marks out of 25</b>
Cognitive	Understanding	05
	Application	05
Psychomotor	Operating Skills	05
	Drawing / drafting skills	05
Affective	Discipline and punctuality	05
<b>TOTAL</b>		<b>25</b>

### ii) Summative Assessment of Practical:

Every practical assignment shall be assessed for 25 marks as per following criteria:

<b>Sr. no</b>	<b>Criteria</b>	<b>Marks allotted</b>
1	Attendance	05
2	Circuit building, handling of ICs	05
3	Observation & Conclusion	05
4	Oral based on performed practical	05
5	Analytical, Logical problem solving skills, Creativity skills, time management	05
<b>TOTAL</b>		<b>25</b>

## I. Instructional Methods:

1. Classroom Learning
2. Active Learning
3. Collaborative Learning
4. Experimental Learning
5. Virtual Laboratory

## J. Teaching and Learning resources:

Chalk board, LCD presentations, Demonstrative kits, Demonstrative charts

### K. Reference Books:

Sr. No	Name of Book	Author	Publication
1	Digital Design	M. Morris Mano	Pearson Education
2	Modern Digital Electronics	R.P. Jain	McGraw Hill
3	Fundamentals of Digital Circuits	Kumar Anand and Bhupendra Kumar Singh	PHI Learning Pvt. Ltd.

### L. Learning Website & Software

1. Electronics Hub: [electronicsforu.com](http://electronicsforu.com)
2. All About Circuits: [allaboutcircuits.com](http://allaboutcircuits.com)
3. Virtual Lab: <https://dec-iitkgp.vlabs.ac.in/>
4. Digital Electronics Course by NPTEL: [nptel.ac.in](http://nptel.ac.in) (NPTEL's official website)
5. Multisim by National Instruments: [ni.com/Multisim](http://ni.com/Multisim)
6. MATLAB and Simulink: [mathworks.com/products/matlab.html](http://mathworks.com/products/matlab.html)

## COURSE ID

**COURSE NAME : ANALOG COMMUNICATION**

**COURSE CODE : ETH304**

**COURSE ABBREVIATION : HACM**

### A. LEARNING SCHEME:

Scheme component		Hours	Credits
Actual Contact Hours / week	Classroom Learning	03	03
	Tutorial Learning	00	
	Laboratory Learning	02	
	SLH-Self Learning	01	
	NLH-Notional Learning	06	

### B. ASSESSMENT SCHEME:-

PAPER DURATION IN HRS	THEORY				BASED ON LL&TL				BASED ON SLA		Total
					Practical						
03	FA-TH	SA-TH	TOTAL		FA -PR		SA-PR		MAX	MIN	175
	MAX	MAX	MAX	MIN	MAX	MIN	MAX	MIN			
	30	70	100	40	25	10	25@	10	25	10	

**C. ABBREVIATIONS: -** CL- Class Room Learning, TL- Tutorial Learning, LL-Laboratory Learning, SLH-Self Learning Hours, NLH-Notional Learning Hours, FA - Formative Assessment, SA -Summative assessment, IKS - Indian Knowledge System, SLA - Self Learning Assessment

**Legends:** @ Internal Assessment, # External Assessment, \*# On Line Examination, @\$ Internal Online Examination.

1. FA-TH represents average of two class tests of 30 marks each conducted during the semester.
2. If candidate is not securing minimum passing marks in FA-PR of any course then the candidate shall be declared as "Detained" in that semester.
3. If candidate is not securing minimum passing marks in SLA of any course then the candidate shall be declared as fail and will have to repeat and resubmit SLA work.
4. Notional Learning hours for the semester are (CL+LL+TL+SL) hrs.\* 15 Weeks
5. 1(one) credit is equivalent to 30 Notional hrs.
6. \* Self learning hours shall not be reflected in the Time Table.

\* Self learning includes micro project / assignment / other activities. (Provide list of all assignments here in tabular format At least 6 to 8 assignments to be given)



## D. i) RATIONALE: -

Analog Communication is the subject that presents information about the basic processes, circuits and other building blocks of communication system. The study of basic operating and handling of various analog communication systems will help to troubleshoot and maintain analog communication systems used for various types of communication.

## ii) INDUSTRY / EMPLOYER EXPECTED OUTCOME

The aim of this course is to help the student to attain the following industry identified outcome through various learning experiences:

3. Identify and measure electrical parameters of analog communication circuits.
4. Maintain and operate analog communication circuits.

## E. COURSE LEVEL LEARNING OUTCOMES (COs)

**ETH304-1:** Choose relevant frequency ranges for different communication systems

**ETH304-2:** Match relevant modulation techniques for specific applications.

**ETH304-3:** Select Frequency modulation and Phase modulation for specific applications

**ETH304-4:** Maintain receiver circuits of Am and FM

**ETH304-5:** Identify relevant type Antenna for various applications.

**ETH304-6:** Use relevant media for transmission and reception of signals.

### Course outcomes and programme outcomes/ programme specific outcomes (CO- PO/PSO) matrix

[Note: Correlation levels: 1: Slight (Low), 2: Moderate (Medium), 3: Substantial (High), “-“ : No correlation]

Cos	Programme Outcomes POs and PSOs								
	PO 1 Basic and Discipline specific knowledge	PO 2 Problem Analysis	PO 3 Design / Development of solutions	PO 4 Engineering Tools, Experimenta tion and Testing	PO 5 Engineering Practices for society, sustainability and Environment	PO 6 Project Managem ent	PO 7 Life- long Learning	PSO1 Operate and Maintain	PSO2 Supervision and Providing Solution
Competency: Explain, operate and maintain different Analog communication systems.	2	1	2	1	-	-	-	1	1
ETH304-1	1	-	-	-	-	-	-	-	-
ETH304-2	2	-	2	-	-	-	-	-	1
ETH304-3	2	-	1	1	-	-	-	-	1
ETH304-4	1	2	2	-	-	-	-	1	1
ETH304-5	-	1	-	-	-	-	-	-	-
ETH304-6	-	1	-	-	-	-	-	-	-

## F. CONTENT:

### D) Practical exercises

The following practical exercises shall be conducted in the *Laboratory for AC Machine developed* by the Institute in practical sessions of batches of about 20- 22 students:

Sr. No.	Laboratory experiences	CO
1.	Generate Amplitude modulation Wave Trace the circuit identify carrier, Modulating signal and modulated signal.	ETH 304-2
2.	Observation & measurements of AM wave Observe AM waveform on CRO Calculate modulating index	ETH 304-2
3.	Study trapezoidal pattern Observe various trapezoidal patterns on CRO. Calculate modulating index using this pattern	ETH 304-2, 4
4.	Demodulation of FM using Diode detector Trace the circuit, Identify Modulated and demodulated signal and observe waveforms on CRO.	ETH 304-2, 3
5.	Generation of Frequency modulated waveform Trace the circuit, identify carrier, modulating signal and modulated signal.	ETH 304-2, 3 6
6.	Observation and measurement of FM wave Observe FM waveforms on CRO Calculate modulating index	ETH 304-2, 3
7.	Demodulation of FM signal Observe FM and FM demodulated waveforms on CRO. Observe similarities in modulated signal and demodulated signal.	ETH 304-2, 4
8.	Visit to AM Transmitter Understand AM transmitter and prepare a project report.	ETH 304-1, 3, 5
9.	Visit to FM Transmitter Understand FM transmitter and prepare a project report	ETH 304-1, 3, 5
10.	Study of super heterodyne radio receiver Identify different blocks & expected waveforms of superheterodyne radio receiver, Identify different controls and their functions and measure the voltages at different check points.	ETH 304-1-4
11.	Fault finding in Super heterodyne receiver Locate faults using voltage and waveform technique at different check points and correct them.	ETH 304-1-4
12.	To plot selectivity and fidelity curve for superheterodyne radio receiver Give input AM wave from signal generator to the circuit and observe output on CRO. Plot the selectivity & fidelity curve by varying carrier frequency.	ETH 304-4,
13.	Study of FM Receiver Identify different blocks & expected waveforms of slope or ratio detector. Identify different controls and their functions and measure the voltages at different check points	ETH 304-4, 5
14.	Study and design Of Yagi-Uda Antenna Draw diagram of yagi_uda antenna. Identify and describe parts of yagi_uda antenna.	ETH 304-6, 5
15.	Study and design of Parabolic reflector (Dish Antenna) Draw diagram of Dish antenna. Identify and describe parts of Dish antenna for particular $\lambda$	ETH 304-6

## II) Theory

### Section I

Sr. no.	Topics/Subtopics	Learning (Hours)	Classroom learning evaluation Marks
	<b><i>ETH304-1 Choose relevant frequency ranges for different communication systems.</i></b>		
<b>1</b>	<b>Introduction to electronic communication system</b> 1.1 Elements of basic electronic communication system 1.2 Classification of electronic communication systems into Wired and Wireless, Uni-cast and Broadcast, simplex, half duplex and full duplex. 1.3 Types of transmission media(Wired transmission). 1.3 Noise Fundamentals, Types, Noise figure, Noise Temperature, numerical based on noise figure and noise Temperature. 1.4 Electromagnetic Frequency spectrum 1.5 Bandwidth and Information Capacity 1.6 Modulation and Demodulation (Definition, Need of Modulation)	<b>6</b>	<b>08</b>
	<b><i>ETH304-2 Match relevant modulation techniques for specific applications.</i></b>		
<b>2</b>	<b>Amplitude Modulation and SSB Techniques.</b> 2.1 Amplitude modulation theory. 2.2 Sidebands, Frequency domain representation and bandwidth of AM wave 2.3 Time domain representation of AM wave and Trapezoidal pattern. 2.4 Power relation in AM wave. 2.5 Amplitude modulator circuits. 2.6 AM Transmitters – Low level and High level 2.7 Single side band technique (SSB) 2.7. 1 Advantages and disadvantages of SSB 2.7.2 Suppression of carrier. 2.7.3 Suppression unwanted side band. 2.8 Concept of vestigial sideband & waveforms 2.9 Numerical problems based on AM & SSB theory.	<b>9</b>	<b>14</b>
	<b><i>ETH304-3 Select Frequency modulation and Phase modulation for specific applications</i></b>		
<b>3</b>	<b>Angle Modulation and FM Transmitters</b> 3.1 Frequency modulation and Phase modulation theory. 3.2 Mathematical representation of FM and PM 3.3 FM and PM waveforms. 3.4 Difference between FM and PM 3.5 Modulation index, Deviation ratio, Bandwidth, Power Considerations. 3.6 Generation of FM – Direct and Indirect methods 3.7 FM transmitters - Direct and Indirect 3.8 Comparison between AM and Angle modulation 3.9 Numerical problems based on FM and PM theory.	<b>7</b>	<b>12</b>
	<b>Sub-total</b>	<b>22</b>	<b>34</b>

## Section –II

Sr. no.	Topics/Subtopics	Learning (Hours)	Classroom learning evaluation Marks
	<b><i>ETH304-4 Maintain receiver circuits of Am and FM</i></b>		
<b>4</b>	<b>Radio Receivers</b> 4.1 AM Receivers 4.1.1 TRF receivers, Super heterodyne receiver. 4.1.2 Receiver performance characteristics 4.1.3 RF section 4.1.4 Frequency mixer and down converters 4.1.5 Image frequency and its rejection 4.1.6 IF amplifiers 4.1.7 AM detector 4.1.8 AGC circuits. 4.2 FM receivers: 4.2.1 FM discriminators – slope detector, ratio detector, PLL detector (Basic working of PLL to be covered) 4.2.2 Pre-emphasis and De-emphasis	<b>10</b>	<b>14</b>
	<b><i>ETH304-5 Identify relevant type Antenna for various applications</i></b>		
<b>5</b>	<b>Antennas</b> 5.1 Radiation Mechanism. 5.2 Radiation pattern 5.3 Antenna gain, resistance, polarization, beam width, bandwidth 5.4 Resonant and non-resonant antennas. 5.5 Half wave dipole 5.6 Loop antenna. 5.7 Helical antenna. 5.8 Yagi-Uda antenna. 5.9 Parabolic reflector antenna	<b>5</b>	<b>10</b>
	<b><i>ETH304-6 Use relevant media for transmission and reception of signals</i></b>		
<b>6</b>	<b>Electromagnetic Wave Propagation</b> 6.1 Electromagnetic waves and polarization 6.2 Reflection, refraction, diffraction of waves 6.3 Ground (surface) waves propagation. 6.4 Space wave propagation. 6.5 Sky wave propagation 6.5.1 Virtual height. 6.5.2 Critical frequency and critical angle 6.5.3 Skip distance. 6.5.4 Maximum usable frequency. 6.6 Fading	<b>8</b>	<b>12</b>
	<b>Sub-total</b>	<b>23</b>	<b>36</b>

### III) List of Assignments under SLA

Sr. No.	List of Assignment (under SLA)	Hours allotted
1.	Write procedure to measure AC and DC Amplitude, time period and frequency using CRO and function generator.	02
2.	Tabulate important characteristics of commonly available analog communication system	02
3.	Tabulate important characteristics of AM modulation..	02
4.	Tabulate important characteristics of , FM, PM modulation	02
5.	Write a procedure to generate amplitude modulated signal.	02
6.	Draw a chart showing waveform of AM, FM, PM modulation	02
7.	Study of SSB techniques	02
8.	Study of different types of Radio Receivers.	02
9.	Draw a chart of different types of antenna.	02
10.	Study different types of Electromagnetic Wave Propagation.	02
11.	Draw a chart different type of Radio Receivers.	02
12.	Study of modulation and demodulation concept.	02
13.	Study different types of antenna.	02
14.	Draw a radiation pattern of different types of antenna.	02
15.	Study working of Pre-emphasis and De-emphasis	02

\*\*Out of 12 eight assignments covering all six COs are compulsory. As per the requirement course teacher can modify the assignments.

### G. Specification table for setting question paper for semester end theory assessment

Section / Topic no.	Name of topic	Distribution of marks (level wise)			Total marks	CO
		Remember	Understand	Apply		
I / 1	<b>Introduction to electronic communication system</b>	4	4	=	8	ETH304--1
I / 2	<b>Amplitude Modulation and SSB Techniques</b>	4	4	6	14	ETH304--2
I / 3	<b>Angle Modulation and FM Transmitters</b>	2	6	4	12	ETH304--3
II / 4	<b>Radio Receivers</b>	2	8	4	14	ETH304--4
II / 5	<b>Antennas</b>	4	4	2	10	ETH304--5
II / 6	<b>Electromagnetic Wave Propagation</b>	4	6	2	12	ETH304--6
Total Marks		20	32	18	70	

## H. Assessment Criteria

### i) Formative Assessment of Practical: -

Every assignment shall be assessed for 25 marks as per following criteria:

Domain	Particulars	Marks out of 25
Cognitive	Understanding	05
	Application	05
Psychomotor	Operating Skills	05
	Drawing / drafting skills	05
Affective	Discipline and punctuality	05
<b>TOTAL</b>		<b>25</b>

### ii) Summative Assessment of Practical:

Every practical assignment shall be assessed for 25 marks as per following criteria:

Sr. no	Criteria	Marks allotted
1	Attendance at regular practical	05
2	Preparedness for practical	05
3	Neat & complete Diagram.	05
4	Observations & handling of instrument.	05
5	Oral Based on Lab work and completion of task	05
<b>TOTAL</b>		<b>25</b>

## I. Instructional Methods:

27. Lectures cum Demonstrations
28. Class room practices
29. Use of projector and soft material for demonstration
4. Virtual Laboratory

## J. Teaching and Learning resources:

Chalk board, LCD presentations, Demonstrative kits, Demonstrative charts

## K. Reference Books:

Sr. No	Name of Book	Author	Publication
1.	Electronic Communication Systems	Wayne Tomasi	Pearson Publication
2.	Communication Electronics	Louis Frenzel	TMH Publication
3.	Electronic Communication Systems	Kennedy.	TMH Publication
4.	Electronic Communication	Roddy Coolen	PHI Publication

## L. Learning Website & Software

- 1) [www.nptel.ac.in](http://www.nptel.ac.in)
- 2) [www.antenna-theory.com](http://www.antenna-theory.com)
- 3) [www.explainthatstuff.com/antennas.html](http://www.explainthatstuff.com/antennas.html)
- 4) [www.circuitstoday.com/single-chip-fm-radio-circuit](http://www.circuitstoday.com/single-chip-fm-radio-circuit)

**COURSE ID :**

**COURSE NAME : ELECTRONICS MEASUREMENTS & INSTRUMENTATION**

**COURSE CODE : ETH305**

**COURSE ABBREVIATION: HEMI**

**A. LEARNING SCHEME:**

Scheme component		Hours	Credits
Actual Contact Hours / week	Classroom Learning	03	03
	Tutorial Learning	00	
	Laboratory Learning	02	
	SLH-Self Learning	01	
	NLH- Notional Learning	06	

**B. ASSESSMENT SCHEME:-**

PAPER DURATION IN HRS	THEORY				BASED ON LL&TL				BASED ON SLA		Total
					Practical						
-	FA-TH	SA-TH	TOTAL		FA -PR		SA-PR		MAX	MIN	
	MAX	MAX	MAX	MIN	MAX	MIN	MAX	MIN			
	-	-	-	-	-	50	20	25@	10	25	10

**C. ABBREVIATIONS:** - CL- Class Room Learning, TL- Tutorial Learning, LL-Laboratory Learning, SLH-Self Learning Hours, NLH- Notional Learning Hours, FA - Formative Assessment, SA -Summative assessment, IKS - Indian Knowledge System, SLA - Self Learning Assessment

**Legends:** @ Internal Assessment, # External Assessment, \*# On Line Examination,

@\$ Internal Online Examination .

1. FA-TH represents average of two class tests of 30 marks each conducted during the semester.
2. If candidate is not securing minimum passing marks in FA-PR of any course then the candidate shall be declared as "Detained" in that semester.
3. If candidate is not securing minimum passing marks in SLA of any course then the candidate shall be declared as fail and will have to repeat and resubmit SLA work.
4. Notional Learning hours for the semester are (CL+LL+TL+SL) hrs.\* 15 Weeks
5. 1(one) credit is equivalent to 30 Notional hrs.
6. \* Self learning hours shall not be reflected in the Time Table.

\* Self learning includes micro project / assignment / other activities. (Provide list of all assignments here in tabular format At least 6 to 8 assignments to be given)

### D. i) RATIONALE: -

Diploma students must have the skill to demonstrate the various electronic measuring instruments while maintaining various electronic equipment/systems. This subject intends to provide the students practical information & technical background. It also provides the students with concepts, principles and procedures of Analog and Digital electronic measuring instruments and the measurement techniques for the measurement of various electronic quantities. Because of the scope of the subject, students are well exposed to a good and wide area of the various electronic measuring instruments as the subject comprises of those basic equipment and transducers of which students should have knowledge.

### ii) INDUSTRY / EMPLOYER EXPECTED OUTCOME

The aim of this course is to attend following industry identified competency through various teaching learning experiences:

- Demonstrate and maintain electronic measuring equipments while troubleshooting of electronic circuits.

### E. COURSE LEVEL LEARNING OUTCOMES (COs)

ETH305-1 Describe various characteristics of measuring instruments.

ETH305-2 Determine the unknown values of components using bridges and demonstrate various digital meters.

ETH305-3 Explain & demonstrate the use of various test instruments.

ETH305-4 Interpret working of various types of sensors and transducers.

ETH305-5 Interpret working of various types of sensors and transducers.

ETH305-6 Maintain signal conditioning and data acquisition systems.

### Course outcomes and programme outcomes/ programme specific outcomes(CO- PO/PSO) matrix

[Note: Correlation levels: 1: Slight (Low), 2: Moderate (Medium), 3: Substantial (High), “-“ : No correlation]

Cos	Programme Outcomes POs and PSOs								
	PO 1 Basic and Discipline specific knowledge	PO 2 Problem Analysis	PO 3 Design / Development of solutions	PO 4 Engineering Tools, Experimenta tion and Testing	PO 5 Engineering Practices for society, sustainability and Environment	PO 6 Project Managem ent	PO 7 Life- long Learning	PSO1 Operate and Maintain	PSO2 Supervision and Providing Solution
ETH305-1	3	-	-	2	-	-	--	1	1
ETH305-2	3	2	-	3	--	--	--	3	1
ETH305-3	3	2	-	3	-	--	--	3	1
ETH305-4	3	2	-	3	--	--	1	2	1
ETH305-5	3	2	-	3	-	--	1	3	1
ETH305-6	3	2	--	3	--	--	1	3	1



## F. CONTENT:

### I) Practical exercises

The following practical exercises shall be conducted in the *Laboratory for AC Machine* developed by the Institute in practical sessions of batches of about 20- 22 students:

(Any 10 )

Sr. No.	Laboratory experiences	CO
1.	Demonstrate the use of analog and digital multimeter.	ETH 305-2
2.	Demonstrate the use of LCR--Q meter	ETH 305-2
3.	Demonstrate the use of CRO as component tester.	ETH 305-3
4.	Identify various types of transducers.	ETH 305-4
5.	Demonstrate the use CRO for measurement of AC/DC voltage & frequency.	ETH 305-3
6.	Test performance of inductive transducer LVDT.	ETH 305-5
7.	Demonstrate the use of CRO for measurement of phase & frequency using Lissajous figures.	ETH 305-3
8.	Demonstrate the use of function generator.	ETH 305-3
9.	Study of whetstone's bridge for measurement of unknown resistances.	ETH 305-2
10.	Demonstrate the use of detectors for Ac bridge like headphone.	ETH 305-2
11.	Measurement of unknown capacitance using bridge.	ETH 305-2
12.	Measurement of unknown inductance using bridge.	ETH 305-2
13.	Demonstrate the use of use of frequency meter	ETH 305-2
14.	Demonstrate the use of any type of temperature transducer.	ETH 305-5
15.	Demonstrate any data acquisition system. (Use lab setup/video/visit).	ETH 305-6

## II) Theory

### Section I

Sr. no.	Topics/Subtopics	Learning (Hours)	Classroom learning evaluation Marks
	<b><i>ETH305-1 Describe various characteristics of measuring instruments.</i></b>		
<b>1</b>	<b>Fundamentals of Measuring Instruments</b> 1.1 Classification of Instruments: Absolute , Secondary Instruments 1.2 Definitions of Static characteristics of Instruments: Accuracy, Precision, Sensitivity, Resolution, Static error, Reproducibility, Drift, Dead Zone. 1.3 Definitions of dynamic characteristics of Instruments: Speed of response, Lag, fidelity, Dynamic error. 1.4 Types of Errors- Gross, Systemic, Random 1.5 Units of measurement of fundamental quantity. 1.6 Definition of Standards and their classification.	<b>6</b>	<b>NA</b>

	<b><i>ETH305-2 Determine the unknown values of components using bridges and demonstrate various digital meters.</i></b>		
<b>2</b>	<b>Measurement of Electrical Parameters</b> 2.1 Bridge balance condition for DC bridge. 2.2 Block diagram, principle of working and numerical of DC bridges: Whetstone's bridge, Guarded whetstone's bridge, Kelvin's bridge. 2.3 Block diagram, principle of working and numerical of AC bridges: Capacitance comparison bridge, Inductance comparison bridge, Maxwell's bridge, Hay bridge, Schering's bridge, Wien's bridge. 2.4 Concepts of ADC & DAC 2.5 Advantages and Disadvantages of Digital Instruments and comparison with analog instruments. 2.6 Definition of Average & RMS value. 2.7 PMMC- Working Principle, Construction. 2.8 Resolution, Sensitivity and Accuracy of digital display. 2.9 Digital Voltmeter-Successive approximation type, Digital frequency meter, LCR, Q meter- Block diagram and operation only	<b>8</b>	<b>NA</b>
	<b><i>ETH305-3 Explain &amp; demonstrate the use of various test instruments.</i></b>		
<b>3</b>	<b>Test &amp; Measuring Instruments</b> 3.1 Oscilloscope subsystems- 3.1.1 Display subsystems- CRT, Deflection of electron beam in CRT, sensitivity. 3.1.2 Vertical deflection subsystems- Input Coupling selector, Input attenuator, Pre-amplifier, Main vertical amplifier, delay line. 3.1.3 Horizontal deflection subsystems- Trigger circuit, Time base generator, Main Horizontal amplifier. 3.1.4 CRO Probes- General block diagram of CRO probe, passive voltage probe, and their compensation, Active voltage probes, current probes. 3.2 CRO-Block diagram of single beam dual trace and dual beam oscilloscope. 3.3 Block diagram of Digital storage oscilloscope. 3.4 Uses of CRO- Frequency and phase measurement, Tracing of diode and transistor characteristics. 3.5 Signal generator-AF and RF type- Block diagram and Operation only. 3.6 Function generator - Block diagram, Simple controls and operation only. 3.7 Specifications. 3.8 Concept of time domain and frequency domain Instruments. 3.9 Spectrum & Logic analyzer- Block diagram and Operation only.	<b>9</b>	<b>NA</b>
	<b>Sub-total</b>	<b>23</b>	<b>--</b>

## Section –II

Sr. no.	Topics/Subtopics	Learning (Hours)	Classroom learning evaluation Marks
	<b><i>ETH305-4 Interpret working of various types of sensors and transducers.</i></b>		
<b>4</b>	<b>Sensors and Transducers</b> 4.1 Instrumentation System: Block diagram of Instrumentation system, function of each block. 4.2 Sensors and Transducer: definition, difference between sensors and transducers, classification of sensors 4.3 Thermal, optical, magnetic and electric sensors: working principle and applications 4.4 Transducer: Need for Transducer, selection criteria of transducer, types: primary and secondary, active and passive, analog and digital, resistive, capacitive, inductive (Linear variable differential transformer (LVDT), Rotary variable differential transformer (RVDT), Piezo electric transducer	<b>9</b>	<b>NA</b>
	<b><i>ETH305- 5 Measure physical quantities using various types of transducers and sensors.</i></b>		
<b>5</b>	<b>Application of Sensors and Transducers</b> 5.1 Temperature measurement types: Resistance Temperature Detector (RTD) – (PT-100), Thermistors, Thermocouple – Seebeck & Peltier effect, Type J, K, R, S, T etc. (Based on material, temperature ranges) 5.2 Pyrometer– Optical type 5.3 Pressure measurement types: Bourdon Tube, Bellows, Diaphragm 5.4 Flow measurement types: variable head flow meter, venturimeter, orifice plate. Variable area flow meter: Rotameter, electromagnetic flow meter. 5.5 Special transducers and measurement: Humidity measurement using hygrometer, pH measurement	<b>9</b>	<b>NA</b>
	<b><i>ETH305-6 Maintain signal conditioning and data acquisition systems.</i></b>		
<b>6</b>	<b>Data Acquisition System</b> 6.1 Signal conditioning: Introduction, types, block diagram and working of AC and DC signal conditioning circuits. 6.2 Data Acquisition Systems (DAS): Introduction, block diagram, working and applications of DAS	<b>4</b>	<b>NA</b>
	<b>Sub-total</b>	<b>22</b>	<b>--</b>

## G. Assessment Criteria

### i) Formative Assessment of Practical: -

Every assignment shall be assessed for 50 marks as per following criteria:

Domain	Particulars	Marks out of 50
Cognitive	Understanding	10
	Application	10
Psychomotor	Operating Skills	10
	Drawing / drafting skills	10
Affective	Discipline and punctuality	10
<b>TOTAL</b>		<b>50</b>

### ii) Summative Assessment of Practical:

Every practical assignment shall be assessed for 25 marks as per following criteria:

Sr. no	Criteria	Marks allotted
1	Attendance at regular practical	05
2	Preparedness for practical	05
3	Neat & complete Diagram.	05
4	Observations & handling of instrument.	05
5	Oral Based on Lab work and completion of task	05
<b>TOTAL</b>		<b>25</b>

## H. Instructional Methods:

4. Lectures cum Demonstrations
5. Class room practices
6. Use of projector and soft material for demonstration
4. Virtual Laboratory

## I. Teaching and Learning resources:

Chalk board, LCD presentations, Demonstrative kits, Demonstrative charts

## J. Reference Books:

Sr. No.	Name of Book	Author	Publication
1	Electrical & Electronic Measurements & Instrumentations	A.K. Sawhney	Dhanpat Rai & Co

## K. Text Books

Sr. No	Name of Book	Author	Publication
1	Modern Electronic Instrumentation & Measurement Techniques	W.D. Cooper	Pearson Education,
2	Electronic Instruments	H.S.Kalsi	Tata Mc Grow Hill

## L. Learning Website & Software

- i. [www.nptel.iitm.ac.in](http://www.nptel.iitm.ac.in)
- ii. [www.learningaboutelectronics.com](http://www.learningaboutelectronics.com)
- iii. [www.electronics-tutorials.com](http://www.electronics-tutorials.com)
- iv. <https://circuitdigest.com/electronic-circuits>
- v. [https://www.tutorialspoint.com/basic\\_electronics/basic\\_electronics\\_transistors.htm](https://www.tutorialspoint.com/basic_electronics/basic_electronics_transistors.htm)

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## COURSE ID:

**COURSE NAME : BASIC PYTHON PROGRAMMING**

**COURSE CODE : ETH306**

**COURSE ABBREVIATION : HBPP**

### A. LEARNING SCHEME:

Scheme component		Hours	Credits
Actual Contact Hours / week	Classroom Learning	02	02
	Tutorial Learning	00	
	Laboratory Learning	02	
	SLH-Self Learning	00	
	NLH- Notional Learning	04	

### B. ASSESSMENT SCHEME:-

PAPER DURATION IN HRS	THEORY				BASED ON LL&TL				BASED ON SLA		Total
					Practical						
-	FA-TH	SA-TH	TOTAL		FA -PR		SA-PR		MAX	MIN	50
	MAX	MAX	MAX	MIN	MAX	MIN	MAX	MIN			
	-	-	-	-	-	25	10	25@	10	-	

**C. ABBREVIATIONS:** - CL- Class Room Learning, TL- Tutorial Learning, LL-Laboratory Learning, SLH-Self Learning Hours, NLH- Notional Learning Hours, FA - Formative Assessment, SA -Summative assessment, IKS - Indian Knowledge System, SLA - Self Learning Assessment

**Legends:** @ Internal Assessment, # External Assessment, \*# On Line Examination, @\$ Internal Online Examination .

1. FA-TH represents average of two class tests of 30 marks each conducted during the semester.
2. If candidate is not securing minimum passing marks in FA-PR of any course then the candidate shall be declared as "Detained" in that semester.
3. If candidate is not securing minimum passing marks in SLA of any course then the candidate shall be declared as fail and will have to repeat and resubmit SLA work.

4. Notional Learning hours for the semester are (CL+LL+TL+SL) hrs.\* 15 Weeks
5. 1(one) credit is equivalent to 30 Notional hrs.
6. \* Self learning hours shall not be reflected in the Time Table.

\* Self learning includes micro project / assignment / other activities. (Provide list of all assignments here in tabular format At least 6 to 8 assignments to be given)

### D. i) RATIONALE: -

Electronics based industries needs to deal with creating circuits design, simulation, signal processing and control systems which can be developed using Python. This course deals with the basics of python to enhance the programming skills of diploma students. The course will enable students to write python programs as well as use different python libraries to solve given problems.

### ii) INDUSTRY / EMPLOYER EXPECTED OUTCOME

The aim of this course is to attain the following industry/employer expected outcome through various teaching learning experiences:

Develop programs using python to solve wide-reaching electronics engineering related problems.

### E. COURSE LEVEL LEARNING OUTCOMES (COs)

Students will be able to achieve & demonstrate the following COs on completion of course based learning

- ETH306-1 - Develop script to demonstrate use of basic building blocks of python.
- ETH306-2 - Implement conditional and looping statements for given problem statement.
- ETH306-3 - Perform operations on sequence structures in python.
- ETH306-4 - Implement basics of object oriented programming concepts.
- ETH306-5 - Create modules and packages for given purpose.

### Course outcomes and programme outcomes/ programme specific outcomes (CO- PO/PSO) matrix

[Note: Correlation levels: 1: Slight (Low), 2: Moderate (Medium), 3: Substantial (High), “-“ : No correlation]

Cos	Programme Outcomes POs and PSOs								
	PO 1 Basic and Discipline specific knowledge	PO 2 Problem Analysis	PO 3 Design / Development of solutions	PO 4 Engineering Tools, Experimenta tion and Testing	PO 5 Engineering Practices for society, sustainability and Environment	PO 6 Project Managem ent	PO 7 Life- long Learning	PSO1 Operate and Maintain	PSO2 Supervision and Providing Solution
ETH306-1	2	-	-	-	-	-	-	-	-
ETH306-2	2	-	-	1	-	-	-	-	-
ETH306-3	1	1	1	2	-	-	-	-	-
ETH306-4	1	2	2	2	-	-	-	-	-
ETH306-5	1	1	1	2	-	-	-	-	-

## F. CONTENT:

### I) Practical exercises

The following practical exercises shall be conducted in the *Laboratory* in practical sessions of batches of about 20- 22 students:

(Minimum 12 of the following practical exercises are to be performed)

Sr. No.	Laboratory experiences	Skills to be developed	CO
1.	*Install and configure Python IDE. Write Python program to display message on screen. ( Any relevant python IDE like IDLE/PyCharm/VSCode/Jupyter Notebook/OnlinePython Compiler.)	Install Python Integrated Development Environment.	ETH306-1
2.	*a) Write simple Python program to calculate equivalent registers connected in series and parallel. Accept values of R1, R2 and R3 from the user. *b) Write simple Python program to calculate value of voltage by applying Ohm's law. Accept value of Current(I) and Resistance(R) from the user.	Use operators in Python	ETH306-1
3.	*Write program to check whether entered frequency is radio frequency or audio frequency.	Implement two-way branching statement	ETH306-2
4.	*a) Write program to display various radio frequency bands using if..elseif ladder. *b) Write program to display resistor color code using switch statement.	Implement multi-way branching statement	ETH306-2
5.	*a. Write a simple Python program to demonstrate use of control loops: i) while ii) do while *b. Create a simple program, to demonstrate use of: for loop in Python (e.g.: various pattern building, printing multiplication table, checking palindrome number etc.)	Implement control loops for solving iterative problems	ETH306-2
6.	*Write Python program to perform following operations on List: a) Create b) Access c) Update d) Delete elements from list.	Perform basic operations on the Lists	ETH306-3
7.	Develop Python program to perform following operations on Tuples: a) Create b) Access	Execute various tuple operations	ETH306-3



Sr. No.	Laboratory experiences	Skills to be developed	CO
	c) Update Delete Tuple elements		
8.	Write Python program to perform following operations on Set: a) Create b) Access c) Update d) Delete Access Set elements	Implement various set operations	ETH306-3
9.	*Create a program to perform following operations on Dictionaries in Python: a) Create b) Access c) Update d) Delete e) Looping through Dictionary	Execute various operations on Dictionaries	ETH306-3
10.	a) *Create python program to demonstrate use of math built-in function. b) *Create python program to demonstrate use of string built-in function.	Use built-in mathematical functions and string functions in python	ETH306-4
11.	Write python programs to define function with arguments. a) Calculate factorial of a number b) Swapping of two variables	Create user defined functions in Python	ETH306-4
12.	Write programs to define function with default arguments.	Implement function with default arguments	ETH306-4
13.	*Create a program to demonstrate use of: Built-in module (e.g. numeric, mathematical functional and programming module) in Python.	Use built-in python mathematical modules	ETH306-5
14.	Write program to create a user-defined module (e.g.: building calculator) in python.	Write user-defined module in python	ETH306-5
15.	*Develop Python program to demonstrate use of NumPy package for creating, accessing and performing different array operations.	Use python built-in packages	ETH306-5
16.	Write program to demonstrate the use of user defined packages in Python.	Implement user-defined packages in python	ETH306-5

## II) Theory

Sr. no.	Topics/Subtopics	Learning (Hours)	Classroom learning evaluation Marks
<b><i>ETH306-1 Develop script to demonstrate use of basic building blocks of python.</i></b>			
<b>1</b>	<b>Basic Python's Constructs</b> <p>2.10 Introduction to Python- Python as scripting Language, Programming language Vs Scripting Language (C vs Python), Python's Technical Strength, Application in different domains</p> <p>2.11 Python's building blocks- Identifiers, Keywords, Variables, Constants, Indentation, Comments in python</p> <p>2.12 Python's Data Types – Numbers, Strings, List, Tuples, Dictionaries, Sets</p> <p>2.13 Input and Output statements in python</p> <p>2.14 Operators in Python- Operators as Arithmetic, Assignment, Unary Minus, Relational, Logical, Boolean, Bitwise, Membership, Identity, Operator precedence and Associativity</p>	<b>6</b>	<b>-</b>
<b><i>ETH306-2 Implement conditional and looping statements for given problem statement.</i></b>			
<b>2</b>	<b>Control Statements in Python</b> <p>2.1 Types of Control Statements – Decision making statements, Looping statements</p> <p>2.2 Decision Making Statements: - if, if...else, else-if ladder, nested if and switch statement</p> <p>2.3 Looping statement: - while loop, for loop, nested loop</p> <p>2.4 Manipulating Loops- use of break, continue and pass statements</p>	<b>5</b>	<b>-</b>
<b><i>ETH306-3 Perform operations on sequence structures in python.</i></b>			
<b>3</b>	<b>Data Structures in Python</b> <p>3.1 List- Defining List, Creating list, Accessing values from list, Updating the elements of a list, Concatenation of two lists, Repeating of Lists, Membership in list, Aliasing and cloning Lists, Methods to process Lists, Nested Lists</p> <p>3.2 Tuples- Defining Tuple, Creating Tuples, Accessing the Tuple elements, Inserting elements in a Tuple, modifying elements of a Tuple, Deleting elements from a Tuple, Basic operations in Tuples, Functions to process Tuples, Nested Tuples</p> <p>3.3 Sets- Defining Set, Creating a Set, Accessing elements from set, Add and update Set, Remove an elements from</p>	<b>7</b>	<b>-</b>

	a Set, Built in functions with Set, Set methods to perform mathematical operations, other relevant set methods 3.4 Dictionaries- Defining Dictionary, Creating Dictionary, Accessing elements from Dictionary, Add and update Dictionary, Delete an element from a Dictionary, Built in functions of Dictionary, Methods to perform Dictionary.		
<b>ETH306-4</b> <i>Implement basics of object oriented programming concepts.</i>			
<b>4</b>	<b>Functions with Basic OOP concepts</b> 4.1 Python Functions- Use of python built in functions (e.g. type/data conversion functions, math and string functions), User defined function- Function definition, function calling, function arguments and parameter passing, Return statement, scope of variables (Global and Local Variables) 4.2 Basic OOP concepts- Introduction to object-oriented programming, Creating classes and objects, Constructors and Destructors in python, Data abstraction and Encapsulation	<b>7</b>	<b>-</b>
<b>ETH306-5</b> <i>Create modules and packages for given purpose</i>			
<b>5</b>	<b>Modules and Packages in Python</b> 5.1 Modules- Writing modules, importing module, python built in modules (Numeric and mathematical module, Functional Programming Module) 5.2 Python packages- Introduction, Writing python packages, using standard packages (NumPy, matplotlib) and user defined package statements	<b>5</b>	<b>-</b>

## G. Assessment Criteria

### i) Formative Assessment of Practical: -

Every assignment shall be assessed for 25 marks as per following criteria:

Domain	Particulars	Marks out of 25
Cognitive	Technical preparedness for practical	05
Psychomotor	Operating skills/Algorithm/flowchart	05
	Observation/Logic/Program/Result	05
Affective	Discipline and punctuality	05
	Procedure/ Safety Measures/Decency/ Presentation	05
<b>TOTAL</b>		<b>25</b>

**ii) Summative Assessment of Practical:**

Every practical assignment shall be assessed for 25 marks as per following criteria:

Sr. No	Criteria	Marks allotted
1	Neat & complete circuit Diagram / schematic Diagram/ Algorithm/ Flowchart/ Program	5
2	Procedure followed to achieve the result	5
3	Observations, Result, Output, Sample Calculations with relevant formulae	5
4	Proper Graphs, workmanship and Safety measures	5
5	Oral	5
	<b>Total</b>	<b>25</b>

**H. Instructional Methods:**

7. Lectures cum Demonstrations
8. Class room practices
9. Use of projector and soft material for demonstration
4. Laboratory work

**I. Teaching and Learning resources:**

Chalk board, LCD presentations, Demonstrative kits, Demonstrative charts

**J. Reference Books:**

Sr. No	Author	Title	Publisher with ISBN Number
1	Giancarlo Zaccone	Natural Computing with Python	BPB, ISBN:9789388511612
2	Martin C. Brown	Python: The Complete Reference	Tata McGraw Hill ISBN: 9789387572942
3	Yashwant Kanetkar	Let Us Python	BPB, ISBN: 978-9391392253
4	Kumar Naveen, Taneja Sheetal.	Python Programming: A modular approach	Pearson, ISBN: 978-9352861293
5	Mark Lutz and David Ascher	Learning Python	O'Reilly, ISBN: 978-1449355739
6	Paul Barry	Head First Python	O'Reilly, ISBN: 978-1449382674
7	John Guttag	Introduction to Computation and Programming Using Python	MIT Press, ISBN: 978-0262529624
8	David Beazley	Python Essential Reference	Addison-Wesley Professional, ISBN: 978-0672329784
9	Dr. R. Nageswara	Core Python Programming	DREAMTECH PRESS, ISBN: 978-

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## K. Learning Website & Software

Sr. No	Link / Portal	Description
1	<a href="https://www.programiz.com/python-programming">https://www.programiz.com/python-programming</a>	Python Programming
2	<a href="https://python-iitk.vlabs.ac.in/Introduction.html">https://python-iitk.vlabs.ac.in/Introduction.html</a>	Virtual Lab for Python Programming-Basic Constructs in Python
3	<a href="https://www.geeksforgeeks.org/python-programming-language/">https://www.geeksforgeeks.org/python-programming-language/</a>	Python Programming
4	<a href="https://intellipaat.com/academy/course/introduction-to-python-programming-free-course/">https://intellipaat.com/academy/course/introduction-to-python-programming-free-course/</a>	Online Course-Python Programming
5	<a href="https://www.w3schools.com/python/">https://www.w3schools.com/python/</a>	Python Programming
6	<a href="https://www.tutorialspoint.com/python/index.htm">https://www.tutorialspoint.com/python/index.htm</a>	Python Programming
7	<a href="https://www.python.org/">https://www.python.org/</a>	Python Programming
8	<a href="https://spoken-tutorial.org/tutorial-search/?search_foss=Python+3.4.3&amp;search_language=English">https://spoken-tutorial.org/tutorial-search/?search_foss=Python+3.4.3&amp;search_language=English</a>	Spoken Tutorial on Python Programming
<b>Note :</b> Teachers are requested to check the creative common license status/financial implications of the suggested online educational resources before use by the students		

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<b>Programme</b>	<b>: - ALL</b>
<b>Semester</b>	<b>: THIRD</b>
<b>Course Title</b>	<b>: ESSENCE OF INDIAN CONSTITUTION</b>
<b>Course Code</b>	<b>: CCH205</b>

#### **A. RATIONALE**

This course will focus on the basic structure and operative dimensions of Indian Constitution. It will explore various aspects of the Indian political and legal system from a historical perspective highlighting the various events that led to the making of the Indian Constitution. The Constitution of India is the supreme law of India. The document lays down the framework demarcating the fundamental political code, structure, procedures, powers, and sets out fundamental rights, directive principles, and the duties of citizens. The course on constitution of India highlights key features of Indian Constitution that makes the students a responsible citizen. In this online course, we shall make an effort to understand the history of our constitution, the Constituent Assembly, the drafting of the constitution, the preamble of the constitution that defines the destination that we want to reach through our constitution, the fundamental right constitution guarantees through the great rights revolution, the relationship between fundamental rights and fundamental duties, the futuristic goals of the constitution as incorporated in directive principles and the relationship between fundamental rights and directive principles.

#### **B. INDUSTRY / EMPLOYER EXPECTED OUTCOME**

The aim of this course is to help the student to attain the following industry /employer expected outcome – Abide by the Constitution in their personal and professional life.

#### **C. COURSE LEVEL LEARNING OUTCOMES (COS)**

Students will be able to achieve & demonstrate the following COs on completion of course based learning

## D. TEACHING-LEARNING & ASSESSMENT SCHEME

Course Code	Course Title	Abbr	Course Category/s	Learning Scheme					Credits	Assessment Scheme												Total Marks
				Actual Contact Hrs./Week			SLH	NLH		Paper Duration	Theory				Based on LL & TL				Based on SL			
															Practical							
				CL	TL	LL					FA-TH		SA-TH		Total		FA-PR		SA-PR		SLA	
							Max	Min			Max	Min	Max	Min	Max	Min	Max	Min				
CCH205	ESSENCE OF INDIAN CONSTITUTION	EIC	VEC	1	-	-	1	2	1	-	-	-	-	-	-	-	-	50	20	50		

### Total IKS Hrs for Sem. : 4 Hrs

Abbreviations: CL- Classroom Learning , TL- Tutorial Learning, LL-Laboratory Learning, SLH-Self Learning Hours, NLH-Notional Learning Hours, FA - Formative Assessment, SA -Summative assessment, IKS - Indian Knowledge System, SLA - Self Learning Assessment

Legends: @ Internal Assessment, # External Assessment, \*# On Line Examination , @\$ Internal Online Examination  
Note :

1. FA-TH represents average of two class tests of 30 marks each conducted during the semester.
2. If candidate is not securing minimum passing marks in FA-PR of any course then the candidate shall be declared as "Detained" in that semester.
3. If candidate is not securing minimum passing marks in SLA of any course then the candidate shall be declared as fail and will have to repeat and resubmit SLA work.
4. Notional Learning hours for the semester are (CL+LL+TL+SL)hrs.\* 15 Weeks
5. 1 credit is equivalent to 30 Notional hrs.
6. \* Self learning hours shall not be reflected in the Time Table.
7. \* Self learning includes micro project / assignment / other activities.

CO1 - List salient features and characteristics of the constitution of India.

CO2 - Follow fundamental rights and duties as responsible citizen and engineer of the country. CO3 - Analyze major constitutional amendments in the constitution.

CO4 - Follow procedure to cast vote using voter-id.

CO5-List the roles and responsibilities of State Election Commission towards peoples in the state.

CO-6 List Judiciary provisions for the peoples in general

## E. THEORY LEARNING OUTCOMES AND ALIGNED COURSE CONTENT

SECTION-I		
Sr. No.	Topics / Sub-topics	Lectures (Hours)
1	<b>CCH 205.1. The Constitution:-</b> 1.1 Introduction. 1.2 The History of making of the Indian Constitution. 1.3 Basic structure and its interpretation. 1.4 Fundamental Rights and Duties and their interpretation	2
2	<b>CCH 205.2 .Union Government</b> 2.1 Structure of the Indian Union. 2.2 President –Role and power. 2.3 Prime minister and council of ministers. 2.4 Lok Sabha and Rajya Sabha. 2.5 Union Territories and their limitations.	3

<b>3</b>	<b>CCH 205.3.</b> State Government. 3.1 Governor –Role and power. 3.2 Chief Minister and council of ministers. 3.3 State secretariat. 3.4 Administrative Regions of Maharashtra.	<b>3</b>
<b>SECTION -II</b>		
<b>4</b>	<b>CCH205.4</b> Local Administration:-Their roles and responsibilities 4.1 District Administration. 4.2 Municipal Corporation. 4.3 Zilla Panchayat 4.4 Taluka (Tahasil) Administration .	<b>2</b>
<b>5</b>	<b>CCH205.5.</b> Election Commission. 5.1 Role and functioning. 5.2 Chief Election Commissioner –Appointment. 5.3 State Election Commission. 5.4 Elections and duties of government /Non government servants – introduction	<b>2</b>
<b>6</b>	<b>CCH205.6.</b> Introduction to Judiciary Provisions :- 1.1 Introduction 1.2 Different courts. 1.3 Government legal advisor-provisions. 1.4 Limitations of courts and co-ordination with Home department.	<b>3</b>

**F. LABORATORY LEARNING OUTCOME AND ALIGNED PRACTICAL / TUTORIAL EXPERIENCES**  
: N.A.

**G. SUGGESTED MICRO PROJECT / ASSIGNMENT/ ACTIVITIES FOR SPECIFIC LEARNING / SKILLS DEVELOPMENT (SELF LEARNING)**

1. Outline the procedure to submit application for Voter-id
2. Assignments are to be provided by the course teacher in line with the targeted COs.
3. Prepare an essay on Constitution of India .
4. Prepare a comparative chart of Unique features of Indian Constitution of India and Constitution of USA

[Assignments are to be provided by the course teacher in line with the targeted COs. A1. Prepare an essay on .Constitution of India . A2 Prepare a comparative chart of Unique features of Indian Constitution of India and Constitution of USA A3. Self-learning topics: Parts of the constitution and a brief discussion of each part Right to education and girl enrollment in schools. GER of Girls and Boys. Right to equality. Social Democracy. Women Representation in Parliament and State Assemblies.]

**Micro project :-** Organize a workshop-cum discussions for spreading awareness regarding Fundamental Rights of the citizen of the country

1. Prepare elaborations where directive principle of State policy has prevailed over Fundamental rights with relevant Supreme Court Judgements.
2. Organize a debate on 42nd, 97th and 103rd Constitutional Amendment Acts of Constitution of India.



### **Seminar**

- 1 Differences in the ideals of Social democracy and Political democracy.
- 2 Democracy and Women's Political Participation in India.
- 3 Khap Panchayat - an unconstitutional institution infringing upon Constitutional ethos.
- 4 Situations where directive principles prevail over fundamental rights.

### **Group discussions on current print articles.**

- Art 356 and its working in Post-Independent India.
- Women's Reservation in Panchayat leading to Pati Panchayats - Problems and Solutions.
- Adoption of Article 365 in India.
- Need of Amendments in the constitution.
- Is India moving towards a Unitary State Model ?

### **Activity**

Arrange Mock Parliament debates.

Prepare collage/posters on current constitutional issues.

- i. National (Art 352) & State Emergencies (Art 356) declared in India.
- ii. Seven fundamental rights.
- iii. Land Reforms and its effectiveness - Case study of West-Bengal and Kerala.

**H. LABORATORY EQUIPMENT / INSTRUMENTS / TOOLS / SOFTWARE**  
REQUIRED : NOT APPLICABLE

**I. SUGGESTED WEIGHTAGE TO LEARNING EFFORTS & ASSESSMENT PURPOSE**  
(Specification Table)

Sr.No	Unit	Unit Title	Aligned COs	Learning Hours	R-Level	U-Level	A-Level	Total Marks
1	I	Constitution and Preamble	CO1	4				
2	II	Fundamental Rights and Directive Principles	CO2	4				
3	III	Governance and Amendments	CO3	4				
4	IV	Electoral Literacy and Voter's Education	CO4	3				
<b>Grand Total</b>				<b>15</b>				

**J. ASSESSMENT METHODOLOGIES/TOOLS**

**Formative assessment (Assessment for Learning): Suggested Proformas are to be used for ASSESSMENT.**

Assignment, Self-learning and Terms work Seminar/Presentation

**Summative Assessment (Assessment of Learning):- Suggested Proformas are to be used for ASSESSMENT**

**K. SUGGESTED COS - POS MATRIX FORM**

Course Outcomes (COs)	Programme Outcomes (POs)							Programme Specific Outcomes* (PSOs)		
	PO-1 Basic and Discipline Specific Knowledge	PO-2 Problem Analysis	PO-3 Design/ Development of Solutions	PO-4 Engineering Tools	PO-5 Engineering Practices for Society, Sustainability and Environment	PO-6 Project Management	PO-7 Life Long Learning	PSO-1	PSO-2	PSO-3
CO1	1	-	-	-	2	-	-			
CO2	1	-	-	-	2	-	-			
CO3	1	2	-	-	2	-	1			
CO4	-	-	-	1	-	-	-			

Legends :- High:03, Medium:02,Low:01, No Mapping: -  
\*PSOs are to be formulated at institute level

## L. SUGGESTED LEARNING MATERIALS / BOOKS

Sr.No	Author	Title	Publisher with ISBN Number
1	P.M.Bakshi	The Constitution of India	Universal Law Publishing, New Delhi 15th edition, 2018, ISBN: 9386515105 (Check the new edition)
2	D.D.Basu	Introduction to Indian Constitution	Lexis Nexis Publisher, New Delhi, 2015, ISBN:935143446X
3	B. K. Sharma	Introduction to Constitution of India	PHI, New Delhi, 6th edition, 2011, ISBN:8120344197
4	MORE READS :	Oxford Short Introductions - The Indian Constitution by Madhav Khosla. The Indian Constitution: Cornerstone of a Nation by Granville Austin. Working a Democratic Constitution: A History by Garnville Austin Founding Mothers of the Indian Republic: Gender Politics of the Framing of the Constitution by Achyut Chetan. Our Parliament by Subhash C. Kashyap. Our Political System by Subhash C. Kashyap. Our Constitution by Subhash C. Kashyap. Indian Constitutional Law by Rumi Pal.	Extra Read
5	B.L. Fadia	The Constitution of India	Sahitya Bhawan, Agra, 2017, ISBN:8193413768

## M. LEARNING WEBSITES & PORTALS

Sr.No	Link / Portal	Description
1	<a href="http://www.legislative.gov.in/constitution-of-india">http://www.legislative.gov.in/constitution-of-india</a>	Constitution overview
2	<a href="https://en.wikipedia.org/wiki/Constitution_of_India">https://en.wikipedia.org/wiki/Constitution_of_India</a>	Parts of constitution
3	<a href="https://www.india.gov.in/my-government/constitution-india">https://www.india.gov.in/my-government/constitution-india</a>	Constitution overview
4	<a href="https://www.toppr.com/guides/civics/the-indian-constitution/the-constitution-of-india/">https://www.toppr.com/guides/civics/the-indian-constitution/the-constitution-of-india/</a>	Fundamental rights and duties
5	<a href="https://main.sci.gov.in/constitution">https://main.sci.gov.in/constitution</a>	Directive principles
6	<a href="https://legallaffairs.gov.in/sites/default/files/chapter%203.pdf">https://legallaffairs.gov.in/sites/default/files/chapter%203.pdf</a>	Parts of constitution
7	<a href="https://www.concourt.am/armenian/legal_resources/world_constitutions/constit/india/india-e.htm">https://www.concourt.am/armenian/legal_resources/world_constitutions/constit/india/india-e.htm</a>	Parts of constitution
8	<a href="https://constitutionnet.org/vl/item/basic-structure-indian-constitution">https://constitutionnet.org/vl/item/basic-structure-indian-constitution</a>	Parts of constitution



# SEMESTER IV COURSES



**COURSE ID:**

**COURSE NAME : MICROCONTROLLERS**

**COURSE CODE : ETH307**

**COURSE ABBREVIATION : HMCS**

**A. LEARNING SCHEME:**

Scheme component		Hours	Credits
Actual Contact Hours / week	Classroom Learning	04	04
	Tutorial Learning	00	
	Laboratory Learning	04	
	SLH-Self Learning	00	
	NLH- Notional Learning	08	

**B. ASSESSMENT SCHEME:-**

PAPER DURATION IN HRS	THEORY				BASED ON LL&TL				BASED ON SLA		Total
					Practical						
03	FA-TH	SA-TH	TOTAL		FA -PR		SA-PR		MAX	MIN	
	MAX	MAX	MAX	MIN	MAX	MIN	MAX	MIN			
	30	70	100	40	25	10	25#	10	-	-	150

**C. ABBREVIATIONS:** - CL- Class Room Learning, TL- Tutorial Learning, LL-Laboratory Learning, SLH-Self Learning Hours, NLH- Notional Learning Hours, FA - Formative Assessment, SA -Summative assessment, IKS - Indian Knowledge System, SLA - Self Learning Assessment

**Legends:** @ Internal Assessment, # External Assessment, \*# On Line Examination, @\$ Internal Online Examination .

1. FA-TH represents average of two class tests of 30 marks each conducted during the semester.
2. If candidate is not securing minimum passing marks in FA-PR of any course then the candidate shall be declared as "Detained" in that semester.
3. If candidate is not securing minimum passing marks in SLA of any course then the candidate shall be declared as fail and will have to repeat and resubmit SLA work.
4. Notional Learning hours for the semester are (CL+LL+TL+SL) hrs.\* 15 Weeks
5. 1(one) credit is equivalent to 30 Notional hrs.

**6. \* Self learning hours shall not be reflected in the Time Table.**

\* Self learning includes micro project / assignment / other activities. (Provide list of all assignments here in tabular format At least 6 to 8 assignments to be given)

**D. i) RATIONALE: -**

A microcontroller is the sole of all embedded electronic types of equipment and is used in most areas of electrical/electronics where automation and monitoring are needed. They include product lines ranging from small consumer electronic products to sophisticated industrial process controllers. A diploma engineer needs to maintain such systems. Programming practices will further enhance students' ability to develop local applications based on microcontrollers. Hence this course is designed to address the above.

**ii) INDUSTRY / EMPLOYER EXPECTED OUTCOME**

The aim of this course is to attend following industry identified competency through various teaching learning experiences: • Maintain electronic equipment/systems comprising of discrete electronic components.

**E. COURSE LEVEL LEARNING OUTCOMES (COs)**

**ETH307-1:** Interpret prominent features of different kinds of microcontrollers.

**ETH307-2:** Interpret the salient architectural features of 8051 microcontroller

**ETH307-3:** Develop and maintain assembly language program for different operations

**ETH307-4:** Interface and program different I/O devices with 8051 in assembly

**ETH307-5:** Maintain different 8051 based applications

**Course outcomes and programme outcomes/ programme specific outcomes (CO- PO/PSO) matrix**

[Note: Correlation levels: 1: Slight (Low), 2: Moderate (Medium), 3: Substantial (High), “-“ : No correlation]

Cos	Programme Outcomes POs and PSOs								
	PO 1 Basic and Discipline specific knowledge	PO 2 Problem Analysis	PO 3 Design / Development of solutions	PO 4 Engineering Tools, Experimenta tion and Testing	PO 5 Engineering Practices for society, sustainability and Environment	PO 6 Project Managemen t	PO 7 Life- long Learning	PSO1 Operate and Maintain	PSO2 Supervision and Providing Solution
ETH307-1	1	1	2	-	-	-	-	2	2
ETH307-2	1	3	2	-	-	-	-	2	2
ETH307-3	-	3	3	1	-	-	-	2	2
ETH307-4	1	2	3	1	-	-	-	3	3
ETH307-5	1	2	-	-	-	-	-	3	3



## F. CONTENT:

### I) Practical exercises

The following practical exercises shall be conducted in the *Laboratory for Microcontrollers* developed by the Institute in practical sessions of batches of about 20- 22 students: (Any 20)

Sr. No.	Laboratory experiences	CO
1.	Identification of various blocks of 8051 microcontroller development board.	ETH307-2
2.	Identify different menus available in compiler software KEIL and demonstrate their use.	ETH307-1
3.	Develop an Assembly Language Program (ALP) for addition of two numbers using various addressing modes and assembler directives.	ETH307-3
4.	Develop an ALP to perform arithmetic operations: addition, subtraction, multiplication and division on 8-bit data.	ETH307-3
5.	Develop an ALP to perform arithmetic operations: addition, subtraction on 16-bit data.	ETH307-3
6.	Develop an ALP to perform addition of BCD data stored at external and store result in internal memory.	ETH307-3
7.	Develop an ALP for sum of series stored in RAM locations 40-49H. Find the sum of the values at the end of the program the lower byte store in 30H the higher byte in 31H.	ETH307-3
8.	Develop an ALP to transfer data from source to destination locations of internal/ external data memory.	ETH307-3
9.	Develop an ALP to exchange block of data from source to destination location of internal/ external data memory.	ETH307-3
10.	Develop an ALP for identifying smallest number from the given data bytes stored in internal/ external data memory.	ETH307-3
11.	Develop an ALP for identifying largest number from the given data bytes stored in internal/ external data memory.	ETH307-3
12.	Develop an ALP for arranging numbers in ascending order stored in internal/ external data memory.	ETH307-3
13.	Develop an ALP for arranging numbers in descending order stored in internal/ external data memory.	ETH307-3
14.	Interface LED with microcontroller and turn it 'ON' with microcontroller interrupt.	ETH307-4
15.	Interface 4 X 4 LED matrix with 8051 to display various pattern.	ETH307-4
16.	Interface 7-segment display to display the decimal number from 0 to 9.	ETH307-4
17.	Interface relay with microcontroller and turn it 'ON' and 'OFF'.	ETH307-4
18.	Develop an ALP to generate delay using timer register.	ETH307-4
19.	Develop an ALP to generate pulse and square wave by using timer delay.	ETH307-4
20.	Develop an ALP to transfer 8 bit data serially on serial port.	ETH307-4
21.	Interface LCD with 8051 microcontroller to display the characters and decimal numbers.	ETH307-5
22.	Interface the given keyboard with 8051 and display the key pressed.	ETH307-5

Sr. No.	Laboratory experiences	CO
23.	Interface ADC with 8051 microcontroller and verify input/output.	ETH307-5
24.	Interface DAC with 8051 microcontroller to generate square wave, triangular wave, saw-tooth wave.	ETH307-5
25.	Interface Stepper motor to microcontroller and rotate in clockwise and anticlockwise direction at the Given angle	ETH307-5
26.	Design water level controller using any suitable open source simulation software to detect and control the water level in a tank.	ETH307-5
27.	Interface temperature sensor LM35 to 8051 to read temperature, convert it to decimal and send the value to Port 0 with some delay.	ETH307-5

## II) Theory

### Section I

Sr. no.	Topics/Subtopics	Learning (Hours)	Classroom learning evaluation Marks
	<b><i>ETH307-1 Interpret the salient features of various types of microcontrollers.</i></b>		
<b>1</b>	<b>Introduction to Microcontrollers</b> 1.1 Evolution of Microcontrollers 1.1.1 Block diagram of Microcomputer 1.1.2 Elements of Microcomputer (Buses Microprocessor, Memory, I/O devices) 1.1.3 Types of buses (Address, Data and control bus) 1.2 Types of architectures: Von Neuman and Harvard Architecture. 1.3 Comparison of Microcontroller and Microprocessor on basis of: Memory, Complexity, Type of Architecture, Cost, Applications, Typical examples of Microcontrollers and Microprocessors 1.4 Need of Microcontroller 1.5 Introduction and technical specifications of various microcontrollers : 1.5.1 8051 Microcontroller 1.5.2 PIC Microcontroller 1.5.3 AVR Microcontroller 1.5.4 ARM Microcontroller 1.5.5 Compare all listed microcontroller with the parameters (Bits, Memory, instruction set & memory architecture) 1.6 Introduction to Microcontroller programming simulation software like – KEIL compiler and explain Software Development Cycle: Editor, Assembler, Compiler, Cross-	<b>06</b>	<b>08</b>

	Compiler, Linker, Locator		
	<b><i>ETH307-2 Interpret the salient architectural features of microcontrollers IC 8051</i></b>		
<b>2</b>	<b>Architecture of 8051 Microcontroller</b> 2.1 8051 Architecture: 2.1.1 Features and Selections factors for Microcontroller 2.1.2 Architectural Block diagram of 8051, function of each block 2.1.3 Pin diagram, function of each pin 2.1.4 Memory organization of Internal memory (RAM and ROM) 2.1.5 Reset and clock circuit 2.1.6 Various registers and SFRs of 8051 2.2 Special Features of 8051 2.2.1 Boolean Processor 2.2.2 Power saving options- idle and power down mode.	<b>12</b>	<b>12</b>
	<b><i>ETH307-3 Develop assembly language program and Maintain the program features for given operations.</i></b>		
<b>3</b>	<b>8051 Instruction Set and Programs</b> 3.1 Overview of 8051 instruction set 3.1.1 Instruction Format for 8051 Microcontroller 3.1.2 Introduction to Assembler and Various addressing modes 3.2 Classification of instructions 3.2.1 Data transfer instructions 3.2.2 Arithmetic instructions 3.2.3 Logical instructions 3.2.4 Branching instructions 3.2.5 Bit manipulation instructions 3.2.6 Stack, subroutine and interrupt related instructions 3.2.7 Assembler Directives: ORG, DB, EQU, END, CODE, DATA 3.3 Simple Programs based on above instructions and directives.	<b>12</b>	<b>14</b>
	<b>Sub-total</b>	<b>30</b>	<b>34</b>

## Section –II

Sr. no.	Topics/Subtopics	Learning (Hours)	Classroom learning evaluation Marks
	<b><i>ETH307-4 Interface and program different I/O devices with 8051 in assembly</i></b>		
<b>4</b>	<b>8051 Internal Peripherals and Related Programs</b> 4.1 Configuration and Programming of I/O Port :P0, P1, P2, P3 4.2 I/O interfacing: LED, Relays, Seven segment display 4.3 Timer/Counter programming 4.3.1 Timer / Counter logic and modes 4.3.2 Simple programs on timer to generate time delay and square wave.	<b>10</b>	<b>14</b>
	<b><i>ETH307-4 Interface and program different I/O devices with 8051 in assembly</i></b>		
<b>5</b>	<b>Serial communication and Interrupt programming</b> 5.1 Serial port of 8051 5.1.1 Serial communication SFRs: SCON, SBUF, PCON 5.1.2 Modes of serial communication 5.1.3 Simple programs for serial communication 5.2 8051 Interrupts 5.2.1 Interrupts and polling concept. 5.2.2 Interrupts SFRs: IE, IP 5.2.3 Simple programs based on interrupts and polling method 5.3 Memory interfacing: Program and data memory	<b>12</b>	<b>14</b>
	<b><i>ETH307-5 Maintain different 8051 based applications</i></b>		
<b>6</b>	<b>Peripheral interfacing and Applications</b> Interfacing diagram with programming of following with 8051 6.1 Key-board interfacing (4X4 Matrix keyboard), concept of key bouncing and debounce logic. 6.2 LCD display interfacing 6.3 8 bit ADC and DAC interfacing (0808/0809) 6.4 DC and Stepper Motor interfacing 6.5 Temperature sensor (LM35) interfacing using ADC to 8051 6.6 Water Level controller design using 8051	<b>08</b>	<b>08</b>
	<b>Sub-total</b>	<b>30</b>	<b>36</b>

### G. Specification table for setting question paper for semester end theory assessment

Section / Topic no.	Name of topic	Distribution of marks (level wise)			Total marks	CO
		Remember	Understand	Apply		
I / 1	Introduction to Microcontrollers	02	2	4	8	ETH307-1
I / 2	Architecture of Microcontroller 8051	02	4	6	12	ETH307-2
I / 3	8051 Instruction Set and Programs	02	6	6	14	ETH307-3
II / 4	8051 Internal Peripherals and Related Programs	02	4	8	14	ETH307-4
II / 5	Serial communication and Interrupt programming	02	6	6	14	ETH307-4
II / 6	Peripheral interfacing and Applications	02	2	4	8	ETH307-5
Total Marks		12	24	34	70	

### H. Assessment Criteria

#### i) Formative Assessment of Practical: -

Every assignment shall be assessed for 25 marks as per following criteria:

Domain	Particulars	Marks out of 25
Cognitive	Understanding	05
	Application	05
Psychomotor	Operating Skills	05
	Drawing / drafting skills	05
Affective	Discipline and punctuality	05
<b>TOTAL</b>		<b>25</b>

#### ii) Summative Assessment of Practical:

Every practical assignment shall be assessed for 25 marks as per following criteria:

Sr. no	Criteria	Marks allotted
1	Attendance at regular practical	05
2	Preparedness for practical	05
3	Neat & complete Diagram.	05
4	Observations & handling of instrument.	05
5	Oral Based on Lab work and completion of task	05
<b>TOTAL</b>		<b>25</b>

**I. Instructional Methods:**

10. Lectures cum Demonstrations
11. Class room practices
12. Use of projector and soft material for demonstration
4. Virtual Laboratory

**J. Teaching and Learning resources:**

Chalk board, LCD presentations, Demonstrative kits, Demonstrative charts

**K. Reference Books:**

Sr. No.	Name of Book	Author	Publication
1	Kenneth, Ayala	8051 Microcontroller Architecture Programming and Application	PHI Learning
2	Deshmukh, Ajay	Microcontroller Theory and Application,	McGraw Hill.
3	Kamal, Raj,	Microcontroller Architecture Programming, Interfacing and System Design	Pearson Education India,
4	Mathur; Panda,	Microprocessors and Microcontrollers	PHI Learning
5	Krishna Kant,	Microprocessors and Microcontrollers: Architecture programming and System Design	PHI Learning

**L. Text Books**

Sr. No	Name of Book	Author	Publication
1	Mazidi, Mohmad Ali; Mazidi, Janice Gelisse; MckinlayRoline D.	The 8051 Microcontroller and Embedded system	Pearson Education
2	Pal, Ajit,	Microcontroller Principle and Application	PHI Learning

**M. Learning Website & Software**

- i. [www.nptel.iitm.ac.in](http://www.nptel.iitm.ac.in)
- ii. [www.learningaboutelectronics.com](http://www.learningaboutelectronics.com)
- iii. [www.futurlec.com](http://www.futurlec.com)
- iv. [www.bis.org.in](http://www.bis.org.in)
- v. [www.electrical4u.com](http://www.electrical4u.com)
- vi. [www.cadsoft.io](http://www.cadsoft.io)
- vii. [www.electronics-tutorials.com](http://www.electronics-tutorials.com)

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**COURSE ID :**

**COURSE NAME : DIGITAL COMMUNICATION**

**COURSE CODE : ETH308**

**COURSE ABBREVIATION : HDCM**

### A. LEARNING SCHEME:

Scheme component		Hours	Credits
Actual Contact Hours / week	Classroom Learning	03	03
	Tutorial Learning	00	
	Laboratory Learning	02	
	SLH-Self Learning	01	
	NLH- Notional Learning	06	

### B. ASSESSMENT SCHEME:-

PAPER DURATION IN HRS	THEORY				BASED ON LL&TL				BASED ON SLA		Total
					Practical						
03	FA-TH	SA-TH	TOTAL		FA -PR		SA-PR		MAX	MIN	
	MAX	MAX	MAX	MIN	MAX	MIN	MAX	MIN			
	30	70	100	40	25	10	25#	10	25	10	175

### C. ABBREVIATIONS: -

CL- Class Room Learning, TL- Tutorial Learning, LL-Laboratory Learning, SLH-Self Learning Hours, NLH- Notional Learning Hours, FA - Formative Assessment, SA -Summative assessment, IKS - Indian Knowledge System, SLA - Self Learning Assessment

**Legends:** @ Internal Assessment, # External Assessment, \*# On Line Examination, @\$ Internal Online Examination .

7. FA-TH represents average of two class tests of 30 marks each conducted during the semester.
8. If candidate is not securing minimum passing marks in FA-PR of any course then the candidate shall be declared as "Detained" in that semester.
9. If candidate is not securing minimum passing marks in SLA of any course then the candidate shall be declared as fail and will have to repeat and resubmit SLA work.
10. Notional Learning hours for the semester are (CL+LL+TL+SL) hrs.\* 15 Weeks

11. 1(one) credit is equivalent to 30 Notional hrs.
12. \* Self learning hours shall not be reflected in the Time Table.

\* Self learning includes micro project / assignment / other activities. (Provide list of all assignments here in tabular format At least 6 to 8 assignments to be given)

## D. RATIONALE:

Digital transmission offers data processing option and flexibility which is not available with analog transmission. By eliminating the need for time-consuming face to face interactions, digital communication in various forms makes it easier for customers to reach out to organizations at a time that is convenient and non-disruptive to them. This is technology group subject, which will enable student to comprehend facts, concepts & working principle of digital communication system. It also familiarizes the student with information theory, measurement of information rate & capacity. This subject helps the student to understand the concept of various pulse modulations, Digital modulation techniques, coding methods and error control, multiplexing & multiple access techniques and S.S. modulation. The knowledge acquired by students will help them to apply it in various modern communication systems.

## E. COURSE LEVEL LEARNING OUTCOMES (COs)

**ETH308-1** Identify basic components in Digital Communication and describe its functions

**ETH308-2** Classify, compare and illustrate the operation of pulse modulation systems .

**ETH308-3** Analyze performance of different digital modulation techniques.

**ETH308-4** Use Coding methods and Error control techniques as per need .

**ETH308-5** Interpret concept of multiplexing and multiple access techniques

**ETH308-6** Interpret the concept of various spread spectrum techniques

### Course outcomes and programme outcomes/ programme specific outcomes (CO- PO/PSO) matrix

[Note: Correlation levels: 1: Slight (Low), 2: Moderate (Medium), 3: Substantial (High), “-“ : No correlation]

Cos	Programme Outcomes POs and PSOs								
	PO 1 Basic and Discipline specific knowledge	PO 2 Problem Analysis	PO 3 Design / Development of solutions	PO 4 Engineering Tools, Experimenta tion and Testing	PO 5 Engineering Practices for society, sustainability and Environment	PO 6 Project Managem ent	PO 7 Life- long Learning	PSO1 Operate and Maintain	PSO2 Supervision and Providing Solution
<b>ETH308-1</b>	2	2	-	1	-	-	--	2	--
<b>ETH308-2</b>	2	2	-	2	--	2	--	2	2
<b>ETH308-3</b>	3	-	2	2	-	2	--	3	2
<b>ETH308-4</b>	2	3	3	3	3	2	--	3	3
<b>ETH308-5</b>	-	2	2	--	-	--	--	2	1
<b>ETH308-6</b>	-	2	2	2	--	--	--	2	2



## F. CONTENT:

### I) Practical exercises

The following practical exercises shall be conducted in the *Laboratory* of the Institute in practical sessions of batches of about 20- 22 students: (Any 10 )

Sr. No.	Laboratory experiences	CO
1.	Generation of natural and flat top sampling signals.	ETH308-2
2.	Generation of Pulse Amplitude modulation / demodulation signal	ETH308-2
3.	Generation of Pulse width modulation /demodulation signal	ETH308-2
4.	Generation of Pulse Position modulation/demodulation signal	ETH308-2
5.	Generation of Pulse code modulation and demodulation signal	ETH308-2
6.	Determine the nyquist rate for given signal by using suitable simulation tool.	ETH308-2
7.	Generation of delta modulation and demodulation signal.	ETH308-3
8.	Generation of adaptive delta modulation and demodulation signal.	ETH308-3
9.	Generation of ASK modulation & demodulation signal	ETH308-3
10.	Generation of FSK modulation & demodulation signal	ETH308-3
11.	Generation of PSK modulation & demodulation signal	ETH308-3
12.	Generation of QPSK modulation & demodulation signal	ETH308-3
13.	Construct the circuit for generation of Hamming code for 4 bit data	ETH308-4
14.	Construct the circuit for one bit error correction using Hamming code	ETH308-4
15.	Generate a TDM signal using any simulation software	ETH308-5
16.	Generate a FDM signal using any simulation software	ETH308-5

### II) Theory

#### Section I

Sr. no.	Topics/Subtopics	Learning (Hours)	Classroom learning evaluation Marks
	<b>ETH308-1 Identify basic components in Digital Communication and describe its functions</b>		
<b>1</b>	<b>Introduction of Digital Communication</b> 1.1 Basic digital communication system( block diagram and function of each block) 1.2 Communication Channel characteristics: Baud rate, Bit rate bandwidth, repeater distance. 1.3 Hartleys law, Shannon-Hartley theorem	<b>5</b>	<b>8</b>

	1.4 channel noise and its effect 1.5 Entropy (definition and equation) 1.6 Advantages and disadvantages of digital communication 1.7 Comparison between analog and digital communication		
	<b><i>ETH308-2 Classify, compare and illustrate the operation of pulse modulation systems</i></b>		
<b>2</b>	<b>Pulse Communication</b> 2.1 Introduction, comparison with Continuous Wave Modulation, advantages 2.2 Sampling: sampling theorem, Nyquist rate and aliasing, natural & flat top. 2.3 PAM, PWM, PPM (definition, generation, block diagram, waveform analysis, and their comparison) 2.4 Pulse code modulation- block diagram of PCM transmitter & receiver, 2.5 Quantization, quantization error 2.6 Companding, 2.7 Inter symbol interference 2.8 Delta modulation- block diagram of DM, slope overload, granular noise. 2.9 ADM, DPCM, block diagram and its working, Advantages and disadvantages	<b>10</b>	<b>14</b>
	<b><i>ETH308-3 Analyze performance of different digital modulation techniques.</i></b>		
<b>3</b>	<b>Digital Modulation Techniques</b> 3.1 concept of coherent and non-coherent detection 3.2 Definition & waveforms, block diagram and working of their transmitter and receiver of following types: 3.2.1 ASK:BASK,QAM 3.2.2 FSK:BFSK ,MFSK 3.2.3 PSK:BPSK,QPSK,MPSK 3.3 Advantages and constellation diagram and waveforms 3.4 Bandwidth, spectrum for each modulation technique and their comparison	<b>7</b>	<b>12</b>
	<b>Sub-total</b>	<b>22</b>	<b>34</b>

## Section II

Sr. no.	Topics/Subtopics	Learning (Hours)	Classroom learning evaluation Marks
	<b><i>ETH308-4 Use Coding methods and Error control techniques as per need .</i></b>		

<b>4</b>	<b>Coding methods and Error control</b> 4.1 Source coding, Shannon-Fano coding, Huffman coding. 4.2 error detection & correction codes 4.2.1 Using parity, Hamming code & CRC code 4.3 Linear block code-calculation of minimum Hamming distance, error detection capability, error correction capability, Hamming code generation 4.4 Line coding: Need, properties 4.4.1 Unipolar RZ and NRZ, Polar RZ and NRZ, 4.4.2 Bipolar NRZ (AMI), split phase and differential Manchester, Polar quaternary and their waveforms 4.5 Channel coding: Error, Causes of error and its effects, 4.6 Comparison between different coding 4.7 simple numerical	<b>8</b>	<b>12</b>
<b><i>ETH308-5 Interpret concept of multiplexing and multiple access techniques</i></b>			
<b>5</b>	<b>Multiplexing and Multiple Access</b> 5.1 Need of Multiplexing, 5.2 TDM, FDM, CDM, definition, block diagram and their comparison. 5.3 Introduction to WDM. 5.4 E & T carrier multiplexing hierarchy 5.5 Access technique –Introduction, need and methods 5.5.1 Time Division Multiple Access (TDMA) 5.5.2 Frequency Division Multiple Access FDMA 5.5.3 Code Division Multiple Access CDMA 5.6 Advantages of TDMA over FDMA.	<b>7</b>	<b>10</b>
<b><i>ETH308-6 Interpret the concept of various spread spectrum techniques</i></b>			
<b>6</b>	<b>Spread spectrum modulation</b> 6.1 Introduction, Advantages over fixed frequency PN Sequence, generation and maximum length sequence spread spectrum modulation 6.2 block diagram of spread spectrum modulation system. 6.3 Direct sequence spread spectrum signal. jamming margin, processing gain, $E_b/N_0$ ratio 6.4 Frequency hop spread spectrum, slow frequency hopping, and fast frequency hopping, comparison of FHSS and DSSS 6.5 Application S. S. modulations	<b>8</b>	<b>14</b>
	<b>Sub-total</b>	<b>23</b>	<b>36</b>

### G. Specification table for setting question paper for semester end theory assessment

Section / Topic no.	Name of topic	Distribution of marks (level wise)			Total marks	CO
		Remember	Understand	Apply		
I / 1	Introduction of Digital Communication	4	4	-	8	ETH308-1
I / 2	Pulse Communication	4	4	6	14	ETH308-2
I / 3	Digital Modulation Techniques	2	4	6	12	ETH308-3
II / 4	Coding methods and Error control	-	4	8	12	ETH308-4
II / 5	Multiplexing and Multiple Access	2	4	4	10	ETH308-5
II / 6	Spread spectrum modulation	4	6	4	14	ETH308-6
Total Marks		16	26	28	70	

### H. Assessment Criteria

**i) Formative Assessment of Practical: -**

Every assignment shall be assessed for 25 marks as per following criteria:

Domain	Particulars	Marks out of 25
Cognitive	Understanding	05
	Application	05
Psychomotor	Operating Skills	05
	Drawing / drafting skills	05
Affective	Discipline and punctuality	05
<b>TOTAL</b>		<b>25</b>

**ii) Summative Assessment of Practical:**

Every practical assignment shall be assessed for 25 marks as per following criteria:

Sr. no	Criteria	Marks allotted
1	Attendance at regular practical	05
2	Preparedness for practical	05
3	Neat & complete Diagram.	05

4	Observations & handling of instrument.	05
5	Oral Based on Lab work and completion of task	05
<b>TOTAL</b>		<b>25</b>

### I. Instructional Methods:

13. Lectures cum Demonstrations
14. Class room practices
15. Use of projector and soft material for demonstration
4. Virtual Laboratory

### J. Teaching and Learning resources:

Chalk board, LCD presentations, Demonstrative kits, Demonstrative charts

### K. Reference Books:

Sr. No.	Name of Book	Author	Publication
1	Digital communication	Sanjay Sharma	S.K.Katariya and sons
2	Digital communication	Rao Ramkrishna P.	Mc Graw Hill Education
3	Digital communication	Simon Haykin	John Wiley and sons
4	Communication Systems	R.P.Singh,S.D.Sapre	Tata Mcgraw hill

### L. Text Books

Sr. No	Name of Book	Author	Publication
1	V. K. Mehta	Electronics Devices and Circuit Theory	S.Chand
2	Electronic communication system	Wayne Tomasi	Pearson
3	Modern Digital and communication	B.P.Lathi	Oxford university press

### M. Learning Website & Software

- i) <https://nptel.ac.in/courses/117105077>
- ii) [www.learningaboutelectronics.com](http://www.learningaboutelectronics.com)
- iii) [www.electronics-tutorials.com](http://www.electronics-tutorials.com)
- iv) <https://www.vlab.co.in/>

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**COURSE ID:**

**COURSE NAME : POWER ELECTRONICS**

**COURSE CODE : ETH309**

**COURSE ABBREVIATION : HPTX**

**A. LEARNING SCHEME:**

Scheme component		Hours	Credits
Actual Contact Hours / week	Class room Learning	04	03
	Tutorial Learning	00	
	Laboratory Learning	02	
	SLH-Self Learning	00	
	NLH-Notional Learning	06	

**B. ASSESSMENTSCHEME:-**

PAPER DURAT ION IN HRS	THEORY				BASEDONLL&TL				BASEDON SLA		Total
					Practical						
03	FA- TH	SA-TH	TOTAL		FA-PR		SA-PR		MAX	MIN	
	MAX	MAX	MAX	MIN	MAX	MIN	MAX	MIN			
	30	70	100	40	25	10	-	-	-	-	125

**C. ABBREVIATIONS:-**CL-Class Room Learning, TL-Tutorial Learning, LL-Laboratory Learning, SLH-Self Learning Hours, NLH-Notional Learning Hours, FA-Formative Assessment, SA -Summative assessment, IKS - Indian Knowledge System, SLA - Self Learning Assessment **Legends:** @ Internal Assessment, # External Assessment, \*# On Line Examination, @\$Internal Online Examination .

1. FA-TH represents average of two class tests of 30 marks each conducted during the semester.
2. If candidate is not securing minimum passing marks in FA-PR of any course then the candidate shall be declared as "Detained" in that semester.
3. If candidate is not securing minimum passing marks in SLA of any course then the candidate shall be declared as fail and will have to repeat and resubmit SLA work.
4. Notional Learning hours for the semester are(CL+LL+TL+SL) hrs.\*15 Weeks
5. 1(one) credits equivalent to 30Notionalhrs.

6. \*Self learning hours shall not be reflected in the Time Table.

\*Self learning includes micro-project /assignment/other activities.(Provide list of all assignments here in tabular format At least 6 to 8 assignments to be given)

### D. i)RATIONALE:-

Diploma engineers must deal with the various electronic components while maintaining various electronic equipment/systems. The use of basic electronics components and handling of various electronics systems will help them troubleshoot electronics equipment used in industry or in the consumer market etc. This course is developed to empower the students to apply their knowledge to solve broad electronic engineering application problems.

### ii)INDUSTRY/EMPLOYER EXPECTED OUTCOME

The aim of this course is to attend following industry identified competency through various teaching learning experiences: • Maintain electronic equipment/systems comprising of discrete electronic components.

### E. COURSE LEVEL LEARNING OUTCOMES (COs)

ETH309 -1 Identify and use relevant power electronics components, firing circuits in Power electronic circuits

ETH309-2 Identify and use relevant power firing circuits/commutation circuit/protection circuits in Power electronic circuits. .

ETH309-3 Use the controlled rectifier in relevant industrial circuit.

ETH309 -4 Use the Chopper in relevant industrial circuits.

ETH309-5 Use relevant Inverter/ cyclo-converter in industrial circuits

ETH309-6 Implement and use of relevant circuit in power electronics applications

### Course outcomes and programme outcomes/ programme specific outcomes (CO- PO/PSO) matrix

[Note: Correlation levels: 1: Slight (Low), 2: Moderate (Medium), 3: Substantial (High), “-”: No correlation]

Cos	Programme Outcomes POs and PSOs								
	PO 1 Basic and Discipline specific knowledge	PO 2 Problem Analysis	PO 3 Design/De velopment of solutions	PO 4 Engineering Tools, Experimenta tion and Testing	PO 5 Engineering Practices for society, sustainability and Environment	PO 6 Project Managemen t	PO 7 Life- long Learning	PSO1 Operate and Maintain	PSO2 Supervision and Providing Solution
ETH309-1	3	2	-	3	-	-	--	2	--
ETH309-2	3	2	-	2	--	--	--	3	1
ETH309-3	3	-	-	2	-	--	--	3	1
ETH309-4	3	1	-	2	--	--	--	3	--
ETH309-5	3	2	-	3	-	--	--	3	1
ETH309-6	-	-	--	3	--	--	2	3	3

## F. CONTENT:

### I) Practical Exercises (Any10)

Sr. No.	Laboratory experiences	CO
1.	Test the SCR(using IC2N4103 or any other equivalent) in forward conduction state and measure holding current ( $I_H$ ) and latching current ( $I_L$ ).	ETH309-1
2.	Test the forward and transfer characteristics of given IGBT(BUP402 or Any other equivalent).	ETH309-1
3.	Test the performance of DIAC(DB3/DB4) and plot its V-I characteristics.	ETH309-1
4.	Test the variation of R, C in R/RC triggering circuit on firing angle of SCR. Measurement of output voltage by changing firing angle through Variation in resistor, capacitor in R and RC triggering circuits of SCR.	ETH309-2
5.	Basic Diac- Triac Full-wave phase control.	ETH309-2
6.	Test the variation of R, C in UJT triggering circuit of SCR.	ETH309-2
7.	Observe and verify Input-Output waveforms of Class C-Complimentary type commutation circuit.	ETH309-2
8.	Test the performance of class-D commutation circuit.	ETH309-2
9.	Use the CRO to observe the output waveform of Half Controlled Rectifier With resistive and inductive load and determine the output voltage	ETH309-3
10.	Use the CRO to observe the output waveform of Full Controlled Rectifier using mid-point and bridge configuration. Use resistive as well as inductive load with free-wheeling diode and determine the output voltage	ETH309-3
12.	Use the CRO to observe the output waveform of Full Controlled Rectifier using mid-point and bridge configuration. Use resistive as well as inductive load with free-wheeling diode and determine the output voltage	ETH309-3
13.	Find the output voltage of Jones's Chopper	ETH309-4
14.	Study Step Down Chopper with resistive-inductive load Measure output voltage of step-up chopper for different values of duty cycles.	ETH309-4
15.	Test the Series Inverter circuit to measure the output frequency and output voltage	ETH309-5
16.	Test the Parallel Inverter circuit to measure the output frequency and output voltage	ETH309-5
17.	Study single phase mid-point cyclo-converter with resistive and inductive load	ETH309-5
18.	Speed control of DC series motor using SCR	ETH309-6



## II) Theory

### Section I

Sr. no.	Topics/Subtopics	Learning (Hours)	Classroom learning evaluation Marks
	ETH309-1 Identify and use relevant power electronics devices, firing circuits in Power electronic circuits.		
1	<b>Power Semiconductor Devices</b> 1.1 Classification of Thyristor family devices 1.2 Construction, working principle, V-I characteristics and applications of Power diode, Power transistor, Power MOSFET and IGBT. 1.3 SCR-Construction, working principle, V-I characteristics, Two transistor analogy, latching and holding current for SCR 1.4 LASCR, TRIAC, GTO, SCS - Construction, working principle, V-I characteristics. 1.5 Triggering devices : UJT, PUT, SUS, SBS, DIAC - Construction, working principle, V-I characteristics	9	9
	ETH309-2. Identify and use relevant power firing circuits/commutation circuit/protection circuits in Power electronic circuits.		
2	<b>Triggering and Commutation methods of SCR</b> 2.1 Turn-ON methods of SCR Forward voltage triggering, thermal triggering, Radiation triggering, dv/dt triggering, gate triggering, 2.2 Gate trigger circuits: DC Gate triggering, AC Gate triggering and pulse Gate triggering 2.3 Gate Triggering Circuits: Resistance firing Circuit, Resistor-Capacitor(RC) firing Circuit, UJT triggering Circuit, PUT-relaxation oscillator circuit 2.4 Turn OFF(commutation) methods: Natural and Forced Commutation, Types: Class-A, Class-B, Class-C, Class-D, Class- E, Class- F 2.5 SCR protection circuits: di/dt protection, dv/dt protection. Snubber circuit, Electronics crowbar protection circuit	9	12

	ETH309-3. Use the controlled rectifier in relevant industrial circuit.		
<b>3</b>	<b>Phase controlled rectifiers</b> 3.1 Phase control parameters: Firing angle and conduction angle 3.2 Single phase half wave controlled rectifier: circuit diagram, working and waveforms with R and RL-load, effect of free-wheeling diode with RL-load, numerical 3.3 Single phase full wave controlled Rectifier (Centre tapped and Bridge): circuit diagram, working and waveforms with R and RL load, effect of freewheeling diode with RL load 3.4 Semi-converters: circuit diagram, working and waveforms with R and RL load 3.5 Effect of input source impedance on Single phase Full-wave controlled rectifier 3.6 Three phase half converters with R/R <sub>L</sub> load (firing angle 0° and 45° only)	<b>12</b>	<b>13</b>
	<b>Sub-total</b>	<b>30</b>	<b>34</b>

## Section II

Sr. no.	Topics/Subtopics	Learning (Hours)	Classroom learning evaluation Marks
	ETH309-4 Use the Chopper in relevant industrial circuits		
<b>4</b>	<b>Choppers</b> 4.1 Classification of Choppers: a) According to the Input /Output voltage level b) According to Circuit operation ( First -Quadrant chopper, Two-Quadrant chopper, Four- Quadrant chopper) c) According to the directions of output voltage and current ( Class/Type A to E) d) According to Commutation Method 4.2 Basic chopper operation: a) Principle of Step-Down chopper b) Principle of Step-up chopper 4.3 Control Strategies: Time-ratio control , Current limit control 4.4 Jone's Chopper	<b>7</b>	<b>12</b>
	ETH309-5 Use relevant Inverter/cyclo-converter in industrial circuits		

<b>5</b>	<b>Inverters and Cyclo-converter</b> 5.1 Classification of Inverters 5.1.1 Series inverter: Basic, Modified and Improved Series inverter 5.1.2 Parallel inverter: with/without feedback diode 5.1.3 Single phase SCR bridge inverter (simple circuit), (Half/Full Bridge inverter with R/ $R_L$ Load) 5.1.3 McMurray Half-bridge Inverter (circuit, waveform) McMurray Full-bridge Inverter (circuit operation only) (McMurray-Bedford inverter not included)  5.2 Performance parameters for the inverter (Definition and importance only) : Harmonic factor of $n^{th}$ Harmonic, Total Harmonic Distortion, Distortion Factor, Lowest order Harmonic  5.3 Voltage control of single-phase inverter (importance and methods) 5.3.1 External control of A.C. output voltage 5.3.2 External control of D.C. input voltage 5.3.3 Internal control of inverter: a) Series inverter control and b) Pulse-width modulation control : Single-pulse width modulation, Multiple-pulse width modulation, Sinusoidal-pulse width modulation <b>5.4 Cyclo-converter</b> 5.4.1 Basic Principle of Operation Cyclo-Converters 5.4.2 Classification of Cyclo-Converters : : Single phase to single phase Cyclo-Converters (resistive and inductive load) : Center-tapped Transformer configuration and Bridge configuration	<b>16</b>	<b>16</b>
	ETH309-6 Implement and use of relevant circuit in power electronics applications		
<b>6</b>	<b>Industrial applications of power electronic devices</b> 6.1 Battery charger using SCR 6.2 Emergency light system using SCR 6.3 DC low power Flasher circuit using SCR 6.4 Static AC and DC circuit breaker using SCR 6.5 Time delay circuit using SCR and UJT 6.6 Block diagram and concept of Online and Offline UPS, Line-interactive UPS system. 6.7 SMPS : concept, block diagram and applications	<b>7</b>	<b>8</b>
	<b>Sub-total</b>	<b>23</b>	<b>36</b>

### G. Specification table for setting question paper for semester end theory assessment

Section/ Topic no.	Name of topic	Distribution of marks(level wise)			Total marks	CO
		Remember	Understand	Apply		
I/ 1	Power Semiconductor Devices	4	6		9	ETH309-1
I/ 2	Triggering and Commutation methods of SCR	2	4	6	12	ETH309-2
I/ 3	Phase controlled rectifiers	2	6	6	13	ETH309-3
II/4	Choppers	2	6	4	12	ETH309-4
II/5	Inverters and Cyclo-converter	2	6	6	16	ETH309-5
II/ 6	Industrial application of power electronics devices		4	4	8	ETH309-6
Total Marks		12	32	26	70	

### H. Assessment Criteria

#### i) Formative Assessment of Practical: -

Every assignment shall be assessed for 25 marks as per following criteria:

Domain	Particulars	Marks out of 25
Cognitive	Understanding	05
	Application	05
Psychomotor	Operating Skills	05
	Drawing/ drafting skills	05
Affective	Discipline and punctuality	05
<b>TOTAL</b>		<b>25</b>

### I. Instructional Methods:

1. Lectures cum Demonstrations
2. Classroom practices
3. Use of projector and soft material for demonstration
4. Virtual Laboratory

### J. Teaching and Learning resources

Chalkboard, LCD presentations, Demonstrative kits, Demonstrative charts

### K. Reference Books:

Sr. No.	Name of Book	Author	Publication
1	Power Electronics.	M D Shingh /KB Khanchandani	Tata McGraw Hill, New Delhi, 2012 or latest
2	Power Electronics	Dr. Bimbhra P.S.	Khanna Publication, New Delhi
3	Power Electronics	P C Sen	Tata McGraw Hill, New Delhi, 2012 or latest
4	Power Electronics.	Rashid M	McGraw Hill
5	Industrial Electronics and Control	S K Bhattacharya, S Chatterjee	TTTI Chandigarh

### L. Text Books

Sr. No	Name of Book	Author	Publication
1	Power Electronics.	JS Katre	Tech-knowledge publication, Pune
2	Power Electronics.	Dr Chitode	Techmax publication, Pune
3	Industrial and power Electronics	Shingare	Electrotech Publication
4	Industrial Electronics-	Shingare	

### M. Learning Website & Software

- i) [www.nptel.iitm.ac.in](http://www.nptel.iitm.ac.in)
- ii) [www.learningaboutelectronics.com](http://www.learningaboutelectronics.com)
- iii) [www.electronics-tutorials.com](http://www.electronics-tutorials.com)
- iv) <https://circuitdigest.com/electronic-circuits>
- v) [https://www.tutorialspoint.com/power\\_electronics/power\\_electronics\\_transistors.htm](https://www.tutorialspoint.com/power_electronics/power_electronics_transistors.htm)
- vi) [https://www.youtube.com/watch?v=O\\_pqCNPs6xw](https://www.youtube.com/watch?v=O_pqCNPs6xw)
- vii) <https://www.youtube.com/watch?v=0nXEUKFBd8A>

**COURSE ID:**

**COURSE NAME : SIMULATION SOFTWARE**  
**COURSE CODE : ETH401**  
**COURSE ABBREVIATION : HSMS**

**A. LEARNING SCHEME:**

Scheme component		Hours	Credits
Actual Contact Hours / week	Classroom Learning	-	02
	Tutorial Learning	-	
	Laboratory Learning	04	
	SLH-Self Learning	-	
	NLH- Notional Learning	04	

**B. ASSESSMENT SCHEME:**

PAPER DURATION IN HRS	THEORY				BASED ON LL&TL				BASED ON SLA		Total
					Practical						
03	FA-TH	SA-TH	TOTAL		FA -PR		SA-PR		MAX	MIN	
	MAX	MAX	MAX	MIN	MAX	MIN	MAX	MIN			
	-	-	-	-	25	10	25@	10	-	-	50

**C. ABBREVIATIONS:**

CL- Class Room Learning, TL- Tutorial Learning, LL-Laboratory Learning, SLH-Self Learning Hours, NLH- Notional Learning Hours, FA - Formative Assessment, SA -Summative assessment, IKS - Indian Knowledge System, SLA - Self Learning Assessment

**Legends:** @ Internal Assessment, # External Assessment, \*# Online Examination, @\$ Internal Online Examination.

1. FA-TH represents average of two class tests of 30 marks each conducted during the semester.
2. If candidate is not securing minimum passing marks in FA-PR of any course then the candidate shall be declared as "Detained" in that semester.
3. If candidate is not securing minimum passing marks in SLA of any course then the candidate shall be declared as fail and will have to repeat and resubmit SLA work.
4. Notional Learning hours for the semester are (CL+LL+TL+SL) hrs.\* 15 Weeks
5. 1(one) credit is equivalent to 30 Notional hrs.
6. \* Self learning hours shall not be reflected in the Time Table.

\* Self learning includes micro project / assignment / other activities. (Provide list of all assignments here in tabular format At least 6 to 8 assignments to be given)

## D. I) RATIONALE:

Industry expects a Diploma Engineer (technologist) to use modern day Electronic Design Automation (EDA) tools for analyzing, designing, and real time testing of analog, digital and mixed electronic circuits and their PCB layouts. These operations are useful in developing, fabricating and testing new prototype circuits. Using basic features of EDA tool prepares student for learning advanced aspects of the modern EDA tool such as MATLAB, SCILAB and Orcad Capture using the simulation software for design of complex circuits.

EDA tool such as SCILAB, MATLAB and Orcad Capture etc are very powerful mathematical computation and electronic circuit design, simulation, and analysis tools. If an engineering problem can be solved using software tool, it is usually more efficient to use the software tool than to write a program in a computer language to solve the problem.

## II) INDUSTRY / EMPLOYER EXPECTED OUTCOME:

The aim of this course is to attend the following industry/ employer expected outcome through various teaching learning experiences.

## E. COURSE LEVEL LEARNING OUTCOMES (COs)

- ETH401- 1** Identify SCILAB / MATLAB OR EQUIVALENT OPEN SOURCE SOFTWARE environment.
- ETH401- 2** Develop SCILAB / MATLAB OR EQUIVALENT OPEN SOURCE SOFTWARE programs using inbuilt functions.
- ETH401- 3** Build communication models in SIMULINK using communication blockset.
- ETH401- 4** Identify and use OrCAD Capture, OrCAD PSpice and OrCAD Layout tools of OrCAD software.
- ETH401-5** Design schematic of electronic circuits in OrCAD Capture.
- ETH401-6** Simulate and create PCB layout of electronic circuits in OrCAD Pspice.

### Course outcomes and programme outcomes/ programme specific outcomes (CO- PO/PSO) matrix

[Note: Correlation levels: 1: Slight (Low), 2: Moderate (Medium), 3: Substantial (High), “-“ : No correlation]

Cos	Programme Outcomes POs and PSOs								
	PO 1 Basic and Discipline specific knowledge	PO 2 Problem Analysis	PO 3 Design / Development of solutions	PO 4 Engineering Tools, Experimenta tion and Testing	PO 5 Engineering Practices for society, sustainability and Environment	PO 6 Project Managem ent	PO 7 Life- long Learning	PSO1 Operate and Maintain	PSO2 Supervision and Providing Solution
<b>ETH401-1</b> Identify SCILAB / MATLAB OR EQUIVALENT OPEN SOURCE SOFTWARE environment.	-	1.00	-	1.00	-	-	-	-	-
<b>ETH401-2</b> Develop SCILAB / MATLAB OR EQUIVALENT OPEN SOURCE SOFTWARE programs using inbuilt functions.	2.00	1.00	2.00	1.00	-	-	-	2.00	-
<b>ETH401-3</b> Build communication models in	2.00	2.00	2.00	1.00	-	-	-	2.00	2.00

Cos	Programme Outcomes POs and PSOs								
	PO 1 Basic and Discipline specific knowledge	PO 2 Problem Analysis	PO 3 Design / Development of solutions	PO 4 Engineering Tools, Experimenta tion and Testing	PO 5 Engineering Practices for society, sustainability and Environment	PO 6 Project Managem ent	PO 7 Life- long Learning	PSO1 Operate and Maintain	PSO2 Supervision and Providing Solution
SIMULINK using communication blockset.									
<b>ETH401- 4</b> Identify and use OrCAD Capture, OrCAD PSpice and OrCAD Layout tools of OrCAD software.	-	1.00	-	1.00	-	-	-	-	-
<b>ETH401-5</b> Design schematic of electronic circuits in OrCAD Capture.	2.00	1.00	2.00	1.00	-	-	-	2.00	-
<b>ETH401-6</b> Simulate and create PCB layout of electronic circuits in OrCAD Pspice.	2.00	2.00	2.00	1.00	-	-	-	2.00	2.00

## F. CONTENT:

### II) Practical Exercises

The following practical exercises shall be conducted in the *Laboratory for Simulation Software developed* by the Institute in practical sessions of batches of about 20- 22 students:  
(Any 20 out of 25)

Sr. No.	Laboratory experiences	CO
1.	Introduction to SCILAB / MATLAB OR EQUIVALENT OPEN SOURCE SOFTWARE.	ETH401-1
2.	SCILAB / MATLAB OR EQUIVALENT OPEN SOURCE SOFTWARE Command line window.	ETH401-1, ETH401-2
3.	Mathematical operations in SCILAB / MATLAB OR EQUIVALENT OPEN SOURCE SOFTWARE.	ETH401-2
4.	Matrix Operations using SCILAB / MATLAB OR EQUIVALENT OPEN SOURCE SOFTWARE.	ETH401-2
5.	Plotting functions using SCILAB / MATLAB OR EQUIVALENT OPEN SOURCE SOFTWARE.	ETH401-3
6.	File handling & String manipulation using SCILAB / MATLAB OR EQUIVALENT OPEN SOURCE SOFTWARE.	ETH401-3
7.	Looping & Branching instructions using SCILAB / MATLAB OR EQUIVALENT OPEN SOURCE SOFTWARE.	ETH401-1, ETH401-2
8.	Introduction to Communication BlockSet using SCILAB / MATLAB OR EQUIVALENT OPEN SOURCE SOFTWARE.	ETH401-3
9.	Analysis of Amplitude Modulation using SCILAB / MATLAB OR EQUIVALENT OPEN SOURCE SOFTWARE.	ETH401-3
10.	Analysis of Frequency Modulation using SCILAB / MATLAB OR EQUIVALENT OPEN SOURCE SOFTWARE.	ETH401-3
11.	Introduction to OrCAD tools -OrCAD Capture, OrCAD PSpice, OrCAD Layout (OrCAD, Multisim or Equivalent Open Source Software)	ETH401-4
12.	Designing circuit using OrCAD Capture such as Rectifiers, Filters, Amplifier-Oscillators, Multivibrators. (OrCAD, Multisim or Equivalent Open Source Software)	ETH401-4, ETH401-5



Sr. No.	Laboratory experiences	CO
13.	Analysis of electronic circuits using OrCAD Pspice. (OrCAD, Multisim or Equivalent Open Source Software)	ETH401-5
14.	PCB Layout designing of above electronic circuits using OrCAD Layout. (OrCAD, Multisim or Equivalent Open Source Software)	ETH401-6
15.	Realize Boolean expression using logic gates and verify its truth table by OrCAD, Multisim or Equivalent Open Source Software.	ETH401-4, ETH401-5
16.	Simulate adder/subtractor and verify the truth table by using OrCAD, Multisim or Equivalent Open Source Software.	ETH401-4, ETH401-5
17.	Verification of equivalent resistance formulae by using OrCAD, Multisim or Equivalent Open Source Software.	ETH401-4, ETH401-5
18.	Verification of KVL by using OrCAD, Multisim or Equivalent Open Source Software.	ETH401-4, ETH401-5
19.	Verification of KCL by using OrCAD, Multisim or Equivalent Open Source Software.	ETH401-4, ETH401-5
20.	Clamper and Clipper Circuit by using OrCAD, Multisim or Equivalent Open Source Software.	ETH401-4, ETH401-5
21.	V-I Characteristics of Diode by using OrCAD, Multisim or Equivalent Open Source Software.	ETH401-5
22.	Simulation of Single stage CE and Two stages RC coupled amplifier using Transistor/FET by using OrCAD, Multisim or Equivalent Open Source Software.	ETH401-5
23.	Simulation of Inverting and Non-inverting amplifier using IC 741 op-amp by using OrCAD, Multisim or Equivalent Open Source Software.	ETH401-5
24.	Simulation of First Order Low Pass and High Pass Butterworth filter using IC 741 op-amp by using OrCAD, Multisim or Equivalent Open Source Software.	ETH401-5
25.	Simulation of Interpret transit analysis of phase shift oscillator/Wien bridge oscillator/ Colpitts/ Hartly Oscillators using Op-amp IC 741 and Transistor by using OrCAD, Multisim or Equivalent Open Source Software.	ETH401-5

\*\* Note- For practical exercises 1 to 10 use MATLAB/ SCILAB or Equivalent Open Source Software and for 11 to 25 use OrCAD, Multisim or Equivalent Open Source Software.

## G. Assessment Criteria

### I. Formative Assessment of Practical:

Every assignment shall be assessed for 25 marks as per following criteria:

Domain	Particulars	Marks out of 25
Cognitive	Understanding	05
	Application	05
Psychomotor	Operating Skills	05
	Drawing / drafting skills	05
Affective	Discipline and punctuality	05
<b>TOTAL</b>		<b>25</b>

## II. Summative Assessment of Practical:

Every practical assignment shall be assessed for 25 marks as per following criteria:

Sr. no	Criteria	Marks allotted
1	Attendance at regular practical	05
2	Preparedness for practical	05
3	Neat & complete Diagram.	05
4	Observations & handling of instrument.	05
5	Oral Based on Lab work and completion of task	05
<b>TOTAL</b>		<b>25</b>

## H. Instructional Methods:

16. Practical Demonstrations
17. Laboratory practices
18. Use of projector and soft material for demonstration
4. Virtual Laboratory

## I. Teaching and Learning resources:

Virtual laboratories, Software tutorials, Live simulation softwares etc.

## J. Reference Books:

Sr. No.	Name of Book	Author	Publication
1	Introduction to MATLAB6	Delores M. Etter, David C. Kuncicky, Doug Hull	Second Edition PEARSON Education Low Price Edition
2	Getting Started With MATLAB7	Rudra Pratap	Oxford University Press, Incorporated, 2006
3	A guide to MATLAB For Beginners and Experienced Users	Brian R. Hunt , Ronald L. Lipsman, Jonathan M. Rosenberg	Cambridge University Press
4	Introduction to P-spice using OrCAD for circuits and Electronics	M. H. Rashid	Pearson Education

## K. Learning Website & Software

- i) <https://in.mathworks.com/>
- ii) <https://www.scilab.org/>
- iii) [https://www.cadence.com/en\\_US/home/tools/pcb-design-and-analysis/orcad.html](https://www.cadence.com/en_US/home/tools/pcb-design-and-analysis/orcad.html)
- iv) <https://www.multisim.com/>

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**COURSE ID :**

**COURSE NAME : SIGNALS AND SYSTEMS**

**COURSE CODE : ETH402**

**COURSE ABBREVIATION : HSYS**

### A. LEARNING SCHEME:

Scheme component		Hours	Credits
Actual Contact Hours / week	Classroom Learning	03	03
	Tutorial Learning	00	
	Laboratory Learning	02	
	SLH-Self Learning	01	
	NLH- Notional Learning	06	

### B. ASSESSMENT SCHEME:-

PAPER DURATION IN HRS	THEORY				BASED ON LL&TL				BASED ON SLA		Total
					Practical						
03	FA-TH	SA-TH	TOTAL		FA -PR		SA-PR		MAX	MIN	150
	MAX		MAX	MIN	MAX	MIN	MAX	MIN			
	30	70	100	40	25	10	-	-	25	10	

**C. ABBREVIATIONS:** - CL- Class Room Learning, TL- Tutorial Learning, LL-Laboratory Learning, SLH-Self Learning Hours, NLH- Notional Learning Hours, FA - Formative Assessment, SA -Summative assessment, IKS - Indian Knowledge System, SLA - Self Learning Assessment

**Legends:** @ Internal Assessment, # External Assessment, \*# On Line Examination, @\$ Internal Online Examination .

1. FA-TH represents average of two class tests of 30 marks each conducted during the semester.
2. If candidate is not securing minimum passing marks in FA-PR of any course then the candidate shall be declared as "Detained" in that semester.
3. If candidate is not securing minimum passing marks in SLA of any course then the candidate shall be declared as fail and will have to repeat and resubmit SLA work.
4. Notional Learning hours for the semester are (CL+LL+TL+SL) hrs.\* 15 Weeks
5. 1(one) credit is equivalent to 30 Notional hrs.
6. \* Self learning hours shall not be reflected in the Time Table.

\* Self learning includes micro project / assignment / other activities. (Provide list of all assignments here in tabular format At least 6 to 8 assignments to be given)

### D. i) RATIONALE: -

In Signals and Systems a signal is a description of how one parameter varies with another parameter. For instance, voltage changing over time in an electronic circuit, or brightness varying with distance in an image. A system is any process that produces an output signal in response to an input signal. Examples of systems that manipulate signals are speech recognition, video streaming, cellular networks and medical scans such as MRI. The disciplines of signal and image processing are concerned with the analysis and synthesis of signals and their interaction with systems

### ii) INDUSTRY / EMPLOYER EXPECTED OUTCOME

The aim of this course is to attend following industry identified competency through various teaching learning experiences: • To identify various signals and their systems for advanced technology in various departments such as aero defense, medical, military, Indian Railways, satellite communications, network based technologies, MATLAB programming etc.

### E. COURSE LEVEL LEARNING OUTCOMES (COs)

ETH402-1 Classify signals and identify ODD and even part of signals

ETH402-2 Analyze operations on signals such as shifting, reversal, scaling etc.

ETH402-3 Identify and classify types of the systems.

ETH402-4 Describe Linear time invariant systems using mathematical models and discuss their properties.

ETH402-5 Explain Fourier series and its properties.

ETH402-6 Explain **Fourier** – Transform properties and analyse signal and system using Z Transform

### Course outcomes and programme outcomes/ programme specific outcomes (CO- PO/PSO) matrix

[Note: Correlation levels: 1: Slight (Low), 2: Moderate (Medium), 3: Substantial (High), “-“ : No correlation]

Cos	Programme Outcomes POs and PSOs								
	PO 1 Basic and Discipline specific knowledge	PO 2 Problem Analysis	PO 3 Design / Development of solutions	PO 4 Engineering Tools, Experimenta tion and Testing	PO 5 Engineering Practices for society, sustainability and Environment	PO 6 Project Managemen t	PO 7 Life- long Learning	PSO1 Operate and Maintain	PSO2 Supervision and Providing Solution
ETH402-1	2	2	1	1	-	-	-	-	-
ETH402-2	2	2	2	2	-	-	-	-	1
ETH402-3	1	2	1	2	-	-	-	1	1
ETH402-4	2	2	2	2	-	-	-	1	1
ETH402-5	2	1	1	1	-	-	-	-	-
ETH402-6	2	2	1	2	-	-	-	-	-

## F. CONTENT:

### I) Practical exercises

The following practical exercises shall be conducted in the *Laboratory for AC Machine* developed by the Institute in practical sessions of batches of about 20- 22 students:

(Any 10 )

Sr. No.	Laboratory experiences	CO
1.	a) Introduction of MATLAB environment b) Basic operations on matrices such as addition, subtraction, multiplication, division	ETH402-1
2.	Generation of various Continuous time signals and sequence using MATLAB inbuilt function such as, Sinusoidal signal, Unit-step signal, Ramp signal, Triangular function, Exponential signal, Impulse signal,	ETH402-1
3.	Write a Program for sampling of continuous time signal.	ETH402-2
4.	Generation of various Discrete time signals and sequence using MATLAB inbuilt function such as, Sinusoidal signal, Unit-step signal, Ramp signal, Triangular function, Exponential signal, Impulse signal	ETH402-1
5.	Operation on signals and sequences (a ) WAP to plot graph of addition of 2 signals (b) WAP to plot graph of Multiplication of 2 signals	ETH402-2
6.	Operation on signals and sequences (a) WAP to plot time shifting of Discrete Time Signal (b) WAP to plot time scaling of Discrete Time Signal (c) WAP to plot time reversal of Discrete Time Signal	ETH402-2
7.	<b>Write a MATLAB program to find the impulse response and step response of a system from its difference equation. Compute and plot the response of a given system to a given input.</b>	ETH402-3
8.	<b>Checking linearity/non-linearity of a system using MATLAB</b>	ETH402-3
9.	<b>Checking causality/non-causality of a system using MATLAB</b>	ETH402-3
10.	WAP for convolution of two DT sequences.	ETH402-4
11.	WAP for cross-correlation of two DT sequences.	ETH402-4
12.	Program for auto-correlation of two DT sequences	ETH402-4
13.	Write a MATLAB program to generate Fourier series of a Square Wave	ETH402-5
14.	WAP to find the Fourier Transform of a given signal and plot its magnitude and phase spectrum	ETH402-6
15.	To locating the zeros and poles and plotting the pole zero maps in s-plane and Z-plane for the given transfer function	ETH402-6

## II) Theory

### Section I

Sr. no.	Topics/Subtopics	Learning (Hours)	Classroom learning evaluation Marks
	<b><i>ETH402-1 Classify signals and identification of ODD and even part of signals</i></b>		
1	<b>Introduction to signals</b> 1.1 A Signal: Definition <b>1.2 Standard Test signals</b> Unit Step function, Unit Impulse function, Ramp signal, Parabolic signal, Exponential signal, Sinusoidal signal, Triangular Signal, Rectangular pulse Signal <b>1.3 Classification of signals</b> 1.3.1 Multichannel and Multidimensional signals 1.3.2 Continuous time Vs discrete time signals 1.3.3 Deterministic Vs random signals 1.3.4 Periodic Vs aperiodic signals 1.3.5 Symmetric (Even) Vs Asymmetric (Odd) signals 1.3.6 Energy and Power signals Comparison of Energy and Power signals 1.7 (Numerical based on Odd and Even signals)	04	08
	<b><i>ETH402-2 Analyze operations on signals such as shifting, reversal, scaling etc</i></b>		
2	<b>Basic Operations on Signals</b> <b>2.1 Basic operations on Continuous time Signals</b> Time Shifting, Time Reversal, Time Scaling, Amplitude scaling, Signal addition, Signal multiplication <b>2.2 Sampling of CT signal</b> <b>2.3 Basic operations on Discrete time Signals</b> Time Shifting, Time Reversal, Time Scaling, Amplitude scaling, Signal addition, Signal multiplication (Numericals based on signal operations)	08	14
	<b><i>ETH 103-3 Identify and classify types of the systems</i></b>		
3	<b>Introduction to Systems</b> <b>3.1 A System:</b> Definition, Block diagram representation of Discrete time systems with blocks adder, a constant multiplier, a signal multiplier, a unit delay element, a unit advance element. <b>3.2 Classification of systems</b> 3.2.1 Continuous time systems and discrete time systems 3.2.2 Static and dynamic systems 3.2.3 Causal and Noncausal System 3.2.4 Linear and Nonlinear system 3.2.5 Time invariant and time variant system 3.2.6 Stable and unstable system (Numerical based on identification of systems)	10	12
	<b>Sub-total</b>	<b>22</b>	<b>34</b>

## Section II

Sr. no.	Topics/Subtopics	Learning (Hours)	Classroom learning evaluation Marks
	<b><i>ETH402-4 4 Describe Linear time invariant systems using mathematical models and discuss their properties</i></b>		
<b>4</b>	<b>Linear time Invariant system and convolution</b> 4.1 Linear time Invariant system 4.2 Convolution using graphical method 4.2.1 Convolution sum of two continuous time sequences 4.2.2 Convolution sum of two discrete time sequences 4.3 Tabular method for convolution of two discrete time sequences 4.4 Properties of discrete convolution 4.5 Concept of Correlation 4.6 Types of correlation 4.6.1 Cross correlation 4.6.2 Auto correlation 4.7 Applications of correlation (Numericals based on Convolution sum using graphical method, tabular method, auto correlation and cross correlation )	<b>09</b>	<b>14</b>
	<b><i>ETH402-5 Explain Fourier series and its properties</i></b>		
<b>5</b>	<b>Fourier Series for Continuous &amp; Discrete Time</b> 5.1 Development of <b>Fourier Series derivation</b> 5.2 Properties of <b>Fourier Series</b> Linearity, Time shifting, Frequency shifting, Time reversal, Time scaling, Multiplication, Convolution (statements only)	<b>04</b>	<b>06</b>
	<b><i>ETH402-6 Explain Fourier – Transform properties and analyse signal and system using Z Transform</i></b>		
<b>6</b>	<b>Continuous Time &amp; Discrete Time Fourier Transform</b> 6.1 Basic concept of <b>Fourier Transform of functions: rectangular signal, Impulse signal</b> 6.2 Properties of <b>Fourier Transform</b> Linearity, Time shifting, Frequency shifting, Time scaling, Multiplication, Convolution (statements and derivation with proof) <b>6.3 Introduction of Z-transform</b> 6.4 Relation between Z transform and Fourier Transform 6.5 ROC (Region of Convergence) 6.5 Pole-zero plot (Simple Numericals on Z transform and Sketch ROC) 6.6 Inverse Z transform 6.6.1 Power series method 6.6.2 Partial fraction expansion method 6.6.3 Residue method	<b>10</b>	<b>16</b>
	<b>Sub-total</b>	<b>23</b>	<b>36</b>

## G. List of Assignments under SLA

Sr.No.	Name of Assignment
1	Solve 10 numerical based on basic operations on Continuous time Signals
2	Solve 10 numerical based on basic operations on discrete time Signals
3	Solve 10 numerical based on all systems.
4	Solve 10 numerical based on all cross correlation.
5	Solve 10 numerical based on all auto correlation.
6	Derive all fourier series properties for continuous signals
7	Derive all fourier transform properties for continuous signals

## H. Specification table for setting question paper for semester end theory assessment

Section / Topic no.	Name of topic	Distribution of marks (level wise)			Total marks	CO
		Rememb er	Understan d	Apply		
I / 1	To Classify signals and illustrate of ODD and even part of signals	2	2	4	08	ETH402-1
I / 2	Analyze operations on signals such as shifting, reversal, scaling etc	2	4	8	14	ETH402-2
I / 3	Identify and classify types of the systems	2	2	8	12	ETH402-3
II / 4	Describe Linear time invariant systems using mathematical models and discuss their properties	2	4	8	14	ETH402-4
II / 5	Explain Fourier series and its properties	2	2	2	06	ETH402-5
II / 6	Transform properties and analyse signal and system using Z Transform	4	4	8	16	ETH402-6
Total Marks		14	18	38	70	

## I. Assessment Criteria

### i) Formative Assessment of Practical: -

Every assignment shall be assessed for 25 marks as per following criteria:

Domain	Particulars	Marks out of 25
Cognitive	Understanding	05
	Application	05
Psychomotor	Operating Skills	05
	Drawing / drafting skills	05
Affective	Discipline and punctuality	05
<b>TOTAL</b>		<b>25</b>



### **J. Instructional Methods:**

19. Lectures cum Demonstrations
20. Class room practices
21. Use of projector and soft material for demonstration
22. Virtual Laboratory
23. NPTEL/Swayam course lectures

### **K. Teaching and Learning resources:**

Chalk board, LCD presentations, Demonstrative kits, Demonstrative charts

### **L. Reference Books:**

<b>Sr. No.</b>	<b>Name of Book</b>	<b>Author</b>	<b>Publication</b>
<b>1</b>	‘Signals & system’	Ramesh Babu	SciTech Publication
<b>2</b>	‘Signals & System’ (Schaum’s out lines)	H. A HSU	Tata McGraw Hill
<b>3</b>	‘Signal & System’	Shah & Bhagli	Mahalaxmi Publication
<b>4</b>	‘Signals & System’	Simon Haykin, Barry Van Veen-	IInd Edition Wiley publication
<b>5</b>	‘Signals & System’	Anand kumar	PHI Publications

### **M. Text Books**

<b>Sr. No</b>	<b>Name of Book</b>	<b>Author</b>	<b>Publication</b>
1	Digital signal processing	N G Palan	Tech max Publication
2	Signals and systems	Dr J S Chitode	Technical Pulicatoins

### **N. Learning Website & Software**

- i. [www.youtube.com](http://www.youtube.com)

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**COURSE ID:**

**COURSE NAME : CONSUMER ELECTRONICS**

**COURSE CODE : ETH501**

**COURSE ABBREVIATION : HSAS**

**A. LEARNING SCHEME:**

Scheme component		Hours	Credits
Actual Contact Hours / week	Classroom Learning	03	03
	Tutorial Learning	00	
	Laboratory Learning	02	
	SLH-Self Learning	01	
	NLH- Notional Learning	06	

**B. ASSESSMENT SCHEME:-**

PAPER DURATION IN HRS	THEORY				BASED ON LL&TL				BASED ON SLA		Total
					Practical						
-	FA-TH	SA-TH	TOTAL		FA -PR		SA-PR		MAX	MIN	50
	MAX	MAX	MAX	MIN	MAX	MIN	MAX	MIN			
	-	-	-	-	-	25	10	-	-	25	

**C. ABBREVIATIONS:** - CL- Class Room Learning, TL- Tutorial Learning, LL-Laboratory Learning, SLH-Self Learning Hours, NLH- Notional Learning Hours, FA - Formative Assessment, SA -Summative assessment, IKS - Indian Knowledge System, SLA - Self Learning Assessment

**Legends:** @ Internal Assessment, # External Assessment, \*# On Line Examination, @\$ Internal Online Examination.

1. FA-TH represents average of two class tests of 30 marks each conducted during the semester.
2. If candidate is not securing minimum passing marks in FA-PR of any course then the candidate shall be declared as "Detained" in that semester.
3. If candidate is not securing minimum passing marks in SLA of any course then the candidate shall be declared as fail and will have to repeat and resubmit SLA work.
4. Notional Learning hours for the semester are (CL+LL+TL+SL) hrs.\* 15 Weeks
5. 1(one) credit is equivalent to 30 Notional hrs.
6. \* Self learning hours shall not be reflected in the Time Table.

\* Self learning includes micro project / assignment / other activities. (Provide list of all assignments here in tabular format At least 6 to 8 assignments to be given)

#### D. i) RATIONALE: -

The usage and demand for consumer electronic appliances is increasing in both domestic as well as industries. This increases the demand for trained man power in the relevant industries. This course will provide working principle of various consumer appliances/gadgets /equipment's and skills to troubleshoot and maintain them in scientific way. The knowledge gained will help the students in the manufacturing units of these consumer gadgets or help the students to start their own enterprise.

#### ii) INDUSTRY / EMPLOYER EXPECTED OUTCOME

The aim of this course is to attend the following industry/employer expected outcome through various teaching learning experiences. Maintain various consumer electronic appliances/equipment's.

#### E. COURSE LEVEL LEARNING OUTCOMES (COs)

ETH501-1 Maintain the given type of audio system.

ETH501-2 Test different types of video systems.

ETH501-3 Troubleshoot various consumer electronic appliances.

ETH501-4 Use various smart appliances.

ETH501-5 Maintain various office automation appliances.

ETH501-6 Analyze the role of communication electronics in everyday life.

#### Course outcomes and Programme outcomes/ Programme specific outcomes (CO-PO/PSO) matrix

[Note: Correlation levels: 1: Slight (Low), 2: Moderate (Medium), 3: Substantial (High), “-”: No correlation]

Cos	Programme Outcomes POs and PSOs								
	PO 1 Basic and Discipline specific knowledge	PO 2 Problem Analysis	PO 3 Design / Develop ment of solutions	PO 4 Enginee ring Tools, Experim entation and Testing	PO 5 Enginee ring Practice s for society, sustaina bility and Environ ment	PO 6 Project Manage ment	PO 7 Life- long Learni ng	PSO1 Operat e and Mainta in	PSO2 Supervisi on and Providing Solution
ETH501-1	3	1	1	3	1	1	3	3	--
ETH501-2	2	1	2	3	1	1	3	3	--
ETH501-3	3	1	2	3	1	1	3	3	--
ETH501-4	2	1	2	3	3	1	3	3	1
ETH501-5	2	1	2	3	1	1	3	3	1
ETH501-6	2	1	2	3	1	1	3	3	2

## F. CONTENT:

### D) Practical exercises

The following practical exercises shall be conducted in the Laboratory in the practical session of batches of about 20- 22 students. Any 12 experiments should be performed.

Sr. No.	Laboratory experiences	CO
1.	Test and measure the various parameters of a microphone.	ETH501-1
2.	Test the given speaker and plot its frequency response.	ETH501-1
3.	Measure voltages at different sections of Hi-Fi amplifier.	ETH501-1
4.	Connect CCTV Cameras to DVR/Rerecord and replay.	ETH501-2
5.	Measure voltage of Power supply, Audio section and Video section of LCD TV. LLO 7.2 Compare the above measured voltage with standard voltage.	ETH501-2
6.	Troubleshoot the faults in a LCD TV- a) No picture, No Audio b) No Audio but proper picture. c) Complete dead TV.	ETH501-2
7.	Locate and rectify faults in a LED TV a) No picture, No Audio b) No Audio but proper picture) Complete dead TV.	ETH501-2
8.	Test the components and operation of the paper feed mechanism in a photocopier machine through dismantling and reassembly.	ETH501-3
9.	Identify and test various front panel controls of microwave oven.	ETH501-3
10.	Interface the scanner to the desktop computer and test its various controls.	ETH501-3
11.	Measure the signal strength of wearable antenna.	ETH501-4
12.	Display faults in smart wrist bands - a) display not working b) poor brightness.	ETH501-4
13.	Interface the laser printer to the desktop computer and identify various controls.	ETH501-5
14.	Interface and configure LED projector using various controls.	ETH501-5
15.	Create new interactive whiteboard pages using Interactive whiteboard simulation software like Mimio Studio, SMART Learning Suite Online.	ETH501-5
16.	Test audio and video settings for a video conferencing session.	ETH501-5
17.	Analyze the signal strength and data speeds of mobile network in different environments.	ETH501-6
18.	Compare internet service providers (ISPs) to find the plan that best suits your needs and budget.	ETH501-6

## II) Theory

### Section I

Sr. no.	Topics/Subtopics	Learning (Hours)	Classroom learning evaluation Marks
	<b><i>ETH501-1 Maintain the given type of audio system.</i></b>		
<b>1</b>	<b>Audio Fundamentals</b> 1.1 Basic characteristics of sound signal: Intensity and loudness, pitch, frequency response, fidelity, sensitivity and selectivity 1.2 Audio Amplifiers: Mono, stereo, quadrasonic, block diagram of Hi- Fi amplifier and its working, use of bass, treble tone controls 1.3 Microphone: Working principle and Types - condenser, crystal, electret, laser 1.4 Speakers: Working principle and types- electrostatic, dynamic, plasma arc, Bluetooth 1.5 Public Address System (PA system) and home theatre: Block diagram and working principle	<b>06</b>	<b>08</b>
	<b><i>ETH501-2 Test different types of video systems.</i></b>		
<b>2</b>	<b>Video Systems</b> 2.1 Closed circuit television (CCTV): functional block diagram, working, installation of CCTV 2.2 Liquid crystal display (LCD) television: Principle, Block diagram and working 2.3 Block diagram and working principle: Light emitting diode (LED) TV, Organic light emitting diode (OLED) TV, Quantum dot light emitting diode (QLED) television 2.4 Direct to Home (DTH) television: Block diagram and working principle 2.5 Smart interactive TV: Features and applications	<b>07</b>	<b>12</b>
	<b><i>ETH501-3 Troubleshoot various consumer electronic appliances.</i></b>		
<b>3</b>	<b>Consumer Electronic Appliances</b> 3.1 Photocopier: Block diagram and working principle 3.2 Microwave Oven: Block diagram, single chip controllers, types, wiring diagram, safety instructions, electrical specifications 3.3 Washing Machine: Block diagram, electrical specifications, types and features of (Automatic, Semi-automatic and Fuzzy Logic) washing machine 3.4 Digital Camera and Camcorder: Working principle, picture processing, picture storage, electrical	<b>09</b>	<b>14</b>

	specification 3.5 Scanner: Working principle, Specifications, types of scanners (Handheld, Flatbed, Sheet fed, Portable Scanners), interface cables, ports and connectors		
	<b>Sub-total</b>	<b>22</b>	<b>34</b>

## Section –II

Sr. no.	Topics/Subtopics	Learning (Hours)	Classroom learning evaluation Marks
	<b><i>ETH501-4 Use various smart appliances.</i></b>		
<b>4</b>	<b>Smart appliances.</b> 4.1 Wearable antenna: Construction, Working principle and applications 4.2 Smart Wrist bands: Construction, applications and functional units (sensors, signal conditioning, microcontrollers, wireless connectivity, power management, firmware, storage) 4.3 Virtual Reality (VR) Headset: Functional block diagram and functional units (tracking unit, processing unit, display unit, sensors, pixel resolution, field of view), virtual reality supported platforms such as Windows Mixed Reality (WMR) 4.4 Augmented Reality (AR) devices: Functional block diagram, working principle, examples 4.5 Recycling of electronic appliances: Regulations and procedures	<b>06</b>	<b>10</b>
	<b><i>ETH501-5 Maintain various office automation appliances.</i></b>		
<b>5</b>	<b>Office Automation appliances</b> 5.1 Laser Printer: Working principle, features, specifications, functional block diagram, control unit and troubleshooting procedure 5.2 Smart Interactive Board: Working procedure, features and specifications 5.3 LED Projector: Working principle, features, specifications, functional block diagram, control unit and troubleshooting procedure 5.4 Biometric Attendance system: Hardware and software components, working procedure 5.5 Video conferencing system: Components and working procedure	<b>09</b>	<b>14</b>
	<b><i>ETH501-6 Analyze the role of communication electronics in everyday life.</i></b>		

<b>6</b>	<b>Communication Electronics</b> <b>6.1 Introduction to Communication Electronics:</b> Basic building blocks of communication systems, modulation and transmission methods. <b>6.2 Cellular Networks:</b> Cellular network technologies (2G,3G, 4G, 5G), network architecture, and mobile phone communication principles. <b>6.3 Wireless Communication Technologies:</b> Various wireless communication technologies like Wi-Fi, Bluetooth, and their applications in consumer electronics. <b>6.4 Network Fundamentals:</b> Network protocols (TCP/IP) network devices (routers, modems), and network topologies.	<b>08</b>	<b>12</b>
	<b>Sub-total</b>	<b>23</b>	<b>36</b>

### G. List of Assignments under SLA

<b>Sr. No.</b>	<b>Micro-projects</b>
1	Compare Battery Life of Portable Devices
2	Control Smart Lights with Voice Commands
3	Develop a Smart Plug Scheduling App (Simulation)
4	Design a Portable Phone Charger
5	Build a Simple Security Camera System (Simulation)
6	Build a Basic Audio Amplifier
7	Design a Passive Audio Crossover (Simulation)
8	Optimize Printer Network Connectivity
9	Build a DIY Document Scanner App
10	Design a Smartphone Signal Booster (Simulation)

Above is a suggestive list of microprojects. Faculty must prepare their own list of microprojects, assignments and activities in a similar way. The faculty must allocate mix of tasks, considering the weakness and strength of the student in acquiring the desired skills. If a microproject is assigned, it is expected to be completed as group of 2 or 3 students

### H. Assessment Criteria

#### i) Formative Assessment of Practical: -

Every assignment shall be assessed for 25 marks as per following criteria:

<b>Domain</b>	<b>Particulars</b>	<b>Marks out of 25</b>
Cognitive	Understanding	05
	Application	05
Psychomotor	Operating Skills	05
	Drawing / drafting skills	05
Affective	Discipline and punctuality	05
<b>TOTAL</b>		<b>25</b>

**I. Instructional Methods:**

- 24. Lectures cum Demonstrations
- 25. Class room practices
- 26. Use of projector and soft material for demonstration
- 4. Virtual Laboratory

**J. Teaching and Learning resources:**

Chalk board, LCD presentations, Demonstrative kits, Demonstrative charts

**K. Reference Books:**

Sr. No.	Name of Book	Author	Publication
1	The Wearable Technology Handbook	Haider Raad	Ohio publishing
2	Virtual Reality for Beginners! How to Understand, Use and Create with VR	Murray Ramirez	Khanna Book Publishing
3	Audio Video Systems: principle and practices and troubleshooting	Gupta R.G.	Create Space Independent Publishing
4	Standard handbook of Audio Engineering	Whitaker Jerry and Benson Blair	McGraw Hill
5	Handbook for Sound Engineering	ELSEVIER-British Library	ELSEVIER-British Library
6	Mastering Digital Television	Whitaker Jerry and Benson Blair	McGraw-Hill

**L. Text Books**

Sr. No	Name of Book	Author	Publication
1	Consumer Electronics	Bali S.P.	Pearson Education India,
2	Audio video systems: principle practices and troubleshooting	Bali R and Bali S.P.	Khanna Book Publishing
3	Innovating with Augmented Reality: Applications in Education and Industry	P Kaliraj, Devi Thirupathi	CRC Press,
4	Multimedia Communications	Jerry D. Gibson.	Academic Press Inc



## M. Learning Website & Software

- i. [https://ed.iitm.ac.in/~raman/agcl/VR\\_Paper.pdf](https://ed.iitm.ac.in/~raman/agcl/VR_Paper.pdf)
- ii. <https://www.nxp.com/assets/block-diagram/en/AugmentedRealityandVirtualRealityHeadsets.pdf>
- iii. [https://www.nxp.com/assets/block-diagram/en/SmartWatch\\_SMARTWATCH.pdf](https://www.nxp.com/assets/block-diagram/en/SmartWatch_SMARTWATCH.pdf)
- iv. [https://www.nxp.com/assets/block-diagram/en/SmartWatch\\_SMARTWATCH.pdf](https://www.nxp.com/assets/block-diagram/en/SmartWatch_SMARTWATCH.pdf)
- v. [https://www.nsdcindia.org/scmp/assets/image/1179656187-CCTV\\_Installation\\_Technician\\_\\_English.pdf](https://www.nsdcindia.org/scmp/assets/image/1179656187-CCTV_Installation_Technician__English.pdf)
- vi. CCTV installation handbook

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**Programme Name/s** ALL

**Programme** :- CE/EE/ME/ET/MT/IF

**Semester** : Fourth

**Course Title** : ENVIRONMENTAL EDUCATION AND SUSTAINABILITY

**Course Code** CCH206

### I. RATIONALE

The survival of human beings is solely depending upon the nature. Thus, threats to the environment directly impact on existence and health of humans as well as other species. Depletion of natural resources and degradation of ecosystems is accelerated due to the growth in industrial development, population growth, and overall growth in production demand. To address these environmental issues, awareness and participation of individuals as well as society is necessary. Environmental education and sustainability provide an integrated, and interdisciplinary approach to study the environmental systems and sustainability approach to the diploma engineers.

### II. INDUSTRY / EMPLOYER EXPECTED OUTCOME

Resolve the relevant environmental issue through sustainable solutions

### III. COURSE LEVEL LEARNING OUTCOMES (COS)

Students will be able to achieve & demonstrate the following COs on completion of course based learning

CO1 - Identify the relevant Environmental issues in specified locality. CO2 - Provide the green solution to the relevant environmental problems. CO3 - Conduct SWOT analysis of biodiversity hotspot

CO4 - Apply the relevant measures to mitigate the environmental pollution.

CO5 - Implement the environmental policies under the relevant legal framework.

### IV. TEACHING-LEARNING & ASSESSMENT SCHEME

Course Code	Course Title	Abbr	Course Category/s	Learning Scheme					Credits	Assessment Scheme											
				Actual Contact Hrs./Week			SLH	NLH		Paper Duration	Theory				Based on LL & TL				Based on SL		Total Marks
				Practical																	
				CL	TL	LL					FA-TH	SA-TH	Total		FA-PR		SA-PR		SLA		
Max	Max	Max	Min	Max	Min	Max	Min	Max	Min	Max	Min	Max	Min								
CCH206	ENVIRONMENTAL EDUCATION AND SUSTAINABILITY	EES	VEC	2	-	-	2	4	2	1.5	30	70*#	100	40	-	-	-	-	25	10	125

[Use Proforma 1 for Assessment of SLA]

## V. THEORY LEARNING OUTCOMES AND ALIGNED COURSE CONTENT

Sr.No	Theory Learning Outcomes (TLO's) aligned to CO's.	Learning content mapped with Theory Learning Outcomes (TLO's) and CO's.	Suggested Learning Pedagogies.
1	<b>CCH206.1</b> 1.1 Explain the need of studying environment and its components. 1.2 Investigate the impact of population growth and industrialization on the relevant environmental issues and suggest remedial solutions 1.3 Explain the Concept of 5 R w.r.t. the given situation 1.4 elaborate the relevance of Sustainable Development Goals in managing the climate change 1.5 Explain the concept of zero carbon-footprint with carbon credit	<b>Unit - I Environment and climate change</b> 1.1 Environment and its components, Types of Environments, Need of environmental studies 1.2 Environmental Issues- Climate change, Global warming, Acid rain, Ozone layer depletion, nuclear accidents. Effect of population growth and industrialization 1.3 Concept of 5R, Individuals' participation in i) 5R policy, ii) segregation of waste, and iii) creating manure from domestic waste 1.4 Impact of Climate change, Factor contributing to climate change, Concept of Sustainable development, Sustainable development Goals (SDGs), Action Plan on Climate Change in Indian perspectives 1.5 Zero Carbon footprint for sustainable development, (IKS-Environment conservation in vedic and pre-vedic India)	Lecture Using Chalk-Board Presentations
2	<b>CCH206.2</b> 2.1 Justify the importance of natural resources in sustainable development 2.2 Explain the need of optimum use of natural resources to maintain the sustainability 2.3 Differentiate between renewable and non-renewable sources of energy 2.4 Suggest the relevant type of energy source as a green solution to environmental issues	<b>Unit - II Sustainability and Renewable Resources</b> 2.1 Natural Resources: Types, importance, Causes and effects of depletion. (Forest Resources, Water Resources, Energy Resources, Land resources, Mineral resources), (IKS- Concepts of Panchmahabhuta) 2.2 Impact of overexploitation of natural resources on the environment, optimum use of natural resources 2.3 Energy forms (Renewable and non-renewable) such as Thermal energy, nuclear energy, Solar energy, Wind energy, Geothermal energy, Biomass energy, Hydropower energy, biofuel 2.4 Green Solutions in the form of New Energy Sources such as Hydrogen energy, Ocean energy & Tidal energy	Lecture Using Chalk-Board Presentations
3	<b>CCH206.3</b> 3.1 Explain the characteristics and functions of ecosystem 3.2 Relate the importance of biodiversity and its loss in the environmental sustainability 3.3 Describe biodiversity assessment initiatives in India 3.4 Conduct the SWOT analysis of the biodiversity hot spot in India 3.5 Explain the need of conservation of biodiversity in the given situation	<b>Unit - III Ecosystem and Biodiversity</b> 3.1 Ecosystem - Definition, Aspects of ecosystem, Division of ecosystem, General characteristics of ecosystem, Functions of ecosystem 3.2 Biodiversity - Definitions, Levels, Value, and loss of biodiversity 3.3 Biodiversity Assessment Initiatives in India 3.4 SWOT analysis of biodiversity hot spot in India 3.5 Conservations of biodiversity - objects, and laws for conservation of biodiversity	Lecture Using Chalk-Board Presentations Video Demonstration

4	<b>CCH206.4</b> 4.1 Classify the pollution based on the given criteria 4.2 Justify the need of preserving soil as a resource along with the preservation techniques 4.3 Maintain the quality of water in the given location using relevant preventive measures 4.4 State the significance of controlling the air pollution to maintain its ambient quality norms 4.5 Compare the noise level from different zones of city with justification 4.6 Describe the roles and responsibilities of central and state pollution control board	<b>Unit - IV Environmental Pollution</b> 4.1 Definition of pollution, types- Natural & Artificial (Man- made) 4.2 Soil / Land Pollution – Need of preservation of soil resource, Causes and effects on environment and lives, preventive measures, Soil conservation 4.3 Water Pollution - sources of water pollution, effects on environment and lives, preventive measures, BIS water quality standards for domestic potable water, water conservation 4.4 Air pollution - Causes, effects, prevention, CPCB norms of ambient air quality in residential area 4.5 Noise pollution - Sources, effects, prevention, noise levels at various zones of the city 4.6 Pollution Control Boards at Central and State Government level: Norms, Roles and Responsibilities	Lecture Using Chalk-Board Presentations
5	<b>CCH206.5</b> 5.1 Explain Constitutional provisions related to environmental protection 5.2 Explain importance of public participation (PPP) in enacting the relevant laws 5.3 Use the relevant green technologies to provide sustainable solutions of an environmental problem 5.4 Explain the role of information technology in environment protection	<b>Unit – V- Environmental legislation and sustainable practices</b> 5.1 Article (48-A) and (51-A (g)) of Indian Constitution regarding environment, Environmental protection and prevention acts 5.2 Public awareness about environment. Need of public awareness and individuals' participation. Role of NGOs 5.3 Green technologies like solar desalination, green architecture, vertical farming and hydroponics, electric vehicles, plant-based packaging 5.4 Role of information technology in environment protection and human health	Lecture Using Chalk-Board Presentations Video Demonstrations

## VI. LABORATORY LEARNING OUTCOME AND ALIGNED PRACTICAL / TUTORIAL EXPERIENCES:

N.A.

## VII. SUGGESTED MICRO PROJECT / ASSIGNMENT/ ACTIVITIES FOR SPECIFIC LEARNING / SKILLS DEVELOPMENT (SELF LEARNING)

### Assignment

Suggest the steps to implement (or improve the implementation) of the 5R policy in your home/institute stating your contribution

Draft an article on India's Strategies to progress across the Sustainable Development Goals

Make a chart of Renewable and non-renewable energy sources mentioning the advantages and disadvantages of each source

Conduct the SWOT analysis of biodiversity hotspot in India

Prepare a mind-mapping for the zero carbon footprint process of your field

Prepare a chart showing sources of pollution (air/water/ soil), its effect on human beings, and remedial actions Any other assignment on relevant topic related to the course suggested by the facilitator

## **UNICEF Certification(s)**

Students may complete the self-paced course launched by Youth Leadership for climate Exchange under UNICEF program on portal [www.mahayouthnet.in](http://www.mahayouthnet.in) . The course encompasses five Modules in the form of Units as given below:

-

Unit 1: Living with climate change

Unit 2 : Water Management and Climate

Action Unit 3: Energy Management and

Climate Action Unit 4 : Waste Management

and Climate Action Unit 5 : Bio-cultural

Diversity and Climate Action

If students complete all the five Units they are not required to undertake any other assignment /Microproject/activities specified in the course. These units will suffice to their evaluations under SLA component

### **Micro project**

Technical analysis of nearby commercial RO plant.

Comparative study of different filters used in Household water filtration unit

Evaluate any nearby biogas plant / vermicomposting plant or any such composting unit on the basis of sustainability and cost-benefit

IKS-Study and prepare a note on Vedic and Pre-Vedic techniques of environmental conservation

Visit a local polluted water source and make a report mentioning causes of pollution Any other activity / relevant topic related to the course suggested by the facilitator

### **Activities**

Prepare a report on the working and functions of the PUC Center machines and its relevance in pollution control. Prepare and analyse a case study on any polluted city of India

Prepare a note based on the field visit to the solid waste management department of the municipal corporation / local authority

Record the biodiversity of your institute/garden in your city mentioning types of vegetation and their numbers

Visit any functional hall/cultural hall /community hall to study the disposal techniques of kitchen waste and prepare a report suggesting sustainable waste management tool

Watch a video related to air pollution in India and present the summary

Any other assignment on relevant topic related to the course suggested by the facilitator

**Note :**

Above is just a suggestive list of microprojects and assignments; faculty must prepare their own bank of microprojects, assignments, and activities in a similar way.

The faculty must allocate judicious mix of tasks, considering the weaknesses and / strengths of the student in acquiring the desired skills.

If a microproject is assigned, it is expected to be completed as a group activity. SLA marks shall be awarded as per the continuous assessment record.

**VIII. LABORATORY EQUIPMENT / INSTRUMENTS / TOOLS / SOFTWARE REQUIRED**

Sr.No	Equipment Name with Broad Specifications	Relevant LLO Number
1	Nil	All

**IX. SUGGESTED WEIGHTAGE TO LEARNING EFFORTS & ASSESSMENT PURPOSE (Specification Table)**

Sr.No	Unit	Unit Title	Aligned COs	Learning Hours	R-Level	U-Level	A-Level	Total Marks
1	I	Environment and climate change	CO1	6	4	4	4	12
2	II	Sustainability and Renewable Resources	CO2	08	4	4	8	16
3	III	Ecosystem and Biodiversity	CO3	6	4	4	4	12
4	IV	Environmental Pollution	CO4	10	4	8	6	20
5	V	Environmental legislation and sustainable practices	CO5	5	4	4	4	10
<b>Grand Total</b>				<b>30</b>	<b>20</b>	<b>24</b>	<b>26</b>	<b>70</b>

**X. ASSESSMENT METHODOLOGIES/TOOLS****Formative assessment (Assessment for Learning)**

Two-unit tests (MCQs) of 30 marks will be conducted and average of two-unit tests considered. Formative assessment of self learning of 25 marks should be assessed based on self learning activity such as UNICEF Certification(s)/Microproject/assignment/activities. (60 % weightage to process and 40 % to product)

Two-unit tests (MCQs) of 30 marks will be conducted and average of two-unit tests considered.

Formative assessment of self learning of 25 marks should be assessed based on self learning activity such as UNICEF Certification(s)/Microproject/assignment/activities. (60 % weightage to process and 40 % to product)

**Assessment of Self Learning :-** Based on work done by students as a self learning Activities such as microprojects, assignments and similar activities using proforma 1 marks of SLA can be calculated.

**XI. SUGGESTED COS-POS MATRIX FORM**

Course Outcomes (COs)	Programme Outcomes (POs)							Programme Specific Outcomes* (PSOs)		
	PO-1 Basic and Discipline Specific Knowledge	PO-2 Problem Analysis	PO-3 Design/ Development of Solutions	PO-4 Engineering Tools	PO-5 Engineering Practices for Society, Sustainability and Environment	PO-6 Project Management	PO-7 Life Long Learning	PSO-1	PSO-2	PSO-3
CO1	-	1	-	-	3	2	3			
CO2	-	2	2	-	3	2	3			
CO3	-	-	-	-	3	1	2			
CO4	1	-	-	-	3	2	2			
CO5	1	-	2	-	3	2	3			

Legends :- High:03, Medium:02,Low:01, No Mapping: -  
 \*PSOs are to be formulated at institute level

## XII. SUGGESTED LEARNING MATERIALS / BOOKS

Sr.No	Author	Title	Publisher with ISBN Number
1	Y. K. Singh	Environmental Science	New Age International Publishers, 2006, ISBN: 81-224-2330-2
2	Erach Bharucha	Environmental Studies	University Grants Commission, New Delhi
3	Rajagopalan R.	Environmental Studies: From Crisis to Cure.	Oxford University Press, USA, ISBN: 9780199459759, 0199459754
4	Shashi Chawla	A text book of Environmental Science	Tata Mc Graw-Hill New Delhi
5	Arvind Kumar	A Text Book of Environmental science	APH Publishing New Delhi (ISBN 978-8176485906)

## XIII. LEARNING WEBSITES & PORTALS

Sr.No	Link / Portal	Description
1	<a href="https://sdgs.un.org/goals">https://sdgs.un.org/goals</a>	United Nation's website mentioning Sustainability goals
2	<a href="http://www.greenbeltmovement.org/news-and-events/blog">http://www.greenbeltmovement.org/news-and-events/blog</a>	Green Belt Movement Blogs on various climatic changes and other issues
3	<a href="http://www.greenbeltmovement.org/what-we-do/tree-planting-for-watersheds">http://www.greenbeltmovement.org/what-we-do/tree-planting-for-watersheds</a>	Green Belt Movement's work on tree plantation, soil conservation and watershed management techniques
4	<a href="https://www.youtube.com/@ierekcompany/videos">https://www.youtube.com/@ierekcompany/videos</a>	International Experts For Research Enrichment and Knowledge Exchange – IEREK's platform to exchange the knowledge in fields such as architecture, urban planning, sustainability
5	<a href="http://www.mahayouthnet.in">www.mahayouthnet.in</a>	UNICEF Initiative for youth leadership for climate action
6	<a href="https://eepmoefcc.nic.in/index1.aspx?lsid=297&amp;lev=2&amp;lid=1180&amp;langid=1">https://eepmoefcc.nic.in/index1.aspx?lsid=297&amp;lev=2&amp;lid=1180&amp;langid=1</a>	GOI Website for public awareness on environmental issues
7	<a href="https://egyankosh.ac.in/handle/123456789/61136">https://egyankosh.ac.in/handle/123456789/61136</a>	IGNOU's Initiative for online study material on Environmental studies

8	<a href="https://egyankosh.ac.in/handle/123456789/50898">https://egyankosh.ac.in/handle/123456789/50898</a>	IGNOU's Initiative for online study material on sustainability
9	<a href="https://sustainabledevelopment.un.org/content/documents/11803Official-List-of-Proposed-SDG-Indicators.pdf">https://sustainabledevelopment.un.org/content/documents/11803Official-List-of-Proposed-SDG-Indicators.pdf</a>	Final list of proposed Sustainable Development Goal indicators
10	<a href="https://sustainabledevelopment.un.org/memberstates/india">https://sustainabledevelopment.un.org/memberstates/india</a>	India's Strategies to progress across the SDGs.
11	<a href="https://www.un.org/en/development/desa/financial-crisis/sustainable-development.html">https://www.un.org/en/development/desa/financial-crisis/sustainable-development.html</a>	Challenges to Sustainable Development
12	<a href="https://nptel.ac.in/courses/109105190">https://nptel.ac.in/courses/109105190</a>	NPTEL course on sustainable development
13	<a href="https://onlinecourses.swayam2.ac.in/cec19_bt03/preview">https://onlinecourses.swayam2.ac.in/cec19_bt03/preview</a>	Swayam Course on Environmental studies (Natural Resources, Biodiversity and other topics)
14	<a href="https://onlinecourses.nptel.ac.in/noc23_hs155/preview">https://onlinecourses.nptel.ac.in/noc23_hs155/preview</a>	NPTEL course on environmental studies which encompasses SDGs, Pollution, Climate issues, Energy, Policies and legal framework
15	<a href="https://www.cbd.int/development/meetings/egmbped/SWOT-analysis-en.pdf">https://www.cbd.int/development/meetings/egmbped/SWOT-analysis-en.pdf</a>	SWOT analysis of Biodiversity
16	<a href="https://www.sanskrit.nic.in/SVimarsha/V2/c17.pdf">https://www.sanskrit.nic.in/SVimarsha/V2/c17.pdf</a>	Central Sanskrit University publication on Vedic and pre Vedic environmental conservation
<b>Note :</b> Teachers are requested to check the creative common license status/financial implications of the suggested online educational resources before use by the students		



# **SEMESTER V COURSES**



**Course Name** : ENTREPRENEURSHIP AND START-UP  
**Course Code** : CCH501  
**Course Abbreviation** : HESU

## A. LEARNING SCHEME:

Scheme component	Actual Contact Hours / week	Credits
Classroom Learning (CL)	02	1
Tutorial Learning (TL)	-	
Laboratory Learning (LL)	--	
Self-Learning Hours (SLH)	01	
Notional Learning (NLH)	03	

## B. ASSESSMENT SCHEME:

PAPER DURATI ON IN HRS	Theory				Based on LL & TL				Based on Self Learning		Total Marks
					Practical						
	FA-TH	SA-TH	Total		FA-PR		SA-PR		SLA		
	Max	Max	Max	Min	Max	Min	Max	Min	Max	Min	
-	-	-	-	-	-	-	-	-	50	20	50

(Total IKS Hrs for Sem:1 Hrs)

## C. ABBREVIATIONS:

CL- Classroom Learning, TL- Tutorial Learning, LL-Laboratory Learning, SLH-Self Learning Hours, NLH-Notional Learning Hours, FA - Formative Assessment, SA -Summative assessment, IKS - Indian Knowledge System, SLA - Self Learning Assessment

**Legends:** @ Internal Assessment, # External Assessment, \*# On Line Examination, @\$ Internal Online Examination

1. FA-TH represents average of two class tests of 30 marks each conducted during the semester.
2. If candidate is not securing minimum passing marks in FA-PR of any course then the candidate shall be declared as "Detained" in that semester.
3. If candidate is not securing minimum passing marks in SLA of any course, then the candidate shall be declared as fail and will have to repeat and resubmit SLA work.
4. Notional Learning hours for the semester are (CL+LL+TL+SL) hrs.\* 15 Weeks
5. 1(one) credit is equivalent to 30 Notional hrs.
6. \* Self learning hours shall not be reflected in the Time Table.

\* Self learning includes micro project / assignment / other activities. (Provide list of all 5 assignments here in tabular format)

### D. i) RATIONALE:

Globalization, liberalization and Privatization along with revolution in information technology have opened up new opportunities transforming lives of masses. In this context, there is an immense opportunity of establishing manufacturing, service, trading, marketing and consultancy enterprises by diploma engineer. Our fast-growing economy provides ample scope for diploma engineers to succeed as an entrepreneur. Entrepreneurship requires distinct skill sets which are attempted to be developed through this course. To begin with, this course aims to develop the competency and the related outcomes in order to start small enterprises.

### i) INDUSTRY / EMPLOYER EXPECTED OUTCOME

The aim of this course is to help the student to attain the following industry identified outcome through various learning experiences:

- 1) Understanding and applying business principles and labor laws.
- 2) Improved business skills, imagination and planning of enterprise.

### E. COURSE LEVEL LEARNING OUTCOMES (CO'S)

**CCH501-1:** Identify entrepreneurial attributes

**CCH501-2:** Identify the business opportunities that suits you

**CCH501-3:** Use the support systems to zero down to your business idea.

**CCH501-4:** Develop comprehensive business plans.

**CCH501-5:** Prepare plans to manage the enterprise effectively.

### Competency, course outcomes and programme outcomes/programme specific outcomes (CP-CO-PO/PSO) matrix

[ Note: Correlation levels: 1: Slight (Low), 2: Moderate (Medium), 3: Substantial (High), "0"]

Competency and Cos	Programme outcome POs and PSO's								
	PO 1 Basic and discipline specific knowledge	PO 2 Problem analysis	PO 3 Design/ development of solutions	PO 4 Engineering tools, experimentation & testing	PO 5 Engineering practices for society, sustainability and environment	PO 6 project management	PO 7 Life-long learning	PSO1	PSO2
Competency									
CCH501-1	2	2	2	-	-	3	2	-	3
CCH501-2	2	2	2	2	-	3	2	-	3
CCH501-3	2	2	2	2	-	3	2	-	3
CCH501-4	2	2	2	2	-	3	2	-	3
CCH501-5	2	2	2	2	-	3	2	-	3

**F. CONTENT:****i. Practical Exercise:**

Not Applicable

**ii. THEORY**

<b>Sr. No.</b>	<b>Topics / Sub-topics</b>	<b>Lectures (Hours)</b>
<b>1</b>	<b>Entrepreneurship Development- Concept and Scope</b> <b>Indian Knowledge System for entrepreneurship development(IKS)</b> 1.1 Concepts and Overview of Entrepreneurship. Evolution and Growth of Entrepreneurship in India. Role of Entrepreneurship in Economic Development. Entrepreneurship as a career. 1.2 Traits of successful intrapreneur / entrepreneur: Consistency, creativity, initiative, independent decision making, assertiveness, persuasion, persistence, information seeking, 1.3 Entrepreneurship: Scope in local and global market. 1.4 Intrapreneur and entrepreneur. 1.5 Types of enterprises and their features: Manufacturing, Service and trading. 1.6 Steps in Setting up of a business	<b>03</b>
<b>2</b>	<b>Entrepreneurial Opportunities and Selection Process:</b> 2.1 Product / Service selection: Process, core competence, product / service life cycle, new product / service development process, mortality curve, Creativity and innovation in product / Service modification / development. 2.2 Process selection: Technology life cycle, forms and cost of transformation, Factors affecting process selection, Location for an industry, Material handling. 2.3 Market study procedures: Questionnaire design, sampling, Market survey, Data analysis 2.4 Getting information from concerned stake holders such as Maharashtra Centre for Entrepreneurship Development (MCED), National Institute for Micro, Small and Medium Enterprises (NI-MSME, Prime Minister Employment Generation Program (PMEGP), Directorate of Industries (DI), Khadi Village Industries Commission (KVIC).	<b>04</b>
<b>3</b>	<b>Support Systems:</b> 3.1 Categorization of MSME, Ancillary Industries. 3.2 Support System-Government Agencies: MCED, NI- MSME, PMEGP, DI, KVIC. 3.3 Support agencies for entrepreneurship guidance, training, registration, technical consolation, technology transfer and quality control, marketing and finance 3.4 Breakeven point, return of investment and return on sales.	<b>03</b>
<b>4</b>	<b>BUSINESS PLAN PREPARATION:</b> 4.1 Sources of Product for Business: Feasibility study. 4.2 Ownership, Capital, Budgeting, Matching Entrepreneur with the project, Feasibility report preparation and evaluation criteria. 4.3 Business plan preparation.	<b>03</b>

<b>5</b>	<b>Managing Enterprise:</b> 5.1 Unique Selling proposition (U.S.P.): Identification, Developing a marketing plan. 5.2 Preparing Strategies of handling Business: Policy making, negotiation and bargaining techniques. 5.3 Risk management: [planning for calculated risk taking, initiation with low-cost projects, integrated futuristic planning, angel investors, venture capitalist. 5.4 Incubation centres: Role and procedure.	<b>03</b>
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### G. SUGGESTED MICRO PROJECTS / ASSIGNMENTS/ ACTIVITIES FOR SPECIFIC LEARNING / SKILLS DEVELOPMENT (SELF LEARNING) UNDER SLA

Sr. No.	Assignments	Relevant CO	Approx Hrs. Required
1	Submit a profile summary (about 500 words) of a successful entrepreneur indicating milestone achievement.	1	02*
2	Undertaking SWOC analysis to arrive at your business idea of a product / service.	1	02
3	Survey industries of your stream; grade them according to the level of scale of production, investment, turnover, pollution to prepare a report on it.	2	02
4	Visit a bank/Financial institution to enquire about various funding schemes for small scale enterprise.	2	02*
5	Collect loan application forms of national banks/other financial institutions.	2	02*
6	Compile the information from financial agencies that will help you set up your business enterprise.	3	02*
7	Compile the information from government agencies that will help you set up your business enterprise.	3	02*
8	Prepare Technological feasibility report of a chosen product/service.	3	02*
9	Prepare a set of short term, medium- and long-term goals for starting a chosen small-scale enterprise.	3	02*
10	Prepare marketing strategy for your chosen product/service.	4	02*
11	Compile the information about insurance schemes covering different risk factors.	4	02
12	Find the breakeven point for the business idea chosen by you.	4	02
13	Prepare a business plan for your chosen small-scale enterprise.	5	02
14	Organize funfair for your class and write report of profit/loss.	5	02
15	Visit report of any industry: Brief history, types and details of services/support assistance being given, any other information which is useful to self-employer/entrepreneur.	5	02

\*-Suggested assignment for the students.

Complete any 8 assignments mentioned below given by subject teacher.

**H. ASSESSMENT CRITERIA:**

## i) Formative Assessment of Practical: -

Every assignment shall be assessed for 25 marks as per following criteria:

Domain	Particulars	Marks out of 25
Cognitive	Understanding	05
	Application	05
Psychomotor	Observation and recording	10
Affective	Discipline and punctuality	05
<b>TOTAL</b>		<b>25</b>

## ii) Assessment of SLA: -

Every Self-learning assignment shall be assessed for 25 marks as per following criteria:

Sr.no	Criteria	Marks allotted
1	Punctuality	05
2	Presentation (neat figures/ drawing etc.)	05
3	Market Survey / Data collection	10
4	Team work	05
	<b>TOTAL</b>	<b>25</b>

**I. INSTRUCTIONAL METHODS:**

- 1 Lectures cum Demonstrations,
- 2 Class room practices.
- 3 Use of projector and soft material for demonstration

**J. TEACHING AND LEARNING RESOURCES:**

Chalk board, Power Point presentations and Demonstrative kits.

**K. REFERENCE BOOKS:**

Sr. No.	Title of Books	Author	Publication
1	The entrepreneurial Instinct: How Everyone Has the Innate Ability to Start a Successful Small Business.	Mehta, Monica	McGraw-Hill Education, New Delhi, 2012, ISBN 978-0-07-179742-9
2	Entrepreneurship	Hisrich R. D.	McGraw-Hill Education, New Delhi, 2013, ISBN-13: 978-1259001635
3	Part I Readings in Entrepreneurship Education	Sareen S.B.	Entrepreneurship Development Institute of India (EDI), GOI, Ahmedabad, 2016; ISBN: 978-0078029169
4	Reading Materials of Entrepreneurship Awareness Camp	Gujral, Raman	Entrepreneurship Development Institute of India (EDI), GOI, Ahmedabad
5	Product Design and manufacturing	Chitale A.K.	PHI Learning, New Delhi, 2014; ISBN: 9788120348738
6	Entrepreneurship Development Small Business Entrepreneurship	Charantimath, Poornima	Pearson Education India, New Delhi; ISBN: 9788131762264

7	Entrepreneurship Development: Special Edition for MSBTE	CPSC, Manila	Tata McGraw Hill, New Delhi
8	Entrepreneurship Development Small Business Management	Khanka S. S.	S. Chand and sons, New Delhi, ISBN: 978-93-5161-094-6
9	Entrepreneurship Development	S. Anil Kumar	New Age International, New Delhi, ISBN: 9788122414349



**COURSE NAME : INTERNSHIP (16 WEEKS)**  
**COURSE CODE : CCH505**  
**COURSE ABBREVIATION : HINT**

### A. LEARNING SCHEME:

Scheme component		Hours	Credits
Actual Contact Hours / week	Classroom Learning	--	10
	Tutorial Learning		
	Laboratory Learning		
	SLH-Self Learning		
	NLH-Notional Learning		

### B. ASSESSMENT SCHEME: -

PAPER DURATION	THEORY				BASED ON LL&TL				BASED ON SLA		TOTAL
					Practical						
NIL	FA- TH	SA- TH	TOTAL		FA -PR		SA-PR		MAX	MIN	200
	MAX	MAX	MAX	MIN	MAX	MIN	MAX	MIN			
	NIL	NIL	NIL	NIL	NIL	100	40	100#	40	-	

Note: Students have to register for Internship with the help of Govt. agency such as BOAT (Board of Apprenticeship Training).

Students have to register Credits for Industrial Training are in-line of guidelines of NCrF : The industrial training is of 16 weeks considering 36-40 hours per week engagement of students (as per Guidelines of GR of Maharashtra Govt.) under Self Learning with guidance of industry supervisor / Mentor.

### C. ABBREVIATIONS: -

CL- Classroom Learning, TL- Tutorial Learning, LL-Laboratory Learning, SLH-Self Learning Hours, NLH- Notional Learning Hours, FA - Formative Assessment, SA -Summative Assessment, IKS - Indian Knowledge System, SLA - Self Learning Assessment

**Legends:** @ Internal Assessment, # External Assessment, \*# Online Examination, @\$ Internal Online Examination.

1. FA-TH represents the average of two class tests of 30 marks each conducted during the semester.
  2. If the candidate is not securing the minimum passing marks in FA-PR of any course, then the candidate shall be declared as "Detained" in that semester.
  3. If the candidate is not securing the minimum passing marks in the SLA of any course, then the candidate shall be declared as a fail and will have to repeat and resubmit the SLA work.
  4. Notional Learning hours for the semester are (CL+LL+TL+SL) hrs.\* 15 Weeks
  5. 1(one) credit is equivalent to 30 Notional hrs.
  6. \* Self learning hours shall not be reflected in the Time Table.
- \* Self learning includes micro project / assignment / other activities

**D. I. RATIONALE: -**

Globalization has prompted organizations to encourage skilled and innovative workforce. Internships are educational and career development opportunities, providing practical/ hands-on experience in a field or discipline. A summer internship is an opportunity for students to get accustomed to modern industry practices, apply the knowledge and skills they've acquired in the classroom to real-world situations, and become familiar with industry environments before they enter the professional world. Keeping this in mind, industrial training is incorporated to all diploma programmes as it enables the student to get equipped with practical skills, soft skills and life skills

**ii. INDUSTRY / EMPLOYER EXPECTED OUTCOME**

The aim of this course is to help the student to attain the following industry identified competency through various teaching learning experiences:

**Apply skills and practices to industrial processes.**

**E. COURSE LEVEL LEARNING OUTCOMES (COS)**

**CCH505.1:** Observe time/resource management and industrial safety aspects.

**CCH505.2:** Acquire professional experience of industry environment.

**CCH505.3:** Prepare report of assigned activities and accomplishments.

**COMPETENCY, COURSE OUTCOMES AND PROGRAMME OUTCOMES (CP-CO-PO) MATRIX**

[ Note: Correlation levels: 1: Slight (Low), 2: Moderate (Medium), 3: Substantial (High), “-”:no correlation]

Competency and COs	Programme Outcomes POs and PSOs								
	PO 1 Basic and discipline specific knowledge	PO 2 Problem analysis	PO 3 design/ development of solutions	PO 4 Engineerin g Tools, experimentation and testing	PO 5 Engineering practice for society, sustainability and environment	PO 6 Project managem ent	PO 7 Life-long learning	PSO1	PSO2
Competency:	2	-	-	-	-	3	-	1	2
CCH505.1	2	-	-	1	-	1	1	1	2
CCH505.2	2	-	1	1	-	2	1	1	2
CCH505.3	1	-	-	2	-	2	-	1	2

**F. GENERAL GUIDELINES FOR ORGANIZING INDUSTRIAL TRAINING**

The industry /organization selected for Industrial training/ internships shall be Government /Public Limited / Private limited / Startup /Centre of Excellence/ Skill Centers/ Skill Parks etc.

**Duration of Training** - 16 weeks students engagement time

**Period of Time slot** – After completion of 4<sup>th</sup> semester examination and up to total sixteen weeks.

**Industry area** - Engineering Programme Allied industries of large, medium or small-scale, Organization/Govt./ Semi Govt Sectors.

**G. ROLE(S) OF DEPARTMENT AT THE INSTITUTE:**

The following activities are expected to be performed by the concerned department at the Institute.  
Table of activities to be completed for Internship

S. N	Activity	Suggested Schedule WEEKS
1	Collection of information about the industry available and ready for extending training with its offered capacity of students (Sample Format 1)	During 4 <sup>th</sup> semester
2	Allocations of Student and Mentor as per availability (Mentor: Student Ratio (1:15))	During 4 <sup>th</sup> semester
3	Communication with Industry and obtaining	14 <sup>th</sup> week of 4 <sup>th</sup> semester
4	Securing consent letter from parents/guardians	15 <sup>th</sup> week of 4 <sup>th</sup> semester
5	Enrollment of Students for industrial training (Format 3)	During 4 <sup>th</sup> semester
6	Issue of letter to industry for training along with details of students and mentor (Format 4)	15 days before end of 4 <sup>th</sup> semester examination
7	Organize Internship Orientation session for students	During 4 <sup>th</sup> semester examination
8	Progressive Assessment of internship by Industry supervisor .	Each week (16 weeks ) -5marks for each week
9	Progressive Assessment of internship by Institute Mentor at the Industry	Each quarter of 1 month(4 weeks) during training period- 5 marks for each quarter
10	Assessment of training by institutional mentor and Industry mentor	5th Semester ESE

**Suggestions-**

- The department can take help of alumni or parents of students having contacts in different industries for securing placement.
- Students would normally be placed as per their choices; in case of more demand for a particular industry, students would be allocated considering their potential. However, preference for placement would be given to students who have arranged placement in a company with the help of their parents or relatives.
- Principal/HOD/Faculty should address students about industrial safety norms, rules, and discipline to be maintained in the industry during training before releasing students for training.
- The faculty members, during the industry visit or sometimes through online mode, will check the progress of the student in the training, student attendance, discipline, and project report preparation each week.

## H. ROLES AND RESPONSIBILITIES OF STUDENTS:

1. Students may interact with the mentor to suggest choices for suitable industry, if any. If students have any contact in industry through their parents or relatives, then the same may be utilized for securing placement for themselves and their peers.
2. Students must fill the forms/formats duly signed by institutional authorities along with a training letter and submit it to a training officer/mentor in the industry on the first day of training.
3. Students must carry with him/her Identity card issued by the institute during the training period.
4. Students should follow industrial dressing protocols, if any. In absence of specific protocol students must wear college uniform compulsorily.
5. Students will have to get all necessary information from the training officer/mentor at industry regarding schedule of training, rules and regulation of the industry and safety norms to be followed. Students are expected to observe these rules, regulations and procedures.
6. Students must be fully aware that if they disobey any rule of industry or do not follow the discipline then non- disciplinary action will be taken.
7. Students must maintain a weekly diary (Format 6) by noting daily activities undertaken and get it duly signed from industry mentor or Industrial training in charge.
8. In case students face any major problems in industry such as an accident or any disciplinary issue then they should immediately report the same to their mentor at the institute.
9. Prepare a final report about the training for submitting to the department at the time of presentation and viva- voce and get it signed from a mentor as well as industry training in charge.
10. Students must submit the undertaking as provided in Format 5.

## I. TYPOGRAPHICAL GUIDELINES FOR INDUSTRY TRAINING REPORT

1. Following is the suggestive format for preparing the training report. Actual report may differ slightly depending upon the nature of industry. The training report may contain the following
2. The training report shall be computer typed (English- British) and printed on A4 size paper.
3. Text Font -Times New Roman (TNR), Size-12 point
4. Subsection heading TNR- 12 point bold normal
5. Section heading TNR- 12 capital bold
6. Chapter Name/ Topic Name – TNR- 14 Capital
7. All text should be justified. (Settings in the Paragraph)
8. The report must be typed on one side only with double space with a margin 3.5 cm on the left, 2.5 cm on the top, and 1.25 cm on the right and at bottom.
9. The training report must be hardbound/ Spiralbound with a cover page in black color. The name of the candidate, diploma (department), year of submission, name of the institute shall be printed on the cover.
10. The training report, the title page should be given first then the Certificate followed by the acknowledgment and then contents with page numbers.

## J. FORMAT OF INDUSTRIAL TRAINING REPORT

Following format may be used for training report. Actual format may differ slightly depending upon the nature of Industry/ Organization.

- Title Page
- Certificate
- Abstract
- Acknowledgement
- Content Page

Chapter 1	Organization structure of Industry and general layout.
Chapter 2	Introduction to Industry / Organization (history, type of products and services, turnover and number of employees etc.)
Chapter 3	Types of Major Equipment/raw materials/instruments/machines/hardware/software used in industry with their specifications, approximate cost, specific use and routine maintenance done
Chapter 4	Processes/ Manufacturing techniques and methodologies and material handling procedures
Chapter 5	Testing of Hardware/Software/ Raw materials/ Major material handling product (lifts, cranes, slings, pulleys, jacks, conveyor belts etc.) and material handling procedures.
Chapter 6	Safety procedures are followed, and safety gear is used by the industry.
Chapter 7	Details of Practical Experiences in Industry/Organization, if any, in Production/Assembly/Testing/Maintenance
Chapter 8	Detailed report of the tasks undertaken (during the training).
Chapter 9	Special/challenging experiences encountered during training if any (may include students' liking & disliking of workplaces).
Chapter 10	Conclusion
Chapter 11	References/sources of information

## **K. LEARNING STRATEGIES DURING TRAINING IN THE INDUSTRY.**

Students should visit the website of the industry where they are undergoing training to collect information about products, processes, capacity, number of employees, turnover etc. They should also refer to the handbook of the major machines and operations, testing, and quality control manuals.

## **L. Table - Distribution of End-Semester-Examination (ESE) marks of Industrial Training**

<b>Internal Examiner</b>			<b>External Examiner</b>	<b>Total ESE marks</b>
<b>Marks for Industrial Training Report</b>	<b>Marks for Seminar/Presentation</b>	<b>Marks for Oral/Viva-voce</b>		
25	25	25	25	100

(To be obtained on Company's Letter Head)

Collecting Information about Industry/Organization available for training along with capacity

- 1) Name of the industry/organization:
- 2) Address/communication details with email:
- 3) Contact person details:
  - a) Name:
  - b) Designation:
  - c) Email
  - d) Contact number/s:
- 4) Type:
  - a) Govt / PSU / Pvt /
  - b) Large scale / Medium scale / Small scale
- 5) Products/Services offered by industry:
- 6) a. Whether willing to offer Industrial training facility during June to August for Diploma in Engineering students: Yes / No.
  - a) If yes, whether you offer 16 weeks training: Yes/No
  - b) Possible Industrial Capacity:

Students	Programme name						Total
	Civil	Mechanical	Electrical	Information Technology	Metallurgy	Electronics & Telecommunication	
Male							
Female							
Total							

- 7) Whether accommodation available for interns Yes / No. If yes capacity: \_\_\_\_\_
- 8) Whether internship is charged or free:
 

If charged please specify amount per candidate: \_

Seal and signature of responsible person at Industry:

**Format-2**

(Obtaining Consent Letter from parents/guardians)

To,  
The principal,  
Government Polytechnic, Kolhapur .

Subject: Consent for Industrial Training.

Respected Sir,

I am fully aware that -

- i. My ward studying in fourth semester at Government Polytechnic, Kolhapur , has to undergo 16 weeks of Industrial training for partial fulfillment towards completion of Diploma in \_\_\_\_\_
- ii. For this fulfillment he/she has been deputed at industry, located at-----  
for Industrial training /internship for the period from \_\_\_\_\_ to \_\_\_\_\_.

With respect to above I give my full consent for my ward to travel to and fro from the mentioned industry.  
Further I undertake that –

- a. My ward will undergo the training at his/her own cost and risk during training and/or stay.
- b. My ward will be entirely under the discipline of the organization where he/she will be placed and will abide by the rules and regulations in face of the said organization.
- c. My ward is NOT entitled to any leave during the training period.
- d. My ward will regularly submit a prescribed weekly diary, duly filled and countersigned by the training supervisor of the organization to the mentor faculty of the polytechnic.

I have explained the contents of the letter to my ward, who has also promised to adhere strictly to the requirements. I assure that my ward will be properly instructed to take his own care to avoid any accidents/injuries in the industry. In case of any accident neither industry nor the institute will be held responsible.

Signature :

Name : \_\_\_\_\_

Address : \_\_\_\_\_

Phone Number : \_\_\_\_\_



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(Issue Letter to the Industry/Organization for the training along with details of students and mentors on I  
nstitute Letter Head)

To,  
The HR Manager,  
\_\_\_\_\_  
\_\_\_\_\_

**Subject:** Placement for Industrial training of 16 weeks in your organization

**Reference:** Your consent letter no \_\_\_\_\_.

Sir,  
With reference to the above we are honored to place the following students from this institute  
for Industrial training in your esteemed organization as per the arrangement arrived .

The purpose of this training is to equip the student with some essential skills relevant to the demands of  
the industry and world of work, as well as to provide exposure to the professional environment and work  
culture. It is hoped that this training may enhance his/her employability and livelihood opportunities. In  
view of the above, we kindly request your support in facilitating this Industrial Training for the student.  
He/she has been adequately oriented and guided on the expectations of this training, including the  
maintenance of a daily diary during the training period.

Additionally, the institute has secured the necessary consent and undertaking from the parent/guardian  
regarding the guidelines for exit training. In view of all the above industry shall refrain from involving  
students into the mundane and housekeeping activities. Your cooperation in this regard will be highly  
appreciated.

Diploma programme in \_\_\_\_\_

Sr. No.	Roll No.	Name of Student	Name and designation of Mentor
1.			
2.			

Kindly extend all possible cooperation to the students for above. Thanking you

Yours Sincerely,

Principal  
Government Polytechnic, Kolhapur

CC- Mentor.

(Undertaking by the students)

To

The Principal,

Government Polytechnic, Kolhapur

Subject: Undertaking regarding Placement for Industrial training of 16 weeks duration.

I Mr./Ms. \_\_\_\_\_ Roll No: \_\_\_\_\_

Son/Daughter of \_\_\_\_\_ studying in  
\_\_\_\_\_ department at your Institute, am fully aware of the  
Industrial Training requirement and related responsibilities and participation in the  
\_\_\_\_\_ Industrial training from: \_\_\_\_\_ To \_\_\_\_\_.

I assure you that I will be of good behavior and be obedient to the staff and mentor during the Industrial training. I will also abide and will not participate in all activity. I will also discipline myself within the rules and regulations of the Institution. I am also aware that I am participating in the Industrial Training at my own risk and I will not hold the Institute responsible in any way in any eventuality namely Accident

/Injury/death or whatever mishap and I myself will be solely responsible for my safety.

Place: Kolhapur

Date:

Signature of the student

## GOVERNMENT POLYTECHNIC, KOLHAPUR

### Evaluation Sheet for Formative Assessment of Internship

Academic Year:

Department: Electrical Engineering

Semester: V

Course – Internship

Course Code - CCH505

Name of Coordinator:

Enrollment No	Name of Student															Marks (5 marks each week) by Mentor & Industry Supervisor jointly							PA Marks by Mentor faculty (20 Marks) [B]	Total [Marks 100] [A]+[B]
		WK 1	WK 2	WK 3	WK 4	WK 5	WK 6	WKN 7	WK 8	WK 9	WK 10	WK 11	WK 12	WK 13	WK 14	WK 15	WK 16	Total [80 Marks] [A]						

**Name and Designation of Mentor**

**Name and Designation of Industry Supervisor**

The faculty should visit the Industry at least for four times Online /Off line mode and He will give Marks for 4 Times Quarterwise. [Mentor will give marks 4 times ( 5 marks each time ) and total of 20 marks]

EVALUATION BY MENTOR

Sr No.	Roll No	Quarter 1 Marks Out Of 5 By mentor	Quarter 2 Marks Out Of 5 By mentor	Quarter 3 Marks Out Of 5 By mentor	Quarter 4 Marks Out Of 5 By mentor	Average of column

## DAILY DIARY FORMAT TO BE MAINTAINED BY STUDENT

Week 1: From \_\_\_\_\_ To \_\_\_\_\_

Day	Activities carried out	Remark
1		
2		
3		
4		
5		
6		

Signature of Mentor : \_\_\_\_\_

Signature of Industrial Supervisor : \_\_\_\_\_

**The same format need to follow for every week of the industrial training.**

**COURSE ID:**

**COURSE NAME : DATA COMMUNICATION & NETWORKING**  
**COURSE CODE : ETH404**  
**COURSE ABBREVIATION : HDCN**

**P. LEARNING SCHEME:**

Scheme component		Hours	Credits
Actual Contact Hours / week	Classroom Learning	07	02
	Tutorial Learning	00	
	Laboratory Learning	04	
	SLH-Self Learning	00	
	NLH-Notional Learning	11	

**Q. ASSESSMENT SCHEME:-**

PAPER DURA TION IN HRS	THEORY				BASED ON LL&TL				Total
					Practical				
	FA-TH	SA-TH	TOTAL		FA -PR		SA-PR		
	MAX	MAX	MAX	MIN	MAX	MIN	MAX	MIN	
03	30	70	100	40	25	10	25#	20	150

**C: ABBREVIATIONS:** - CL- Class Room Learning, TL- Tutorial Learning, LL-Laboratory Learning, SLH-Self Learning Hours, NLH-Notional Learning Hours, FA - Formative Assessment, SA - Summative assessment, IKS - Indian Knowledge System, SLA - Self Learning Assessment

**Legends:** @ Internal Assessment, # External Assessment, \*# On Line Examination, @\$ Internal Online Examination.

1. FA-TH represents average of two class tests of 30 marks each conducted during the semester.
2. If candidate is not securing minimum passing marks in FA-PR of any course then the candidate shall be declared as "Detained" in that semester.
3. If candidate is not securing minimum passing marks in SLA of any course then the candidate shall be declared as fail and will have to repeat and resubmit SLA work.
4. Notional Learning hours for the semester are (CL+LL+TL+SL) hrs.\* 06 Weeks
5. 1(one) credit is equivalent to 30 Notional hrs.
6. \* Self learning hours shall not be reflected in the Time Table.

\* Self learning includes micro project / assignment / other activities. (Provide list of all assignments here in tabular format At least 6 to 8 assignments to be given)

**D. i) RATIONALE: -**

Communication plays a vital role in various fields like business, academics, defense, budget research, engineering, medicine. In the present Industrial & commercial environment, the technician is expected to use digital communication skillfully. The primary purpose of this course is to give an elementary but sound fundamental understanding of how data communication work, its basic components, how they work and basic knowledge of applications of Internet. After understanding basic communication system it is worth to discuss Data Communication & Networking. Telecommunication & Data communication

is the fastest growing technology & undoubtedly has strong growth in future hence students should know data transfer from one system to another through different communication networks like LAN, WAN, MAN & different switching techniques.

## ii) INDUSTRY / EMPLOYER EXPECTED OUTCOME

The aim of this course is to help the student to attain the following industry identified outcome through various learning experiences.

- Implement different protocols used for data communication using various switching techniques and network topologies.
- Understand network topologies, and protocols used in various switching approaches and wireless technologies.
- Implement star and tree topologies using computers and simple protocols in C for wide range of applications.

## G. COURSE LEVEL LEARNING OUTCOMES (COs)

ETH404-1 Describe the concepts of data communication.

ETH404-2 Identify different network models and illustrate its functioning based on network devices

ETH404-3 Recognize different switching techniques and explore their applications.

ETH404-4 Detect and correct digital transmission errors and Show Protocols for Noiseless and Noisy Channels.

ETH404-5 Apply knowledge to understand different IEEE Standards.

ETH404-6 Summarize application layer protocols and describe its formats.

### Course outcomes and Programme outcomes/ Programme specific outcomes (CO- PO/PSO) matrix

[Note: Correlation levels: 1: Slight (Low), 2: Moderate (Medium), 3: Substantial (High), “-“ : No correlation]

Cos	Programme Outcomes POs and PSOs								
	PO 1 Basic and Discipline specific knowledge	PO 2 Problem Analysis	PO 3 Design / Development of solutions	PO 4 Engineering Tools, Experimenta tion and Testing	PO 5 Engineering Practices for society, sustainability and Environment	PO 6 Project Managemen t	PO 7 Life- long Learning	PSO1 Operate and Maintain	PSO2 Supervision and Providing Solution
ETH404-1	3	-	-	-	-	-	2	-	2
ETH404-2	3	2	-	-	2	2	2	-	2
ETH404-3	3	-	-	-	-	-	2	-	1
ETH404-4	3	-	-	2	-	-	-	2	3
ETH404-5	2	2	1	2	2	2	2	-	2
ETH404-6	3	3	2	2	3	2	3	2	3

## H. CONTENT:

### II) Practical exercises

The following practical exercises shall be conducted in the Laboratory for this course in practical sessions of batches of about 20- 24 students.

**Note-** Minimum 10-12 Practicals are required to complete which shall address all Cos.

Sr. No.	Laboratory experiences	CO
28.	Perform Survey of any existing data communication system. <ul style="list-style-type: none"> <li>• Identify data communication system components.</li> <li>• Explain Working and use of the data communication system.</li> <li>• List transmission impairments.</li> </ul>	ETH 404-1



29.	Implement different network topologies using CISCO packet tracer. • Define the term, topology. • Compare different types of Network i.e. LAN, MAN, WAN.	ETH 404-1
30.	Compare different transmission (guided) media • Locate different transmission media and identify differences between them.	ETH 404-2
31.	Identify layout of lab network • Draw a line diagram of the topology used for computer lab networking.	ETH 404- 1,2
32.	Router, Repeater and bridge • Install and test Router / Repeater / bridge (any one).	ETH 404-2
33.	Prepare a network cable using CAT5/6 cable and RJ45 connector and test its connectivity – straight connection / cross connection.	ETH404-2
34.	Interconnect two PCs to share file and list various settings used.	ETH 404- 2,3
35.	Implement Peer to Peer Network by installing NIC Driver and assign IP address, name to node and share resources like Drives, Folder etc.	ETH 404-2,3
36.	Using HUB/Switches, install a LAN network consisting of six computers.	ETH 404-2,3
37.	Perform Flow Control and Error Control protocol (any one) using CISCO packet tracer.	ETH 404- 4
38.	Configure static and dynamic IP addresses.	ETH 404-2, 6
39.	Create connection to available wireless network.	ETH 404-4,5
40.	Use any Packet sniffing tool for network traffic management.	ETH 404-4,5,6
41.	Print documents from remote system in a network , Share folder and printer in a network (using wired and wireless system).	ETH 404-2,5
42.	Troubleshooting using TCP/IP • Understand and implement various TCP/IP utilities. • Execute TCP/IP network commands: ipconfig, ping, tracert.	ETH 404-2,4,6
43.	Configure/Test internet connectivity • Execute TCP/IP network commands: netstat, pathping, route.	ETH 404-2,4,6
44.	Implement IP addresses for intranet in Class A, Class B, Class C. • Implement Class-full Address in a given network node. • Identify range of IP Address in various classes. • Justify the reason to choose various IP address classes for creating given network.	ETH 404-6

### III) Theory

#### Section I

Sr. no.	Topics/Subtopics	Learning (Hours)	Classroom learning evaluation Marks
	<b>ETH404-1</b> Describe the concepts of data communication.		

<b>1</b>	<b>Introduction of Data Communication</b> 1.1. Data Communication – components, data representation. 1.2. Protocols, Standards, Standards Organizations. 1.3. Transmission impairment – Attenuation, Distortion, Noise. 1.4. Network definition & criteria ( Performance, reliability, security) 1.5. Network topology: Mesh, ring, bus, star, hybrid 1.6. Types Of Networks :LAN,WAN and MAN. 1.7. Performance of network – Bandwidth, Throughput, Latency (Delay), Transmission time, Bandwidth delay product, Jitter	<b>7</b>	<b>12</b>
	<b>ETH404-2</b> Identify different network models and illustrate its functioning based on network devices		
<b>2</b>	<b>Network models and Network Devices</b> 2. Network models – 2.1. OSI(Organization and brief Functions Of each layer) 2.2. TCP/IP(Organization and Brief Functions Of each layer) 2.3. Levels ( Types ) of addressing – Physical, Logical (IP), Port, and Specific addresses. 2.4. Transmission (Guided) media and types – performance, advantages and applications 2.5. Network Devices and Network Connectors, Hubs, Repeater, Bridges, Switches, Routers, Gateway.	<b>8</b>	<b>14</b>
	<b>ETH404-3</b> Recognize different switching techniques and explore their applications.		
<b>3</b>	<b>Switching Techniques</b> 3.1 Connection oriented and Connectionless services 3.2 Switching Basics – 3.3 Circuit Switching; 3.4 Packet Switching – 3.5 Datagram approach, Virtual circuit approach. 3.6 Message Switching. 3.7 Frame Relay – Introduction ,The need of Frame Relay 3.8 Frame Relay works, Frame Relay frame format.	<b>6</b>	<b>8</b>
	<b>Sub-total</b>	<b>21</b>	<b>34</b>

### Section –II

Sr. no.	Topics/Subtopics	Learning (Hours)	Classroom learning evaluation Marks
	ETH404-4 : Detect and correct digital transmission errors and Show Protocols for Noiseless and Noisy Channel.		
<b>4</b>	<b>Error detection &amp; correction protocol</b> 4.1 Data Link Control & Protocol – Framing, Flow & Error Control 4.2 Noiseless and noisy channels 4.3 Simple, Stop-N- Wait Protocol. 4.4 Stop-N-Wait ARQ Protocol. 4.5 Go Back N ARQ. 4.6 Selective Repeat ARQ.	<b>06</b>	<b>10</b>
	ETH404-5: Apply knowledge to understand different IEEE Standards		
<b>5</b>	<b>Wireless LAN and Network security</b> 5.1 Introduction to wired LAN - IEEE Standards, Standard Ethernet, Fast Ethernet, Gigabit Ethernet (Features) 5.2 Wireless LAN systems - IEEE 802.11 - Architecture- BSS, ESS. 5.3 Physical layer: FHSS, DSSS, OFDM. 5.4 MAC layer – DCF, PCF 5.5 Cryptography: Need, Encryption model	<b>06</b>	<b>10</b>
	ETH 404-6: Summarize application layer protocols and describe its formats.		
<b>6</b>	<b>Applications Layer (organization, Brief description and applications)</b> 6.1 Domain Name System (DNS) Label, Domain name, Domain, Zone. Root Server, Primary Server, Secondary Server, Zone transfer. 6.2 Simple Mail Transfer Protocol (SMTP), Multipurpose Internet Mail Extension (MIME), POP, IMAP 6.3 World wide web(www) 6.4 Uniform Resource Locator (URL) 6.5 Hyper Text Transfer Protocol(HTTP); 6.6 IP version 4	<b>09</b>	<b>16</b>
	<b>Sub-total</b>	<b>21</b>	<b>36</b>

**G : List of Assignments under SLA (Optional)**

Sr. No.	List of Assignment (under SLA)	Hours allotted
26.	Tabulate important characteristics of Data communication Network	02
27.	Tabulate important characteristics of commonly available Network.	02
28.	Tabulate important characteristics of commonly available Topologies.	02
29.	Tabulate important characteristics of commonly available Transmission medium.	02
30.	Write a procedure to test Network connectivity.	02
31.	Draw a chart showing Network Model.	02
32.	Study of different types addressing.	02
33.	Study of different types Network Devices.	02
34.	Study of different types Switching.	02
35.	Write procedure to transfer file between two PC.	02
36.	Select specific error correction & detection protocols.	02
37.	Study different types of wireless LAN systems.	02
38.	Draw a cryptography diagram understand working.	02
39.	Study working of DNS,SMTP, MIME,POP,IMAP,HTTP,FTP.	02
40.	Create different types of IP addresses and URL.	02

**H : Specification table for setting question paper for semester end theory assessment**

Section / Topic no.	Name of topic	Distribution of marks (level wise)			Total marks	CO
		Remember	Understand	Apply		
I / 1	Concept of Data Communication	4	4	4	12	ETH 404-1
I / 2	Introduction To Networking	4	6	4	14	ETH 404-2
I / 3	Switching Techniques	2	4	2	8	ETH 404-3
II / 4	Error detection & correction	2	6	2	10	ETH 404-4
II / 5	Wireless LAN and Network security	2	4	4	10	ETH 404-5
II / 6	Applications Layer	4	8	4	16	ETH 404-6
Total Marks		18	32	20	70	

## **I :-Assessment Criteria**

### **i) Formative Assessment of Practical: -**

Every assignment shall be assessed for 25 marks as per following criteria:

<b>Domain</b>	<b>Particulars</b>	<b>Marks out of 25</b>
Cognitive	Understanding	05
	Application	05
Psychomotor	Operating Skills	05
	Drawing / drafting skills	05
Affective	Discipline and punctuality	05
<b>TOTAL</b>		<b>25</b>

### **ii) Summative Assessment of Practical:**

Every practical assignment shall be assessed for 25 marks as per following criteria:

<b>Sr. no</b>	<b>Criteria</b>	<b>Marks allotted</b>
1	Attendance at regular practical	05
2	Preparedness for practical	05
3	Neat & complete Diagram.	05
4	Observations & handling of instrument.	05
5	Oral Based on Lab work and completion of task	05
<b>TOTAL</b>		<b>25</b>

## **J) Instructional Methods:**

6. Lectures cum Demonstrations
7. Class room practices
8. Use of projector and soft material for demonstration
4. Virtual Laboratory

## **K) Teaching and Learning resources:**

Chalk board, LCD presentations, Demonstrative kits, Demonstrative charts

## **LI) Reference Books:**

<b>Sr. No</b>	<b>Name of Book</b>	<b>Author</b>	<b>Publication</b>
1.	Data Communication & Networking	Achyut S. Godbole.	Tata McGraw-Hill Edition
2.	Data Communication & Networking	B.A. Forouzan.	Tata McGraw-Hill Edition(4th Edition)
3.	Data & Network Communication	Michal Miller.	Thomson Delmar Learning

## **M) Learning Website & Software**

- 1) [www.nptel.ac.in](http://www.nptel.ac.in)
- 2) [www.tutorialspoint.com/data\\_communication\\_computer\\_network](http://www.tutorialspoint.com/data_communication_computer_network).
- 3) [freevideolectures.com](http://freevideolectures.com) › Networking › IIT Kharagpur.
- 4) [www.concise-courses.com/security/wireshark-basics](http://www.concise-courses.com/security/wireshark-basics).

\* \* \*

**COURSE ID:**

**COURSE NAME : EMBEDDED SYSTEM**

**COURSE CODE : ETH405**

**COURSE ABBREVIATION : HEMS**

**A. LEARNING SCHEME:**

Scheme component		Hours	Credits
Actual Contact Hours / week	Classroom Learning	07	02
	Tutorial Learning	00	
	Laboratory Learning	04	
	SLH-Self Learning	00	
	NLH- Notional Learning	11	

**B. ASSESSMENT SCHEME:-**

PAPER DURAT ION IN HRS	THEORY				BASED ON LL&TL				BASED ON SLA		Total
					Practical						
03	FA- TH	SA-TH	TOTAL		FA -PR		SA-PR		MAX	MIN	
	MAX	MAX	MAX	MIN	MAX	MIN	MAX	MIN			
	30	70	100	40	25	10	25#	10	--	--	150

**C. ABBREVIATIONS: -**

CL- Class Room Learning, TL- Tutorial Learning, LL-Laboratory Learning, SLH-Self Learning Hours, NLH- Notional Learning Hours, FA - Formative Assessment, SA -Summative assessment, IKS - Indian Knowledge System, SLA - Self Learning Assessment

**Legends:**

@ Internal Assessment, # External Assessment, \*# On Line Examination,@\$ Internal Online Examination.

1. FA-TH represents average of two class tests of 30 marks each conducted during the semester.
2. If candidate is not securing minimum passing marks in FA-PR of any course then the candidate shall be declared as "Detained" in that semester.
3. If candidate is not securing minimum passing marks in SLA of any course then the candidate shall be declared as fail and will have to repeat and resubmit SLA work.
4. Notional Learning hours for the semester are (CL+LL+TL+SL) hrs.\* 15 Weeks
5. 1 (one) credit is equivalent to 30 Notional hrs.

**\* Self learning hours shall not be reflected in the Time Table.**

\* Self learning includes micro project / assignment / other activities. (Provide list of all assignments here in tabular format At least 6 to 8 assignments to be given)

#### D. i) RATIONALE: -

Embedded system is a new trend in the field of automation. The subject has been introduced in order to enhance the knowledge of microcontroller programming. This subject will help a student to design small embedded systems and write the code for the same.

#### ii) INDUSTRY / EMPLOYER EXPECTED OUTCOME

The aim of this course is to attend following industry identified competency through various teaching learning experiences: “Develop simple embedded system applications.”

### M.COURSE LEVEL LEARNING OUTCOMES (COs)

**ETH405-1** Classify, Define and State the functions of embedded system

**ETH405-2** Choose appropriate family of microcontroller for different applications

**ETH405-3** Apply Embedded C programming

**ETH405-4** Interpret the communication standards of embedded systems

**ETH405-5** Develop the basic embedded system applications using Arduino board

**ETH405-6** Analyze the features of Real Time Operating System

#### Course outcomes and programme outcomes/ programme specific outcomes

##### (CO- PO/PSO) matrix:

[Note: Correlation levels: 1: Slight (Low), 2: Moderate (Medium), 3: Substantial (High), “-“ : No correlation]

Cos	Programme Outcomes POs and PSOs								
	PO 1 Basic and Discipline specific knowledge	PO 2 Problem Analysis	PO 3 Design / Developmen t of solutions	PO 4 Engineering Tools, Experimenta tion and Testing	PO 5 Engineering Practices for society, sustainability and Environment	PO 6 Project Manageme nt	PO 7 Life- long Learning	PSO1 Operate and Maintain	PSO2 Supervisio n and Providing Solution
<b>ETH405-1</b>	2	2	2	1	-	-	2	2	2
<b>ETH405-2</b>	2	3	2	2	-	-	2	2	2
<b>ETH405-3</b>	2	2	3	3	-	-	2	2	2
<b>ETH405-4</b>	2	2	2	2	-	-	2	3	3
<b>ETH405-5</b>	2	1	2	2	-	-	2	3	3
<b>ETH405-6</b>	3	3	2	3	-	-	2	3	3



## **N. CONTENT:**

### **II) Practical exercises**

The following practical exercises shall be conducted in the *Laboratory for Microcontrollers* developed by the Institute in practical sessions of batches of about 20- 22 students:

<b>Sr. No.</b>	<b>Laboratory experiences</b>	<b>CO</b>
17.	Identification of pins of PIC18F452, ATmega328 Microcontrollers	ETH405-2
18.	Identify the different peripherals and its interfacing with PIC and AVR microcontrollers know how on the Development boards available in the laboratory	ETH405-2
19.	* Use an Arduino IDE for ATmega 328 Programming	ETH405-2
20.	* Use Keil Compiler's integrated Development Environment (IDE) for developing Embedded C programs for 8051 Microcontrollers	ETH405-3
21.	* Development and execution of the program in embedded C for sending data on port lines. 1) Sending ASCII characters/numbers to a particular port 2) LED Blinking continuously 3) LED state control by push button switch 4) Get status of a pin and output it on another pin	ETH405-3
22.	Development and execution of the program in embedded C for arithmetic operation and time delay 1) LED Blinking for 500 times 2) Toggle all the bits of a port with a 250 msec delay in between 3) Sounding a buzzer connected to a port pin upon door sensor sense the open condition	ETH405-3
23.	Development and execution of the program in C to display numbers from 0 to 9 on 7 segment display interfaced with 8051 with a delay in between	ETH405-3
24.	Study of communication Protocols its types: Serial: I2C, CAN, USB, serial peripheral interface (SPI).	ETH405-4
25.	* Study of Wireless protocol : IrDA, Bluetooth, Zigbee, WiFi, LoWPAN	ETH405-4
26.	Serial Communication using RS 485C	ETH405-4
27.		
28.	* Development and execution of the program for switch and LED interface with Arduino UNO	ETH405-5
29.	* Development and execution of the program LCD Interfacing to Arduino UNO board	ETH405-5
30.	Development and execution of the program for speed control of DC motor interfaced with Arduino UNO using PWM	ETH405-5
31.	* Develop an embedded system for home automation with the use of at least 2 sensors and 2 actuators.	All COs
32.	Develop an embedded system for industrial control application with the use of at least 2 sensors and 2 actuators.	All Cos

*Note- \* is compulsory practical.*

**III) Theory****Section I**

<b>Sr. no.</b>	<b>Topics/Subtopics</b>	<b>Learning (Hours)</b>	<b>Classroom learning evaluation Marks</b>
<b><i>ETH405-1 Classify, Define and State the functions of Embedded system</i></b>			
<b>1</b>	<b>Introduction to Embedded System</b> 1.1. Embedded system 1.1.1. Block diagram of Embedded system 1.1.2. Embedded system functions and characteristics 1.1.3. Classification of Embedded systems 1.1.3.1. Small scale, medium scale, large scale 1.1.3.2. Sophisticated, standalone 1.1.3.3. Reactive/Real time (Soft and Hard) 1.1.4. Steps in design and development of embedded system	<b>07</b>	<b>10</b>
<b><i>ETH405-2 Choose appropriate family of microcontroller for different applications</i></b>			
<b>2</b>	<b>Microcontroller architectures</b> 2.1. Microcontroller types, architectures and features of different families with applications 2.1.1. 8051 Microcontroller family 2.1.2. PIC Microcontroller family 2.1.3. AVR Microcontroller family 2.1.4. ARM Microcontroller family 2.2. Study of a specific microcontroller from above families: AT89S52, PIC18F452, ATmega328 etc.	<b>07</b>	<b>12</b>
<b><i>ETH405-3 Apply Embedded C programming</i></b>			
<b>3</b>	<b>Programming 8051 in Embedded C</b> 3.1. Overview of 8051 architecture 3.2. Embedded C syntax, data types, and time delay 3.3. I/O programming 3.4. Logic operations and conditional control 3.5. Data serialization	<b>07</b>	<b>12</b>
	<b>Sub-total</b>	<b>21</b>	<b>34</b>

## Section –II

Sr • N o.	Topics/Subtopics	Learning (Hours)	Classroom learning evaluation Marks
<b><i>ETH405-4 Interpret the communication standards of embedded systems</i></b>			
<b>4</b>	<b>Communication Standards and Protocols</b> 4.1 Modes of communication: serial, parallel, synchronous and asynchronous 4.2 Serial Communication standards: RS 485, MAX 232 bidirectional level converter 4.3 Communication Protocols its types: Serial: I2C, CAN, USB, serial peripheral interface (SPI). 4.4 Wireless protocol : IrDA, Bluetooth, Zigbee, WiFi, LoWPAN (Overview only )	<b>06</b>	<b>10</b>
<b><i>ETH405-5 Develop the basic embedded system applications using Arduino board</i></b>			
<b>5</b>	<b>Interfacing &amp; programming Input and Output devices with ARDUINO</b> 5.1 Introduction to Arduino- UNO, NANO, MEGA Boards 5.2 Functional Block diagram of Arduino UNO Board, and Pin functions 5.3 Introduction to Arduino IDE 5.3.1 Functions used in Arduino IDE: math, analog I/O, digital I/O, Timer 5.4 Peripheral interfacing and programming with Arduino UNO: 5.4.1 LED, Switch, Relay 5.4.2 LCD, Keyboard 5.4.3 Stepper motor, DC Motor , DHT11 and Ultrasonic sensors	<b>08</b>	<b>14</b>
<b><i>ETH405-6 Analyze the features of Real Time Operating System</i></b>			
<b>6</b>	<b>Introduction to Real Time Operating System (RTOS)</b> 6.1 Operating System: General and Real time 6.2 Characteristics of Real Time Operating System: Consistency, Reliability, Scalability, Performance, Predictability 6.3 Functions of RTOS: Task Management, Scheduling, Resource allocation 6.4 Features of RTOS: Watchdog Timer, semaphore 6.5 Deadlock: Reason of occurrence, Deadlock detection prevention and ignoring	<b>07</b>	<b>12</b>
	<b>Sub-total</b>	<b>21</b>	<b>36</b>

**O. Specification table for setting question paper for semester end theory assessment**

Section / Topic no.	Name of topic	Distribution of marks (level wise)			Total marks	CO
		Remember	Understand	Apply		
I / 1	Introduction to Embedded System	04	02	04	10	ETH405-1
I / 2	Microcontroller architectures	02	04	06	12	ETH405-2
I / 3	Programming 8051 in Embedded C	02	06	04	12	ETH405-3
II / 4	Communication Standards and Protocols	02	04	04	10	ETH405-4
II / 5	Interfacing & programming Input and Output devices with ARDUINO	02	04	08	14	ETH405-5
II / 6	Introduction to Real Time Operating System (RTOS)	02	06	04	12	ETH405-6
Total Marks		14	26	30	70	

**P. Assessment Criteria****i) Formative Assessment of Practical: -**

Every assignment shall be assessed for 25 marks as per following criteria:

Domain	Particulars	Marks out of 25
Cognitive	Understanding	05
	Application	05
Psychomotor	Operating Skills	05
	Drawing / drafting skills	05
Affective	Discipline and punctuality	05
<b>TOTAL</b>		<b>25</b>

**ii) Summative Assessment of Practical:**

Every practical assignment shall be assessed for 25 marks as per following criteria:

Sr. No.	Criteria	Marks allotted
1	Attendance at regular practical	05
2	Preparedness for practical	05
3	Neat & complete Diagram.	05
4	Observations & handling of instrument.	05
5	Oral Based on Lab work and completion of task	05
<b>TOTAL</b>		<b>25</b>

**Q. Instructional Methods:**

27. Lectures cum Demonstrations
28. Class room practices
29. Use of projector and soft material for demonstration
4. Virtual Laboratory

**R. Teaching and Learning resources:**

Chalk board, LCD presentations, Demonstrative kits, Demonstrative charts

### S. Reference Books:

Sr. No.	Author	Name of Book	Publication
1	Kenneth, Ayala	8051 Microcontroller Architecture Programming and Application	PHI Learning
2	Deshmukh, Ajay	Microcontroller Theory and Application,	McGraw Hill.
3	Kamal, Raj,	Microcontroller Architecture Programming, Interfacing and System Design	Pearson Education India,
4	Dawoud Shenouda Dawoud	Peter Dawoud Serial Communication Protocols and Standards	River Publishers, Denmark, 2020 ISBN: 978-8770221542
5	J.M.Hughes	Arduino: A Technical Reference	O'REILL ISBN: 978-1491921760

### T. Learning Website & Software

Sr No	Link/Portal	Description
1	<a href="https://www.microchip.com/en-us/toolsresources/develop/microchip-studio">https://www.microchip.com/en-us/toolsresources/develop/microchip-studio</a>	Microchip Studio for AVR® and SAM Devices is an integrated development platform from Microchip
2	<a href="http://arduino.cc/">http://arduino.cc/</a>	Link for Software Download and installation Arduino Related Hardware and
3	<a href="https://learn.sparkfun.com/tutorials/w-hat-is-an-arduino">https://learn.sparkfun.com/tutorials/w-hat-is-an-arduino</a>	Arduino Basics
4	<a href="https://onlinecourses.swayam2.ac.in/aic20_sp04/preview">https://onlinecourses.swayam2.ac.in/aic20_sp04/preview</a>	Introduction and Concepts of Arduino
5	<a href="https://support.arduino.cc/">https://support.arduino.cc/</a>	T technical documentation utorials, data sheets, guides and other
6	<a href="https://semiconductors.es/datasheetpdf/219613/ATMEGA32.html">https://semiconductors.es/datasheetpdf/219613/ATMEGA32.html</a>	Datasheet for ATmega Microcontrollers
7	<a href="https://www.alldatasheet.com/datasheetpdf/pdf/82338/MICROCHIP/PIC16F877A.html">https://www.alldatasheet.com/datasheetpdf/pdf/82338/MICROCHIP/PIC16F877A.html</a>	Datasheet for PIC Microcontroller

\*\*\*\*\*

**COURSE ID:**

**COURSE NAME** : **INDUSTRIAL ORGANISATION AND**  
**MANAGEMENT COURSE CODE** : **CCH502**  
**COURSE ABBREVIATION** : **HIOM**

**A. LEARNING SCHEME:**

Scheme component		Hours	Credits
Actual Contact Hours / week	Classroom Learning	05	2
	Tutorial Learning	--	
	Laboratory Learning	--	
	SLH-Self Learning	01	
	NLH-Notional Learning	06	

**B. ASSESSMENT SCHEME**

PAPER DURATION IN HRS	THEORY				BASED ON LL&TL				BASED ON SLA		TOTAL
					Practical						
1	FA-TH	SA-TH	TOTAL		FA -PR		SA-PR		MAX	MIN	100
	MAX	MAX	MAX	MIN	MAX	MIN	MAX	MIN			
	15*#	35*#	50@\$	20	--	--	--	--	50	20	

**C. ABBREVIATIONS: -**

CL-Class Room Learning, TL- Tutorial Learning, LL-Laboratory Learning, SLH-Self Learning Hours, NLH-Notional Learning Hours, FA - Formative Assessment, SA - Summative assessment, IKS - Indian Knowledge System, SLA - Self Learning Assessment **Legends:** @ Internal Assessment, # External Assessment, \*# OnLine Examination,

@\$ Internal Online Examination.

FA-TH represents an average of two class tests of 30 marks each conducted during the semester.

- If a candidate is not securing minimum passing marks in FA-PR of any course then the candidate shall be declared as "Detained" in that semester.
- If a candidate is not securing minimum passing marks in SLA of any course then the candidate shall be declared as fail and will have to repeat and resubmit SLA work.
- Notional Learning hours be reflected in the Time Table.

\* Self-learning includes micro project/assignment / other activities.

## D. i.RATIONALE:

Management ability is a higher-grade ability, which every successful engineer must possess. This science has been developed in those days when it was treated as an art in earlier stages. It is impossible for an individual though technically sound to achieve goals of the organizations. Effective implementation of management policies is a tough task. The Diploma holder should learn these principles of management and various techniques.

## ii.INDUSTRY / EMPLOYER EXPECTED OUTCOME

The aim of this course is to help the student to attain the following industry identified competency through various teaching learning experiences:

**To improve management ability of individual through teaching.**

## E. COURSE OUTCOMES (COs):

**CCH502.1** Apply principles of management and carry out various functions of management

**CCH502.2** Prepare organization structure for small and medium scale industry.

**CCH502.3** Perform duties of stores in-charge and materials manager.

**CCH502.4** Practice industrial safety rules, codes, practices and acts.

**CCH502.5** Apply various modern management techniques

Competency, course outcomes and programme outcomes/programme specific outcomes (cp-co-po/ps) matrix

Competency and Cos	Programme Outcomes POs and PSOs								
	PO 1 Basic and Discipline specific knowledge	PO2 Problem Analysis	PO3 Design / Development of solutions	PO4 Engineering Tools, Experimentation and Testing	PO 5 Engineering Practices for society, sustainability and Environment	PO6 Project Management	PO7 Life-long Learning		
Competency: The aim of this course is to improve management ability of individual through teaching.	3	3	2	2	3	3	3	3	3
CCH502-1	3	2	2	2	2	3	3	2	3
CCH502-2	3	3	2	3	2	3	3	3	2
CCH502-3	3	3	3	3	2	3	3	2	2
CCH502-4	2	3	3	3	2	3	3	3	3
CCH502-5	2	3	3	3	2	3	3	3	3

Note : Correlation levels : 1: Slight (Low), 2: Moderate (Medium), 3: Substantial (High), “0” ]

## F. CONTENT:

### I) PRACTICAL EXERCISES - Not Applicable

#### ii) THEORY

SECTION-I			
Sr. No.	Topics	Teaching (Hours)	Theory evaluation
<b>Course Outcome CCH502-1: - Apply principles of management and carry out various functions of management</b>			
<b>1</b>	<b>PRINCIPLES OF MANAGEMENT</b> 1.1 Concept of management 1.2 Principles of management 1.3 Objectives of management 1.4 Scope and importance of management 1.5 Levels of management 1.6 Managerial competencies: Communication, Planning and Administration, Team work, Strategic action and General awareness	<b>04</b>	<b>4</b>
<b>Course Outcome CCH-502-1: - Apply principles of management and carry out various functions of management</b>			
<b>2</b>	<b>Functions of Management</b> 2.1 Planning: Forms of planning, Strategic levels and Planning, Phases of Planning 2.2 Decision Making: Decision making conditions, Basic types of Decisions 2.3 Organizing: Introduction to Organization design, basic types of Departmentalization, Co-ordination, Authority 2.4 Motivation: Work Motivation, Three approaches to Motivation, 2.5 Leadership: Leadership and Power, Leadership Development 2.6 Communication: The Communication process, Impact of Information Technology, Hurdles to effective communication 2.7 Controlling: Foundations of control, creative Effective control, Primary methods	<b>06</b>	<b>5</b>
<b>Course Outcome CCH502-1: - Apply principles of management and carry out various functions of management</b>			
<b>3</b>	<b>HUMAN RESOURCE MANAGEMENT.</b> 3.1 Definition and concept, 3.2 Aim, Objectives and functions of HR dept. 3.2 Principles of personnel policy, details recorded in policy 3.3 Recruitment and selection of employees 3.4 Training: Objectives, benefits, types and methods 3.5 Workers Participation in Management	<b>06</b>	<b>5</b>



<b>Course Outcome CCH502-2: Prepare organization structure for small and medium scale industry.</b>			
<b>4</b>	<b>FORMS OF BUSINESS ORGANISATION</b> 4.1 Types of industrial sectors 4.2 Forms of business organization 4.3 Individual Proprietorship 4.4 Partnership 4.5 Joint stock companies 4.6 Co-operatives, Public sectors, Government undertakings.	<b>05</b>	<b>5</b>
<b>SECTION II</b>			
<b>Course Outcome CCH502.3: Perform duties of stores in-charge, material and finance manager.</b>			
<b>5</b>	<b>MATERIALS MANAGEMENT</b> 5.1 Importance of purchase 5.2 Functions and Objectives 5.3 Duties of purchasing officer 5.4 Methods of purchasing and procedure 5.5 Scope and importance of material management 5.6 Objectives of material management 5.7 Duties of Material manager 5.8 Concept of supply chain management 5.9 Modern trends in material management: MRP, ERP	<b>05</b>	<b>4</b>
<b>6</b>	<b>FINANCIAL MANAGEMENT</b> 6.1 Concept, Scope and Importance 6.2 Functions of financial management 6.3 Types of capital: Fixed, working 6.4 Factors affecting Working capital 6.5 Capitalization: over, under 6.6 Sources of Finance 6.7 Industrial taxation	<b>05</b>	<b>4</b>
<b>CCH502.4: Practice industrial safety rules, codes, practices and acts.</b>			
<b>7</b>	<b>INDUSTRIAL ACT &amp; SAFETY</b> 7.1 Factory Act, Electricity Act, Workmen Compensation Act, ESI Act, pollution Control Act 7.2 Accidents: Economic aspects, direct and indirect cost of accidents Causes, Types, Remedies, Personal Protective Equipment's (PPE), Reporting & Investigation of accidents 7.3 Safety management: safety in industry, committees, programs, Safety codes, Safety training, 7.4 Occupational Safety and Health Administration – Promoting, norms and standards Housekeeping: definition, concept, necessity, advantages, procedure	<b>05</b>	<b>4</b>
<b>CCH502.5: Apply various modern management techniques.</b>			
<b>8</b>	<b>MODERN MANAGEMENT TECHNIQUES</b> 8.1 PERT & CPM 8.2 Various terms related with network analysis 8.3 Various Time estimates 8.4 Construction of Network Diagram Computation of Critical Path	<b>04</b>	<b>4</b>
	<b>Total</b>	<b>40</b>	<b>35</b>

**Online Exam for this course.**

**G. SPECIFICATION TABLE FOR SETTING QUESTION PAPER FOR SEMESTER END THEORY EXAMINATION:**

Topic No.	Name of topic	Distribution of marks (Cognitive level- wise)			Course Outcome	Total Marks
		Remember	Understand	Apply		
1	Principles Of Management	02	02	--	CCH502.1	04
2	Functions Of Management	02	02	01	CCH502.1	05
3	Human Resource management	02	02	01	CCH502.1	05
4	Forms Of Business organization	02	02	01	CCH502.2	05
5	Materials Management	02	01	01	CCH502.3	04
6	Financial Management	02	01	01	CCH502.3	04
7	Industrial Act & Safety	02	01	01	CCH502.4	04
8	Modern Management Techniques	02	01	01	CCH502.5	04
<b>TOTAL</b>						<b>35</b>

**H. INSTRUCTIONAL METHODS**

- Lectures cum Demonstrations
- Classroom practices

**I. TEACHING AND LEARNING RESOURCES:**

Chalk board, LCD presentations, Audio presentations, Question Bank.

**J. REFERENCE BOOKS:**

Sr. No.	Author	Title	Publisher
1	Bangaand Sharma	Industrial Organisation & Management	Khanna Publisher
2	O P Khanna	Industrial Engg. & Management	DhanpatRai & sons New Delhi
3	P.C. Pandey & C. K. Sing	Management Science	DhanpatRai & sons New Delhi
4	Industrial Organisation	P.T. Ghan	Tata McGraw Hill
5	Management Information System	Waman S. Jawadekar	Tata McGraw Hill
6	P.C. Pandey & C. K. Sing	Management Science	DhanpatRai & sons New Delhi

**K. LEARNING WEBSITE & SOFTWARE: -**

- <https://nptel.ac.in/courses/122/106/122106031/>
- <https://nptel.ac.in/courses/110/105/110105154/>
- <https://nptel.ac.in/courses/110/101/110101150/>
- <https://nptel.ac.in/courses/110/101/110101153/>

**COURSEID:**

**COURSE NAME : MARKETING MANAGEMENT**

**COURSE CODE : CCH503**

**COURSE ABBREVIATION : HMKM**

**A. LEARNING SCHEME:**

Scheme component		Hours	Credits
Actual Contact Hours / week	Classroom Learning	05	2
	Tutorial Learning	--	
	Laboratory Learning	--	
	SLH-Self Learning	01	
	NLH-Notional Learning	06	

**B. ASSESSMENT SCHEME**

PAPER DURATION IN HRS	THEORY				BASED ON LL&TL				BASED ON SLA		TOTAL
					Practical						
1	FA-TH	SA-TH	TOTAL		FA -PR		SA-PR		MAX	MIN	100
	MAX	MAX	MAX	MIN	MAX	MIN	MAX	MIN			
	15*#	35*#	50@\$	20	--	--	--	--	50	20	

**A. ABBREVIATIONS: -**

CL-Class Room Learning, TL- Tutorial Learning, LL-Laboratory Learning, SLH-Self Learning Hours, NLH-Notional Learning Hours, FA - Formative Assessment, SA - Summative assessment, IKS - Indian Knowledge System, SLA - Self Learning Assessment

**Legends:** @ Internal Assessment, # External Assessment, \*# OnLine Examination,

@\$ Internal Online Examination.

FA-TH represents an average of two class tests of 30 marks each conducted during the semester.

- If a candidate is not securing minimum passing marks in FA-PR of any course then the candidate shall be declared as "Detained" in that semester.
- If a candidate is not securing minimum passing marks in SLA of any course then the candidate shall be declared as fail and will have to repeat and resubmit SLA work.
- Notional Learning hours be reflected in the Time Table.

\* Self-learning includes micro project/assignment / other activities.

**B. i. RATIONALE:**

The globalization has lead to the requirement of multi-skilled personnel to execute the works and that too in a cost effective way. It is observed that the diploma holder in Engineering has wide spectrum for development, if the enters the marketing field. Provided he has the interest with proper initiative. Marketing now a day can provide fruitful employment to the diploma holder to make a career in marketing.

**ii. INDUSTRY / EMPLOYER EXPECTED OUTCOME**

The aim of this course is to help the student to attain the following industry identified competency through various teaching learning experiences:

**Work as a marketing personnel for electrical components or business.**

**C. COURSE OUTCOMES (COs):**

CCH503-1:-Interpret different marketing techniques. CCH503-2:-Identify different types of markets.

CCH503-3:-Carry out different marketing works/functions. CCH503-4:-Assist in marketing work of industrial products. CCH503-5:-Carry out market studies for products.

Competency, course outcomes and programme outcomes/programme specific outcomes (cp-co-po/ps) matrix

Note : Correlation levels : 1: Slight (Low), 2: Moderate (Medium), 3: Substantial (High), “0” ]

Competency and Cos	Programme Outcomes POs and PSOs								
	PO 1 Basic and Discipline specific knowledge	PO 2 Problem Analysis	PO 3 Design / Development of solutions	PO 4 Engineering Tools, Experimentation and Testing	PO 5 EngineerIng Practices for society, sustainability and Environment	PO 6 Project Managem ent	PO 7 Life-long Learning	PSO1	PSO2
Competency: Work as a marketing personnel for electrical components or business.	3	3	2	2	3	3	3	3	3
CCH503-1:-Interpret different marketing techniques.	3	2	2	2	2	3	3	2	3
CCH503-2:-Identify different types of markets.	3	3	2	3	2	3	3	3	2
CCH503-3:-Carry out different marketing works/functions.	3	3	3	3	2	3	3	2	2
CCH503-4:-Assist in marketing work of industrial products.	2	3	3	3	2	3	3	3	3
CCH503-5:-Carry out market studies for products.	2	3	3	3	2	3	3	3	3

**D. CONTENT:****I) PRACTICAL EXERCISES - Not Applicable****II) THEORY****Section I**

Sr.No.	TOPICS Sub-Topics	Periods	Marks
CCH503-1: Interpret different marketing techniques.			
1	<b>Marketing</b> 1.1 Meaning and significance of marketing 1.2 Marketing system 1.3 Concept of marketing 1.4 Product selling 1.5 Trends in modern marketing	8	6
CCH503-2: Identify different types of markets.8			
2	<b>Markets</b> 2.1 Meaning of market 2.2 Types of market 2.3 Government and industrial market	8	6
CCH503-3: Carry out different marketing works/functions.			
3	<b>Functions &amp; Management</b> 3.1 Marketing functions 3.2 Meaning of marketing 3.3 Management and functioning 3.4 Types of marketing organizations 3.5 Marketing manager and his duties	8	6

**Section II**

Sr.No.	TOPICS Sub-Topics	Periods	Marks
CCH503-4: Prepare scheme for marketing industrial products.			
4	<b>Marketing Industrial Products</b> 4.1 Types of industrial products 4.2 Characteristics and marketing considerations 4.3 Marketing planning 4.4 Product decision 4.5 Pricing decision	8	6
CCH503-5: Carry out market studies for products.			
5	<b>Strategies</b> 5.1 Marketing strategy 5.2 Marketing mix 5.3 Market survey 5.4 Market information systems 5.5 Buying behaviors	8	6
CCH503-6: Devise proper advertising techniques for industrial products.			
6	<b>Advertising</b> 6.1 Role of advertising in marketing 6.2 Marketing management without advertising 6.3 Brief introduction to cost component of advertising	8	5
	<b>Total</b>	40	35

## E. SPECIFICATION TABLE FOR SETTING QUESTION PAPER FOR SEMESTER END THEORY EXAMINATION:

Topic No.	Name of topic	Distribution of marks (Cognitive level- wise)			Total Marks
		Remember	Understand	Apply	
1	Marketing	2	2	2	6
2	Markets	2	2	2	6
3	Marketing Functions & Management	2	2	2	6
4	Marketing Industrial Products	2	2	2	6
5	Strategies	2	2	2	6
6	Advertising	2	2	1	5
<b>TOTAL</b>					<b>35</b>

## F. INSTRUCTIONAL METHODS

- c. Lectures cum Demonstrations
- d. Classroom practices

## G. TEACHING AND LEARNING RESOURCES:

Chalk board, LCD presentations, Audio presentations, Question Bank

## H. REFERENCE BOOKS:

- 1) Basic Marketing - by Cundiff & Still.
- 2) Marketing Management - by R. S. Davar.
- 3) Salesmanship, Sales Management & Advertising - by Satyanarayana.
- 4) Modern Marketing Management - by R. S. Davar.
- 5) Marketing Management (cases & Concepts) - by Dholakia, Khurana & Jain

### Websites :

- i <https://nptel.ac.in/courses/122/106/122106031/>
- ii <https://nptel.ac.in/courses/110/105/110105154/>
- iii <https://nptel.ac.in/courses/110/101/110101150/>
- iv <https://nptel.ac.in/courses/110/101/110101153/>

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**COURSE NAME : PROJECT MANAGEMENT**  
**COURSE CODE : CCH504**  
**COURSE ABBREVIATION : HPRM**

### A. LEARNING SCHEME:

Scheme component		Hours	Credits
Actual Contact Hours / week	Classroom Learning	05	2
	Tutorial Learning	--	
	Laboratory Learning	--	
	SLH-Self Learning	01	
	NLH-Notional Learning	06	

### B. ASSESSMENT SCHEME

PAPER DURATION IN HRS	THEORY				BASED ON LL&TL				BASED ON SLA		TOTAL
					Practical						
1	FA-TH	SA-TH	TOTAL		FA -PR		SA-PR		MAX	MIN	100
	MAX	MAX	MAX	MIN	MAX	MIN	MAX	MIN			
	15*#	35*#	50@\$	20	--	--	--	--	50	20	

### A. ABBREVIATIONS: -

CL-Class Room Learning, TL- Tutorial Learning, LL-Laboratory Learning, SLH-Self Learning Hours, NLH-Notional Learning Hours, FA - Formative Assessment, SA - Summative assessment, IKS - Indian Knowledge System, SLA - Self Learning Assessment

**Legends:** @ Internal Assessment, # External Assessment, \*# OnLine Examination,

@\$ Internal Online Examination.

FA-TH represents an average of two class tests of 30 marks each conducted during the semester.

- If a candidate is not securing minimum passing marks in FA-PR of any course then the candidate shall be declared as "Detained" in that semester.
- If a candidate is not securing minimum passing marks in SLA of any course then the candidate shall be declared as fail and will have to repeat and resubmit SLA work.
- Notional Learning hours be reflected in the Time Table.

\* Self-learning includes micro project/assignment / other activities.

**B. RATIONALE:**

A diploma holder in Electrical Engineering now a day finds himself entrusted with managing projects or part there off. The age at which he/she is entrusted such responsibilities is declining day by day as a result of which he is stressed much and expected to perform well. This course intends to introduce the elementary aspects of Scientific Project Management whose emphasis is on success, though everybody has his own style of managing projects successfully.

**ii. EXPECTED OUTCOME:**

Use the principles of project management as a supervisor in relevant works.

**C. COURSE OUTCOMES (COs):**

CCH504-1: Identify project types and the tools/techniques used in it.

CCH504-2: Identify actions for project manager using computer based project management systems as well. CCH504-3: Assist in establishing a project.

CCH504-4: Carry out project control activities.

CCH504-5: Implement projects using different techniques.

Competency, course outcomes and programme outcomes/programme specific outcomes (cp-co-po/pso) matrix

Competency and Cos	Programme Outcomes POs and PSOs								
	PO 1 Basic and Discipline specific knowledge	PO 2 Problem Analysis	PO 3 Design / Development of solutions	PO 4 Engineering Tools, Experimentation and Testing	PO 5 EngineerIng Practices for society, sustainability and Environment	PO 6 Project Managem ent	PO 7 Life-long Learning	PSO1	PSO2
Competency: Use the principles of project management as a supervisor in relevant works.	3	3	2	2	3	3	3	3	3
CCH504-1	3	2	2	2	2	3	3	2	3
CCH504-2	3	3	2	3	2	3	3	3	2
CCH504-3	3	3	3	3	2	3	3	2	2
CCH504-4	2	3	3	3	2	3	3	3	3
CCH504-5	2	3	3	3	2	3	3	3	3

Note : Correlation levels : 1: Slight (Low), 2: Moderate (Medium), 3: Substantial (High), “0” ]



## D. CONTENT:

### I) PRACTICAL EXERCISES - Not Applicable

### II) THEORY

#### Section I

Sr. no.	Topics / subtopics	Teaching (Hours)	Theory Marks
CCH504-1:Identify the tools and techniques used in project management.			
1	<b>Project Management and its tools/techniques</b> 1.1 Idea of project 1.2 Characteristics of project 1.3 Categories of projects 1.4 Project life cycle phases (definition of each phase) 1.5 Project life cycle curve. Tools And Techniques For Project Management 1.6 Selection techniques 1.7 Execution planning and techniques 1.8 Scheduling and co-ordination techniques 1.9 Cost and productivity control techniques. 1.10 Communication and clean up techniques	8	7
CCH504-2:Identify actions for project manager using computer based project management systems as well.			
2	<b>Project Management System</b> 2.1 Block diagram with brief description , Computer based project management 2.2 Rationale behind computerized project management systems <b>Project Management</b> 2.3 Need of project manager 2.4 Problems of project manager 2.5 Basic education for project manager 2.6 Roles and responsibilities of project management as a profession	8	7
	Total		

### Section II

Sr. no.	Topics Subtopics	Teaching (Hours)	Theory Marks
CCH504-3: Work in a group to establish a project.			
3	<b>Establishing Project ( Scope &amp; Cost )</b> 3.1 Guidelines given by planning commission ( brief description about guidelines ) 3.2 Financing arrangements, capital cost, sources of financing 3.3 Internal and external sources 3.4 Terms of finance Short, intermediate and long term 3.5 Names of financial institutions with their mode of working in brief 3.6 Preparation of cost estimates types of estimates with very brief description: Order of magnitude, study estimate, preliminary estimate, Definitive estimate, detailed estimate.	8	7
CCH504-4: Carry out project control activities.			
4	<b>Project Activities And Profitability</b> 4.1 Project implementation schedule 4.2 Basis of time estimation 4.2.1 Time study 4.2.2 Previous project data 4.2.3 Estimates from the vendors/ contractors 4.2.4 Allocated time and committed time. Overall implementation schedule Bar chart Evaluation of project profitability methods 4.2.5 Pay-back period ( PBP ) return on investments ( ROI ) concept of zero date 4.3 Pre-project activities and advance actions ( very brief description )	8	7
CCH504-5: Implement project task using different techniques.			
5	<b>Implementation Control &amp; Monitoring</b> <b>Implementation</b> 5.1 Work breakdown structure 5.2 Project execution plan ( brief description ) Project procedure manual 5.3 Project control system 5.4 Need for flexibility 5.5 Project diary <b>Control &amp; Monitoring</b> 5.6 Direction reviews meeting creativity techniques such as questioning 5.7 Attribute listing 5.8 Brain Communications in a project feed back and forward 5.9 Unscheduled meetings and social get-togethers	8	7
	Total	40	35

### E. SPECIFICATION TABLE FOR SETTING QUESTION PAPER FOR SEMESTER END THEORY EXAMINATION:

Topic No.	Name of topic	Distribution of marks (Cognitive level- wise)			Total Marks
		Remember	Understand	Apply	
1	Project Management and its tools/techniques	3	2	2	7
2	Project Management System	3	2	2	7
3	Establishing Project	3	2	2	7
4	Project Activities and Profitability	3	2	2	7
5	Implementation Control & Monitoring	3	2	2	7
<b>TOTAL</b>					<b>35</b>

### F. INSTRUCTIONAL METHODS

- a. Lectures cum Demonstrations
- b. Classroom practices

### G. TEACHING AND LEARNING RESOURCES:

Chalk board, LCD presentations, Audio presentations, Question Bank

### H. REFERENCE BOOKS:

1. Project Management - by S. Choudhury (Tata McGraw Hill Pub.)
2. Project Management - A System Approach to planning, Scheduling & Controlling By Harold Kerzner (CBS Publishers & Distributors)

### I. Websites :

- i <https://nptel.ac.in/courses/122/106/122106031/>
- ii <https://nptel.ac.in/courses/110/105/110105154/>
- iii <https://nptel.ac.in/courses/110/101/110101150/>
- iv <https://nptel.ac.in/courses/110/101/110101153/>

**COURSE ID:**

**COURSE NAME : INTERNET OF THINGS**

**COURSE CODE : ETH501**

**COURSE ABBREVIATION : HIOT**

**A. LEARNING SCHEME:**

Scheme component		Hours	Credits
Actual Contact Hours / week	Classroom Learning	03	01
	Tutorial Learning	00	
	Laboratory Learning	04	
	SLH-Self Learning	00	
	NLH- Notional Learning	07	

**B. ASSESSMENT SCHEME: -**

PAPER DURAT ION IN HRS	THEORY				BASED ON LL&TL				BASED ON SLA		Total
					Practical						
--	FA- TH	SA-TH	TOTAL		FA -PR		SA-PR		MAX	MIN	
	MAX	MAX	MAX	MIN	MAX	MIN	MAX	MIN			
	--	--	--	--	--	50	20	50@	20	--	--

**C. ABBREVIATIONS: -** CL- Class Room Learning, TL- Tutorial Learning, LL- Laboratory Learning, SLH-Self Learning Hours, NLH- Notional Learning Hours, FA - Formative Assessment, SA -Summative assessment, IKS - Indian Knowledge System, SLA - Self Learning Assessment

**Legends:** @ Internal Assessment, # External Assessment, \*# On Line Examination, @\$ Internal Online Examination.

1. FA-TH represents average of two class tests of 30 marks each conducted during the semester.
2. If candidate is not securing minimum passing marks in FA-PR of any course then the candidate shall be declared as "Detained" in that semester.
3. If candidate is not securing minimum passing marks in SLA of any course, then the candidate shall be declared as fail and will have to repeat and resubmit SLA work.
4. Notional Learning hours for the semester are (CL+LL+TL+SL) hrs.\* 15 Weeks

5. 1(one) credit is equivalent to 30 Notional hrs.

**6. \* Self learning hours shall not be reflected in the Time Table.**

#### **D. i) RATIONALE: -**

This course equips students with essential IoT knowledge, covering architecture, protocols, security, and real-world applications. They will learn to design smart solutions that enhance efficiency and automation across industries like healthcare, agriculture, and smart cities. The focus of IoT empowers students to drive innovation, optimize resources, and contribute to a smarter, more connected society.

#### **ii) INDUSTRY / EMPLOYER EXPECTED OUTCOME**

The aim of this course is to attend following industry identified competency through various teaching learning experiences:

“Maintain system based on Internet of Things (IoT).”

### **E. COURSE LEVEL LEARNING OUTCOMES (COs)**

**ETH501-1** Describe the fundamental architecture of Internet of Things (IoT).

**ETH502-2** Apply programming principles to develop IoT applications with NodeMCU.

**ETH503-3** Integrate sensors and actuators in IoT based system.

**ETH504-4** Implement IoT communication for data handling.

**ETH505-5** Describe IoT based applications in given specific field.

**ETH505-6** Apply the fundamentals of 3D printing technology and its applications.

#### **Course outcomes and programme outcomes/ programme specific outcomes (CO- PO/PSO) matrix:**

[Note: Correlation levels: 1: Slight (Low), 2: Moderate (Medium), 3: Substantial (High), “- “: No correlation]

Cos	Programme Outcomes POs and PSOs								
	PO 1 Basic and Discipline specific knowledge	PO 2 Problem Analysis	PO 3 Design / Developmen t of solutions	PO 4 Engineering Tools, Experimenta tion and Testing	PO 5 Engineering Practices for society, sustainability, and Environment	PO 6 Project Manageme nt	PO 7 Life- long Learning	PSO1 Operate and maintain	PSO2 Supervision and Providing Solution
<b>ETH501-1</b>	2	2	1	-	-	-	2	2	2
<b>ETH502-2</b>	3	3	3	3	1	1	2	2	2
<b>ETH503-3</b>	3	3	3	2	2	2	3	2	2
<b>ETH504-4</b>	2	2	1	2	1	2	2	3	3
<b>ETH505-5</b>	3	3	3	2	2	2	3	3	3
<b>ETH505-6</b>	3	3	3	3	2	2	3	3	3

## F. CONTENT:

### I) Practical exercises

The following practical exercises shall be conducted in the *Laboratory for Internet of Things* developed by the Institute in practical sessions of batches of about 20- 22 students:

(Any 10)

Sr. No.	Laboratory experiences	CO
1	NodeMCU-ESP8266 Hardware Overview and computer interface	ETH501-1
2	Configuration of Arduino IDE for NodeMCU	ETH501-2
3	Implement LED blinking using NodeMCU	ETH501-2
4	Control LED using push button	ETH501-2
5	Measure Light Intensity Using LDR Sensor	ETH501-2
6	Interfacing Humidity sensor with NodeMCU	ETH501-3
7	Interfacing PIR Sensor with NodeMCU	ETH501-3
8	Analyze Object Detection Using IR Sensor	ETH501-3
9	Interface the ultrasonic sensor with NodeMCU and measure the distance from object	ETH501-3
10	Implement Wi-Fi Connectivity in NodeMCU	ETH501-4
11	Implement HTTP web server on NodeMCU to display sensor data.	ETH501-4
12	Monitoring and controlling light intensity using NodeMCU and cloud platform	ETH501-4
13	Design a Smart Street Light System Using LDR & Relay	ETH501-5
14	Implementation of IoT enabled Smart Home applications	ETH501-5
15	Build an IoT-Based Weather Station	ETH501-5
16	Implement a smart energy monitoring solution (PF, Voltage, Current, Energy consumption etc.)	ETH501-5
17	Create an IoT-Based Security System with Motion Detection & Alarm	ETH501-5
18	Create an IoT-Based smart irrigation system	ETH501-5
19	Calibration of 3D printer-bed level, temperature calibration, filament flow rate calibration	ETH501-6
20	Printing and assembling multiple parts to create functional objects	ETH501-6

## II) Theory

### Section I

Sr. no.	Topics/Subtopics	Learning (Hours)	Classroom learning evaluation Marks
<b><i>ETH501-1 Describe the fundamental architecture of Internet of Things (IoT).</i></b>			
<b>1</b>	<b>Overview of IoT</b> 1.1 Basics of IoT: need, history, definition, characteristics, architecture of IoT with block diagram, IoT applications 1.2 IoT Levels & Deployment Templates 1.3 IoT Applications (agriculture, smart home, smart city)	<b>3</b>	
<b><i>ETH501-2 Apply programming principles to develop IoT applications with NodeMCU.</i></b>			
<b>2</b>	<b>IoT Hardware Platform</b> 2.1 Overview of NodeMCU ESP8266: Key features and specifications, Hardware architecture and GPIO pins. 2.2 Programming with Arduino IDE: Setting up Arduino IDE for NodeMCU, Creating, compiling, and uploading programs. 2.3 Applications using NodeMCU ESP8266 and Arduino IDE. (Use of functions, string, array, timer, I/O function, PWM, interface LED & switch)	<b>3</b>	
<b><i>ETH501-3 Integrate sensors and actuators in IoT based system.</i></b>			
<b>3</b>	<b>IoT Sensors and Actuators</b> 3.1 Sensors: LDR, PIR, DHT11, IR, Gas sensor-MQ, soil moisture sensor, ultrasound. 3.2 Actuators: relay, DC motor, stepper motor, buzzer. 3.3 Programming and Interfacing sensors and actuators with NodeMCU: Gas sensor -MQ 4, Humidity sensor- DHT11, Photo sensors- LDR, PIR, IR with NodeMCU.	<b>3</b>	
	<b>Sub-total</b>	<b>9</b>	

## Section –II

Sr. No	Topics/Subtopics	Learning (Hours)	Classroom learning evaluation Marks
<b>ETH501-4 Implement IoT communication for data handling.</b>			
<b>4</b>	<b>IoT Communication Protocol and cloud integration</b> 4.1 Overview of IoT Communication Protocols: HTTP-REST, MQTT, CoAP (features, methods, communication, applications) 4.2 IEEE802.11: Wi-Fi (features, applications), configure Wi-Fi on NodeMCU, Wi-Fi libraries, code for connecting to Wi-Fi networks 4.3 Procedure to create HTTP webserver with NodeMCU 4.4 Introduction to IoT cloud platforms: AWS IoT, ThingSpeak, Google Cloud IoT, Microsoft Azure IoT. (Use any one for demo) 4.5 IoT networking technology: LoRa, NB-IoT, (Features and applications)	<b>4</b>	
<b>ETH501-5 Describe IoT based applications in given specific field.</b>			
<b>5</b>	<b>IoT Applications</b> 5.1 Agriculture: Greenhouse control using IoT, Weather forecasting 5.2 Smart City: Street light control system, Traffic control System, Waste management 5.3 IoT based smart energy meter 5.4 Home automation: controlling lights, Fans, and smart lock	<b>3</b>	
<b>ETH501-6 Apply the fundamentals of 3D printing technology and its applications.</b>			
<b>6</b>	<b>3D Printing for IoT</b> 6.1 Basic principle of 3D printing, steps in 3D printing process 6.2 Common basic slicer settings (layer height, fill density, supports, platform adhesion-skirt, brim, raft, shell thickness) 6.3 Post processing techniques, need of post processing, steps in post processing 6.4 IoT Enclosures for Prototyping	<b>2</b>	
	<b>Sub-total</b>	<b>9</b>	



## G. Assessment Criteria

### i) Formative Assessment of Practical: -

Every assignment shall be assessed for 50 marks as per following criteria:

Domain	Particulars	Marks out of 25
Cognitive	Understanding	10
	Application	10
Psychomotor	Operating Skills	10
	Drawing / drafting skills	10
Affective	Discipline and punctuality	10
TOTAL		50

### ii) Summative Assessment of Practical:

Every practical assignment shall be assessed for 25 marks as per following criteria:

Sr. no	Criteria	Marks allotted
1	Attendance at regular practical	10
2	Preparedness for practical	10
3	Neat & complete Diagram.	10
4	Observations & handling of instrument.	10
5	Oral Based on Lab work and completion of task	10
TOTAL		50

## H. Instructional Methods:

1. Lectures cum Demonstrations
2. Class room practices
3. Use of projector and soft material for demonstration
4. Virtual Laboratory

## I. Teaching and Learning resources:

Chalk board, LCD presentations, Demonstrative kits, Demonstrative c

### J. Reference Books:

Sr. No.	Author	Name of Book	Publication
1	Arshdeep Bahga, Vijay Madisetti	Internet of Things: A Hands-On Approach	University Press, ISBN: 9788173719547
2	Raj Kamal	INTERNET OF THINGS Architecture and Design Principles	McGraw Hill Education (India) Private Limited, ISBN: 9789390727384
3	Adrin McEwen & Hakim Cassimality	Designing the Internet of things	Wiley India, ISBN: 9781118430620
4	Sudip Misra, Anandarup Mukherjee, Arijit Roy	Introduction to IoT	Cambridge University Press, ISBN: 9781108842952
5	David Hanes, Gonzalo Salgueiro, Patrick Grossetete, Rob Barton, Jerome Henry	IoT Fundamentals: Networking Technologies, Protocols, and Use Cases for the Internet of Things	Cisco Press, ISBN: 9781587144561
6	Richard Blum	Sams Teach Yourself Arduino™ Programming in 24 Hours	Pearson Education, Inc. ISBN: 9780672337123
7	Rahul Dubey	An Introduction to Internet of Things: Connecting Devices, Edge Gateway, and Cloud with Applications	Cengage India Private Limited, ISBN: 9789353500931931

### K. Learning Website & Software

Sr No	Link/Portal	Description
1	<a href="https://infyspringboard.onwingspan.com/web/en/app/toc/lex_auth_0130009449730539521875_shared/overview">https://infyspringboard.onwingspan.com/web/en/app/toc/lex_auth_0130009449730539521875_shared/overview</a>	IoT Platform
2	<a href="https://infyspringboard.onwingspan.com/web/en/app/toc/lex_auth_01329474210427699229893_shared/overview">https://infyspringboard.onwingspan.com/web/en/app/toc/lex_auth_01329474210427699229893_shared/overview</a>	"Mastering IoT with Arduino". Infosys Spring board online course for ThinkSpeak platform.
3	<a href="https://www.arduino.cc/en/software">https://www.arduino.cc/en/software</a>	Arduino IDE software
4	<a href="https://www.tinkercad.com/projects?subject=arduino&amp;sort=views">https://www.tinkercad.com/projects?subject=arduino&amp;sort=views</a>	Arduino projects on Tinkercad
5	Introduction to Internet of Things - Course (nptel.ac.in)	Complete coverage of IoT
6	<a href="https://infyspringboard.onwingspan.com/web/en/app/toc/lex_auth_01384301295320268828657_shared/overview">https://infyspringboard.onwingspan.com/web/en/app/toc/lex_auth_01384301295320268828657_shared/overview</a>	IoT Automation with ESP8266 with Projects

**COURSE ID :**

**COURSE NAME : PROJECT**  
**COURSE CODE : ETH503**  
**COURSE ABBREVIATION : HPRJ**

## R. LEARNING SCHEME:

Scheme component		Hours	Credits
Actual Contact Hours / week	Classroom Learning	00	02
	Tutorial Learning	00	
	Laboratory Learning	04	
	SLH-Self Learning	02	
	NLH-Notional Learning	06	

## S. ASSESSMENT SCHEME:-

PAPER DURA TION IN HRS	THEORY				BASED ON LL&TL				BASED ON SLA		Total
					Practical						
---	FA-TH	SA-TH	TOTAL		FA -PR		SA-PR		MAX	MIN	
	MAX	MAX	MAX	MIN	MAX	MIN	MAX	MIN			
	---	---	---	---	----	----	----	50#	20	50	20
											100

**C: ABBREVIATIONS:** - CL- Class Room Learning, TL- Tutorial Learning, LL-Laboratory Learning, SLH-Self Learning Hours, NLH-Notional Learning Hours, FA - Formative Assessment, SA - Summative assessment, IKS - Indian Knowledge System, SLA - Self Learning Assessment

**Legends:** @ Internal Assessment, # External Assessment, \*# On Line Examination, @\$ Internal Online Examination.

1. FA-TH represents average of two class tests of 30 marks each conducted during the semester.
2. If candidate is not securing minimum passing marks in FA-PR of any course then the candidate shall be declared as "Detained" in that semester.
3. If candidate is not securing minimum passing marks in SLA of any course then the candidate shall be declared as fail and will have to repeat and resubmit SLA work.
4. Notional Learning hours for the semester are (CL+LL+TL+SL) hrs.\* 15 Weeks
5. 1(one) credit is equivalent to 30 Notional hrs.
6. \* Self learning hours shall not be reflected in the Time Table.

\* Self learning includes micro project / assignment / other activities. (Provide list of all assignments here in tabular format At least 6 to 8 assignments to be given)

#### **D. i) RATIONALE: -**

The inclusion of project in the curriculum provides students with a well-rounded education, blending theoretical knowledge with practical implementation. It equips them with technical expertise, problem-solving abilities, and industry-relevant skills, making them competent professionals in the field of Electronics and Telecommunication Engineering.

Students learn various theoretical concepts related to **Electronics, Communication systems, Microcontrollers, Signal processing, Networking, and Embedded systems**. A project allows them to apply these concepts in real-world applications, strengthening their understanding. Students gain hands-on experience with **hardware components, circuit design, programming, and troubleshooting techniques**. This enhances their technical competence and makes them industry-ready. It also encourages students to think critically, analyze problems, and develop innovative solutions.

#### **ii) INDUSTRY / EMPLOYER EXPECTED OUTCOME**

The set of **technical, analytical, problem solving, managerial, and interpersonal skills** that students develop while working on projects. These competencies prepare students for real-world applications, industry roles, entrepreneur and higher studies and prepare them to excel in their career in the field of engineering and technology.

### **I. COURSE LEVEL LEARNING OUTCOMES (COs)**

ETH501-1 Identify the problem through literature survey.

ETH501-2 Select, collect and use required information/knowledge to solve the problem.

ETH501-3 Design a system to solve the identified problem.

ETH501-4 Troubleshoot the hardware and software of designed system.

ETH501-5 Communicate effectively and confidently as a member and leader of team.

ETH501-6 Prepare and present project proposals/project/seminar report.

**Course outcomes and programme outcomes/ programme specific outcomes  
(CO- PO/PSO) matrix**

[Note: Correlation levels: 1: Slight (Low), 2: Moderate (Medium), 3: Substantial (High), “-“ : No correlation]

COs	Programme Outcomes POs and PSOs								
	PO 1 Basic and Discipline specific knowledge	PO 2 Problem Analysis	PO 3 Design / Development of solutions	PO 4 Engineering Tools, Experimenta tion and Testing	PO 5 Engineering Practices for society, sustainability and Environment	PO 6 Project Managemen t	PO 7 Life- long Learnin g	PSO1 Operate and Maintain	PSO2 Supervision and Providing Solution
ETH501-1	3	3		-	2	1	3	-	2
ETH501-2	3	3	3	3	-	1	3	1	2
ETH501-3	3	3	3	3	3	3	3	3	3
ETH501-4	3	-	3	3	-	2	3	3	3
ETH501-5	3	-	-	2	3	3	-	2	2
ETH501-6	3	-	-	-	3	3	3	-	3

## J. CONTENT:

Project activity is performed in groups. A group can be of minimum 2 and maximum of three students. Group formation of students and allotment of guide should be done at the beginning of the semester by Project Coordinator.

In order to inculcate following skills into students, faculty should guide them to select proper projects with respect to sub points given under each skill.

### 1. Technical Proficiency

- Application of core electronics principles in designing, analyzing, and testing circuits.
- Hands-on experience with **microcontrollers, sensors, actuators, and embedded systems**.
- Proficiency in **circuit simulation tools** like Proteus, Multisim, and MATLAB.
- Understanding and implementation of **PCB design, soldering, and prototyping** techniques.

### 2. Practical Problem-Solving Skills

- Ability to identify, analyze, and troubleshoot **hardware and software issues**.
- Development of innovative solutions for **real-world problems in automation, IoT, and communication systems**.
- Integration of **analog and digital circuits** for improved system efficiency.

### 3. Industry Relevance and Employability

- Exposure to **emerging technologies** like IoT, robotics, wireless communication, and smart systems.
- Hands-on experience with **programming languages such as C, Python, and Embedded C** for electronics applications.
- Knowledge of **industry standards, safety regulations, and sustainable electronics design**.

#### 4. Project Management & Teamwork

- Effective planning, execution, and management of projects within **time and budget constraints**.
- Enhancement of teamwork and leadership skills through **collaborative project development**.
- Experience in preparing **technical documentation, project reports, and research papers**.

#### 5. Innovation and Entrepreneurial Opportunities

- Encouragement to develop **new product ideas, prototypes, and innovative solutions**.
- Opportunities for **startup incubation, patents, and commercializing project ideas**.
- Contribution to advancements in **electronics, automation, and communication technologies**.

#### 6. Communication and Presentation Skills

- Ability to **document and present technical findings** effectively.
- Development of **public speaking and technical writing skills** for conferences, project demonstrations, and job interviews.
- Exposure to **industry practices in project documentation and report preparation**.

Project should be performed in stages as given below. Each stage is addressing one CO.

Stage	CO	Description of Activity to be performed by student
<b>Stage-I Project Selection and Planning</b>	CO1	<ul style="list-style-type: none"> <li>• Identify project domain (Hardware, software, Embedded, IoT, Communication, networking etc.)</li> <li>• Select a project topic based on interest and feasibility</li> <li>• Define project objectives and scope</li> <li>• Conduct preliminary research and literature review</li> <li>• Form a project team and assign roles</li> <li>• Prepare a project proposal and seek approval from faculty</li> </ul>
<b>Stage-2 Requirement Analysis</b>	CO2	<ul style="list-style-type: none"> <li>• Gather user/client requirements (if applicable)</li> <li>• Study technical and functional requirements</li> <li>• Perform feasibility study (technical, financial, and operational)</li> </ul>

		<ul style="list-style-type: none"> <li>• Prepare Software Requirement Specification (SRS) or technical documentation</li> </ul>
<b>Stage– 3 Design and Development</b>	CO3	<ul style="list-style-type: none"> <li>• Create system architecture or technical design</li> <li>• Develop flowcharts, circuit diagrams, or CAD models (depending on project type)</li> <li>• Select appropriate tools, software, or materials</li> <li>• Start coding, prototyping, or fabricating the project</li> <li>• Test individual modules/components</li> </ul>
<b>Stage–4 Testing and Implementation</b>	CO 4	<ul style="list-style-type: none"> <li>• Perform unit testing of different modules</li> <li>• Integrate all components and perform system testing</li> <li>• Debug and fix errors</li> <li>• Conduct performance evaluation and quality testing</li> <li>• Deploy or demonstrate the working prototype</li> </ul>
<b>Stage –5 Presenting and demonstrating the project to enhance technical communication</b>	CO5	<ul style="list-style-type: none"> <li>• Clearly explain your work using technical terms that everyone understands.</li> <li>• Document your findings, test results, and changes systematically.</li> <li>• Confidently communicate project idea, progress and results to faculty and evaluators.</li> <li>• Prepare and assign speaking roles for team presentation.</li> <li>• Address disagreements professionally focusing on solutions.</li> </ul>
<b>Stage – 6 Documentation and Report Writing</b>	CO6	<ul style="list-style-type: none"> <li>• Prepare detailed project documentation</li> <li>• Include introduction, methodology, design details, results, and conclusion</li> <li>• Format the report as per the institution's guidelines</li> <li>• Cite references and include diagrams/screenshots</li> <li>• Create a PowerPoint presentation summarizing the project</li> <li>• Demonstrate the working model or prototype to faculty and other students</li> <li>• Answer questions from them</li> <li>• Collect feedback and make necessary improvements.</li> </ul>

## I :-Assessment Criteria

### i) Self-Learning Assessment of Project: -

Assessment of each CO is done for 50 marks as per Rubrics given below. Average marks of all COs will be marks allotted to SLA out of 50.

#### 1. Project Proposal & Planning CO 1: (50 Marks)

Criteria	Excellent (10 or 9)	Good (8 or 7)	Satisfactory (6 or 5)	Needs Improvement (4 or 3)	Poor (2 or 1)
<b>Project Idea &amp; Relevance</b>	The idea is innovative, feasible, and highly relevant to the field.	The idea is relevant and feasible but lacks innovation.	The idea is acceptable but needs improvement in feasibility or relevance.	The idea is weak or not well-defined.	The idea is unclear or lacks purpose.
<b>Problem Definition &amp; Objectives</b>	Clearly defined problem statement with well-structured objectives.	Clear problem statement with minor refinements needed.	Problem statement is somewhat clear but objectives need more clarity.	Problem statement is vague and lacks clear objectives.	Problem statement is unclear and objectives are missing.
<b>Feasibility Study</b>	Strong technical, financial, and operational feasibility analysis.	Feasibility analysis is done but needs minor refinements.	Feasibility study is present but lacks depth.	Feasibility study is incomplete or weak.	No feasibility study provided.
<b>Project diary and documentation</b>	Entries are consistently recorded, well-organized, and timely.	Most entries are timely and well-organized.	Some entries are missing or lack consistency.	Few entries recorded on time.	No entries recorded on time.
<b>Punctuality and overall contribution</b>	Always takes responsibility for assigned tasks and ensures completion.	Responsible but missing timely completion of task	Usually responsible but may need reminders.	Sometimes neglects tasks or requires frequent follow-ups.	Often fails to complete tasks or ignores responsibilities.

#### 2. Requirement Analysis & Literature Review (50 Marks)



<b>Criteria</b>	<b>Excellent (10 or 9)</b>	<b>Good (8 or 7)</b>	<b>Satisfactory (6 or 5)</b>	<b>Needs Improvement (4 or 3)</b>	<b>Poor (2 or 1)</b>
<b>Understanding of Requirements</b>	Requirements are well-identified, structured, and well-documented.	Most requirements are identified but need minor refinements.	Requirements are identified but lack depth and clarity.	Some requirements are missing or poorly defined.	Requirements are unclear or not defined.
<b>Literature Review</b>	Extensive review with relevant references and a	Adequate research with good references.	Basic research with minimal references.	Limited research with weak references.	No research or references provided.
<b>Finalizing requirements</b>	90% finalized	75% finalized	60% finalized	40% finalized	20% finalized
<b>Project diary and documentation</b>	Entries are consistently recorded, well-organized, and timely.	Most entries are timely and well-organized.	Some entries are missing or lack consistency.	Few entries recorded on time.	No entries recorded on time.
<b>Punctuality and overall contribution</b>	Always takes responsibility for assigned tasks and ensures completion.	Responsible but missing timely completion of task	Usually responsible but may need reminders.	Sometimes neglects tasks or requires frequent follow-ups.	Often fails to complete tasks or ignores responsibilities.

### **3. Design & Methodology (50 Marks)**

<b>Criteria</b>	<b>Excellent (10 or 9)</b>	<b>Good (8 or 7)</b>	<b>Satisfactory (6 or 5)</b>	<b>Needs Improvement (4 or 3)</b>	<b>Poor (2 or 1)</b>
<b>System Design / Technical Design</b>	Well-structured design with detailed diagrams/models	Design is mostly complete but needs minor improvement	Basic design provided with missing details.	Design is incomplete or lacks clarity.	No proper design provided.

		ts.			
<b>Tools &amp; Technology Used</b>	Appropriate selection and justified use of tools and technologies.	Mostly appropriate tools with minor improvements needed.	Tools are relevant but not well justified.	Tools are inappropriate or not well applied.	No proper tools/technologies used.
<b>Implementation Plan &amp; Timeline</b>	Well-defined timeline with tasks properly scheduled.	Timeline is defined but minor adjustments needed.	Timeline is present but lacks clear milestones.	Timeline is vague or not well-structured.	No proper timeline provided.
<b>Project diary and documentation</b>	Entries are consistently recorded, well-organized, and timely.	Most entries are timely and well-organized.	Some entries are missing or lack consistency.	Few entries recorded on time.	No entries recorded on time.
<b>Punctuality and overall contribution</b>	Always takes responsibility for assigned tasks and ensures completion.	Responsible but missing timely completion of task	Usually responsible but may need reminders.	Sometimes neglects tasks or requires frequent follow-ups.	Often fails to complete tasks or ignores responsibilities.

#### 4. Testing and Implementation (50 Marks)

<b>Criteria</b>	<b>Excellent (10 or 9)</b>	<b>Good (8 or 7)</b>	<b>Satisfactory (6 or 5)</b>	<b>Needs Improvement (4 or 3)</b>	<b>Poor (2 or 1)</b>
<b>Development &amp; Execution</b>	Project is progressing as per plan with major milestones achieved.	Good progress but minor delays in execution.	Project is moving forward but with some major delays.	Limited progress with significant delays.	No progress made.
<b>Technical Skills &amp; Implementation</b>	Strong technical skills applied effectively in the project.	Good technical skills but minor improvements needed.	Basic technical skills applied but needs improvement.	Weak technical implementation with errors.	Poor or no technical implementation.
<b>Testing</b>	Well-defined test	Testing	Basic testing	Minimal testing	No testing

<b>Procedures</b>	cases and rigorous testing performed.	performed with minor missing elements.	done but lacks thoroughness.	with errors remaining.	conducted.
<b>Project diary and documentation</b>	Entries are consistently recorded, well-organized, and timely.	Most entries are timely and well-organized.	Some entries are missing or lack consistency.	Few entries recorded on time.	No entries recorded on time.
<b>Punctuality and overall contribution</b>	Always takes responsibility for assigned tasks and ensures completion.	Responsible but missing timely completion of task	Usually responsible but may need reminders.	Sometimes neglects tasks or requires frequent follow-ups.	Often fails to complete tasks or ignores responsibilities.

#### 5. Presenting and Demonstrating (50 Marks)

<b>Criteria</b>	<b>Excellent (10 or 9)</b>	<b>Good (8 or 7)</b>	<b>Satisfactory (6 or 5)</b>	<b>Needs Improvement (4 or 3)</b>	<b>Poor or 1) (2</b>
<b>Bug Fixing &amp; Improvements</b>	Identified and resolved all critical issues.	Fixed most issues but some minor ones remain.	Fixed some major issues but minor bugs still exist.	Attempted to fix issues but project still has major flaws.	No bug fixing or improvements done.
<b>Teamwork &amp; Collaboration</b>	Excellent coordination and teamwork among members.	Good teamwork with minor communication issues.	Teamwork is fair but lacks consistency.	Weak teamwork with noticeable conflicts.	No teamwork or collaboration evident.
<b>Project Demonstration</b>	Working model demonstrated effectively.	Working model with minor glitches.	Partially working model demonstrated.	Attempted but model is non-functional.	No demonstration.
<b>Project diary and documentation</b>	Entries are consistently recorded, well-	Most entries are timely and well-organized.	Some entries are missing or lack	Few entries recorded on time.	No entries recorded on time.

	organized, and timely.		consistency.		
<b>Punctuality and overall contribution</b>	Always takes responsibility for assigned tasks and ensures completion.	Responsible but missing timely completion of task	Usually responsible but may need reminders.	Sometimes neglects tasks or requires frequent follow-ups.	Often fails to complete tasks or ignores responsibilities.

#### 6. Documentation & Report Writing (50 Marks)

<b>Criteria</b>	<b>Excellent (10 or 9)</b>	<b>Good (8 or 7)</b>	<b>Satisfactory (6 or 5)</b>	<b>Needs Improvement (4 or 3)</b>	<b>Poor or 1) (2)</b>
<b>Clarity &amp; Structure</b>	Well-organized report with clear structure.	Mostly clear with minor formatting issues.	Basic structure but lacks clarity in some sections.	Poorly structured with many missing elements.	No proper report or very disorganized.
<b>Technical Details &amp; References</b>	Detailed technical content with proper citations.	Good technical content but minor gaps.	Basic technical details with minimal references.	Poor technical content with weak references.	No technical details or citations provided.
<b>Presentation Quality</b>	Well-structured, engaging, and professional.	Good presentation with minor refinements needed.	Basic presentation but lacks engagement.	Poor presentation with many issues.	No proper presentation.
<b>Project diary and documentation</b>	Entries are consistently recorded, well-organized, and timely.	Most entries are timely and well-organized.	Some entries are missing or lack consistency.	Few entries recorded on time.	No entries recorded on time.
<b>Punctuality and overall contribution</b>	Always takes responsibility for assigned tasks and	Responsible but missing timely completion	Usually responsible but may need	Sometimes neglects tasks or requires frequent follow-ups.	Often fails to complete tasks or ignores responsibilities.

	ensures completion.	of task	reminders.		
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**ii) Summative Assessment of Project:**

External examiner will do the assessment as per rubrics given below for 50 Marks.

<b>Criteria</b>	<b>Excellent (10 or 9)</b>	<b>Good (8 or 7)</b>	<b>Satisfactory (6 or 5)</b>	<b>Needs Improvement (4 or 3)</b>	<b>Poor (2 or 1)</b>
Technical Knowledge & Application	Deep understanding, innovative solutions applied effectively	Good application of concepts with minor errors	Basic understanding but lacks innovation	Limited knowledge, frequent mistakes	Poor understanding, major errors in application
Design & Methodology	Logical, well-structured, and technically sound design	Mostly well-designed but has minor flaws	Basic structure but lacks depth or efficiency	Weak design, lacks structure or technical depth	Poorly structured, ineffective methodology
Implementation & Execution	Fully functional project with excellent performance	Mostly functional with minor issues	Partially functional but significant limitations	Limited functionality, major issues	Project does not function properly
Presentation & Documentation	Well-structured, detailed, error-free report with strong visuals	Well-documented, minor errors in formatting or content	Basic report with some errors, weak visuals	Poorly structured report with missing details	No report or poorly written and formatted document
Conclusion Contribution	Strong conclusion with insightful recommendations and real-world impact	Good conclusion, minor lack of depth	Basic conclusion with minimal recommendations	Weak or missing conclusion, little relevance	No clear conclusion or impact

Maximum Marks of Summative assessment = 50



# **SEMESTER VI COURSES**

